

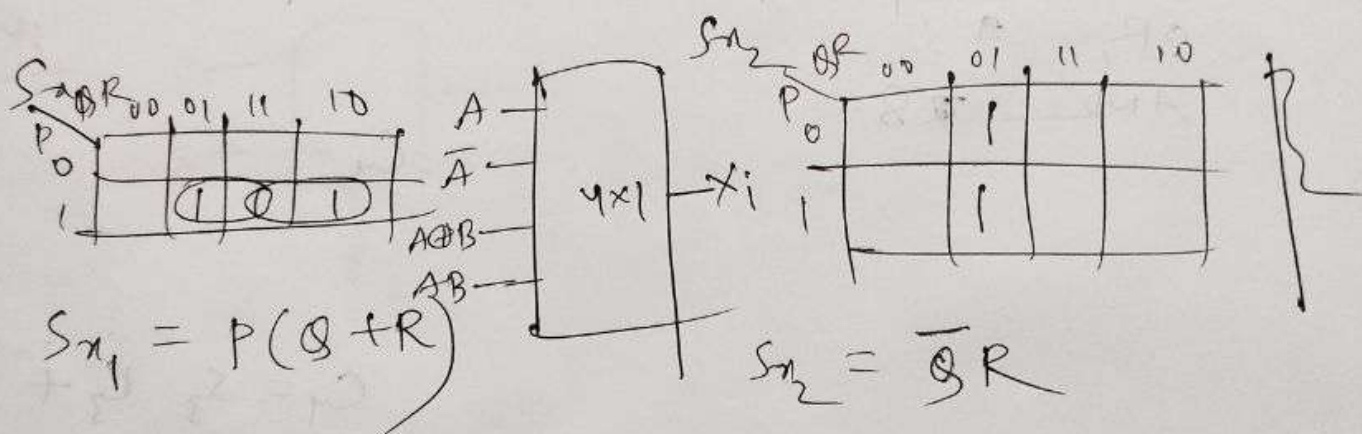
P	Q	R	X_i	Y_i	Z_i	C_n	F
0	0	0	A	B	0 1	0	$A+B$
0	0	1	\bar{A}	0	0 1	1	$\bar{A}+1$
0	1	0	A	B	0 1	0	$A+B$
0	1	1	A	B	1	1	$A+B+1$
1	0	0	A	0	1	1	$A+1$
1	0	1	AB	0	1	0	AB
1	1	0	AB	0	1	0	$A \oplus B$
1	1	1	$A \oplus B$	0	1	0	$A \oplus B$

X_i

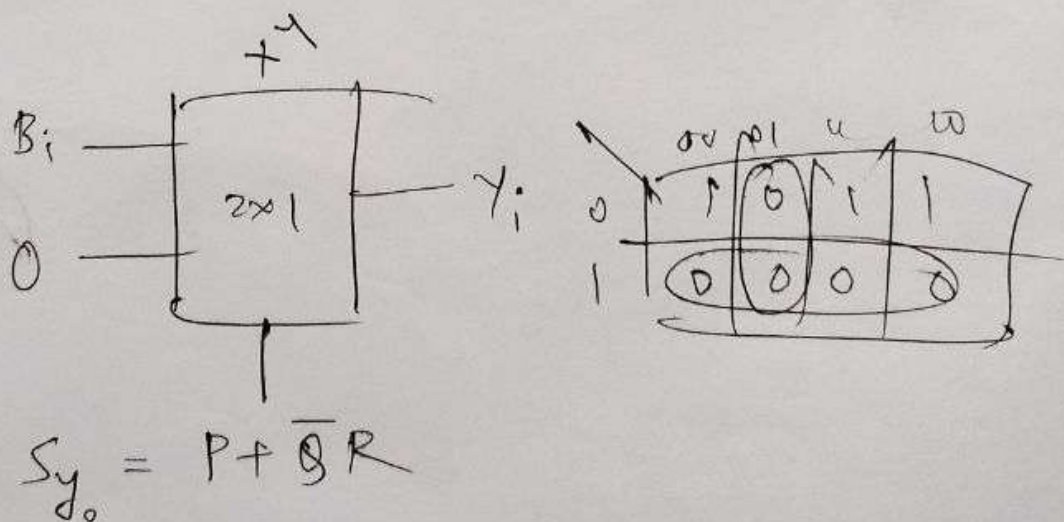
P	Q	R	X_i	S_{x_1}	S_{x_0}
0	0	0	A	0	0
0	0	1	\bar{A}	0	1
0	1	0	A	0	0
0	1	1	A	0	0
1	0	0	A	0	0
1	0	1	AB	1	1
1	1	0	$A \oplus B$	1	0
1	1	1	$A \oplus B$	1	0

Custom codes for distinct outputs

(for MUX)



Y_i

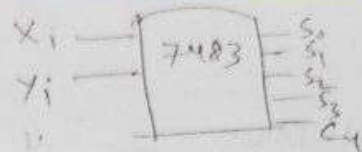


Cin

P \ QR	00	01	11	10
0	0	1	1	0
1	1	0	0	0

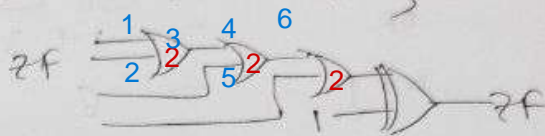
$$C_{in} = \overline{P}R + P\overline{Q}R$$

$$= \overline{P}R + P(\overline{Q} + R)$$



OF Flg

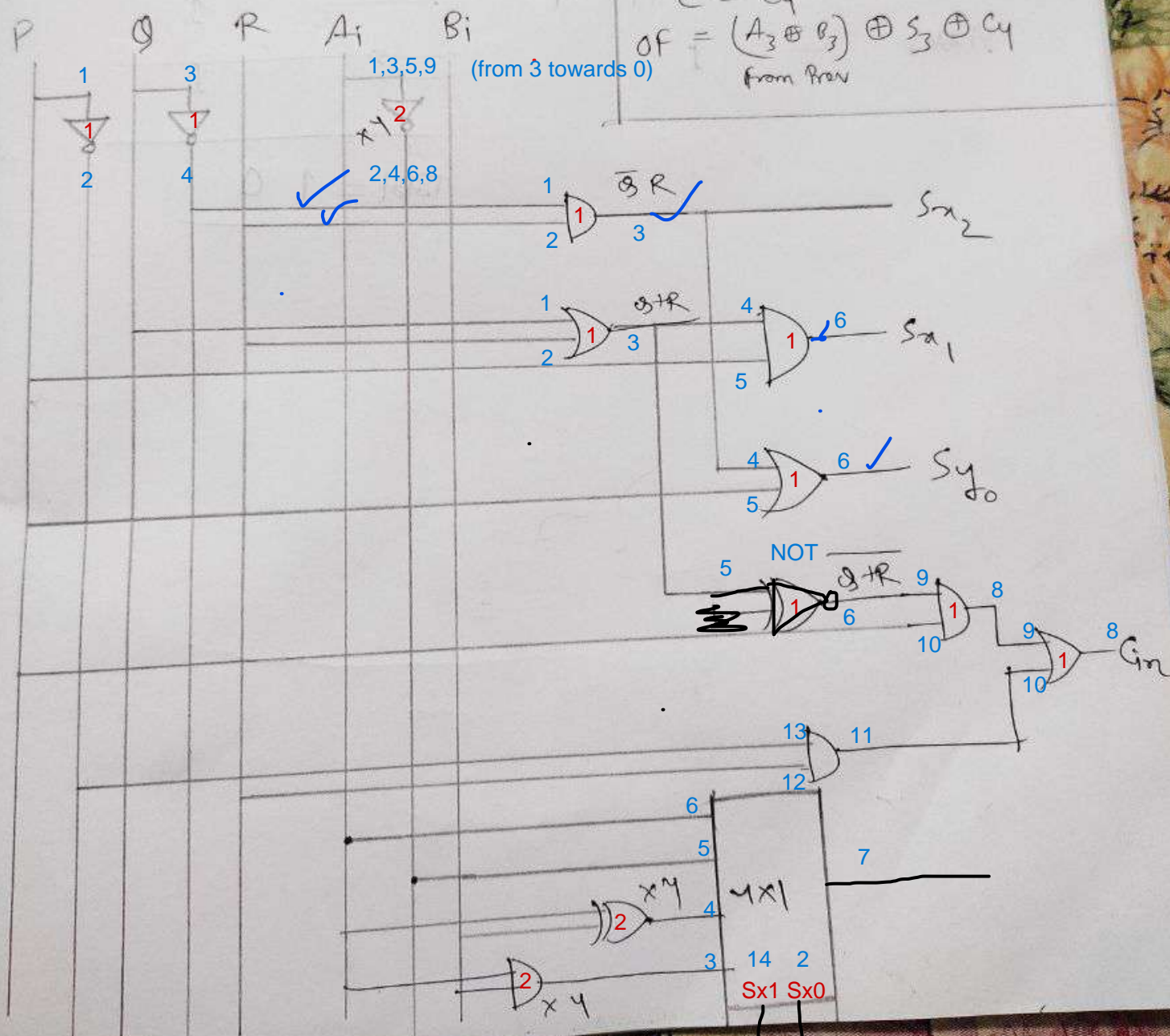
$$SF = S_3$$



$$C = C_4$$

$$OF = (A_3 \oplus B_3) \oplus S_3 \oplus C_4$$

From Prev



		IC	
4X1 MUX	4	2	
2X1 MUX	4	1	
AND	$4 + 4 = 8$ <small>[xi] [other]</small>	2	
OR	$3 + 3 = 6$	1	
NOT	6	1	
XOR	$1 + 2 + 1 = 4$	1	
Adder	1	1	

Total = 10

Flags Calculation:

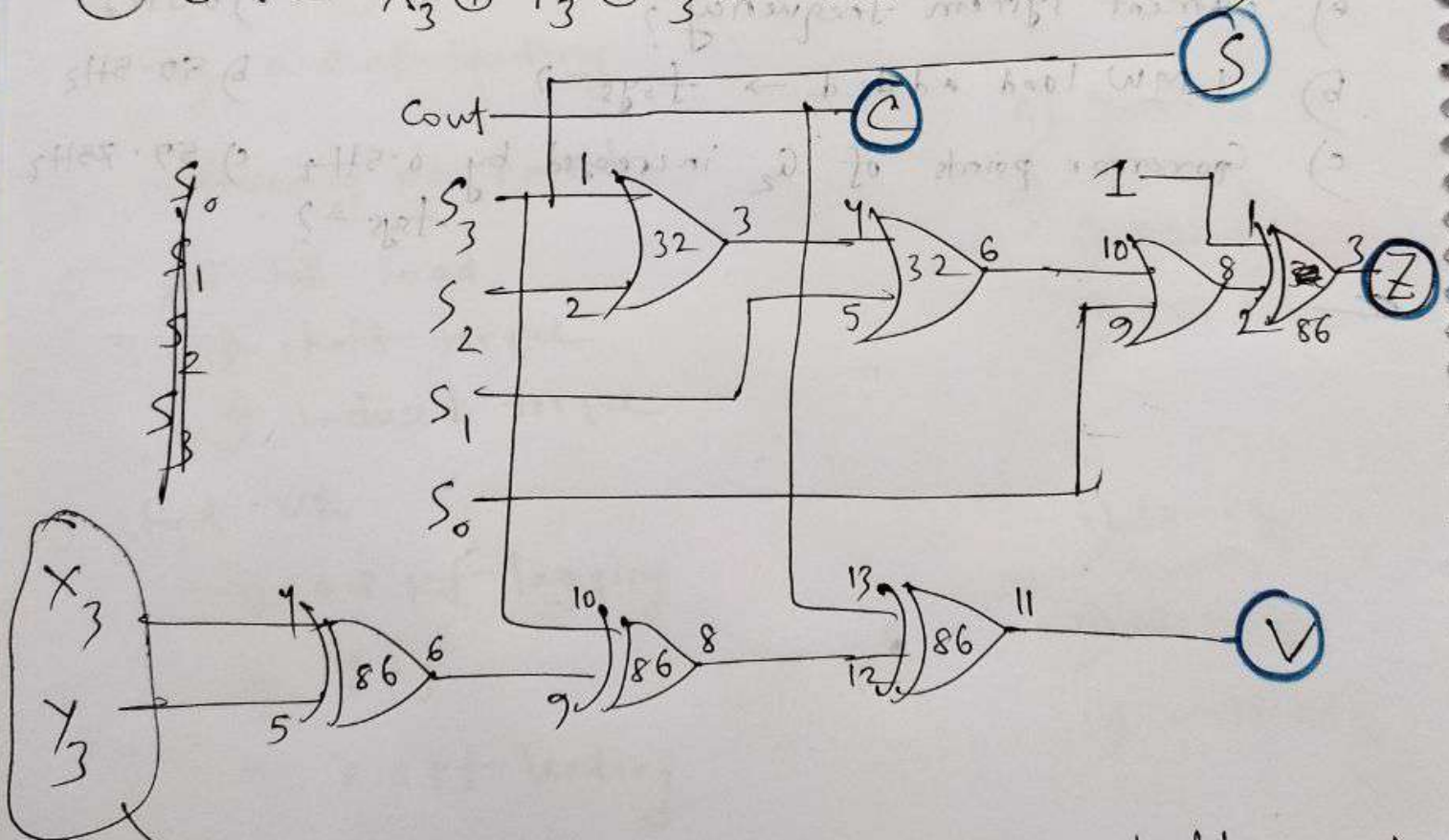
C, Z, S, \checkmark

① $S = S_3$

② $C_{in} = C_{out}$

③ $Z = S_0 + S_1 + S_2 + S_3$ (Implement this NOT with XOR)

④ $V = X_3 \oplus Y_3 \oplus S_3 \oplus C_{out}$ with 1



→ these are present in the left part at output of 'input_preprocessor'