Last date of submission for written assignment: March 10, 2020
Instruction: 2M for punctual submission + 3M for neat diagrams and clear explanation. Zero marks for delayed submission.

Q1. Draw circuit of CB configuration of a BJT. Draw the input/output characteristic curves and explain in detail.

Q2. Draw circuit of CE configuration of a BJT. Draw the input/output characteristic curves and explain in detail.

Q3. Explain in detail the following BJT biasing circuits:

- Fixed bias
- Collector to base bias(DC bias with voltage feedback)
- Potential divider bias

Q4. How does a biased BJT circuit act as a voltage amplifier?

Q1., Q2., Refer Chapter 3 Boylestad, Chapter 8 V.K. Mehta
Q3., Q4., Refer Chapter 4 Boylestad, Chapter 9 V.K. Mehta