

**SVKM'S NMIMS, School of Technology Management & Engineering | Navi-
Mumbai**

MBA-Tech (A.Y. 2020-21)

Assignment

(Cache Memory / Semiconductor Memories)

1. Discuss memory hierarchy in detail.
2. What are the features of cache memory design?
3. What is cache memory? Why is placed between CPU and Main memory?
Explain the three levels of cache memory operation.
4. Explain all the cache memory mapping techniques with suitable example.
5. Explain replacement algorithm used in associative & set.
6. Explain the operation of SRAM & DRAM with diagram of single bit storage cell.