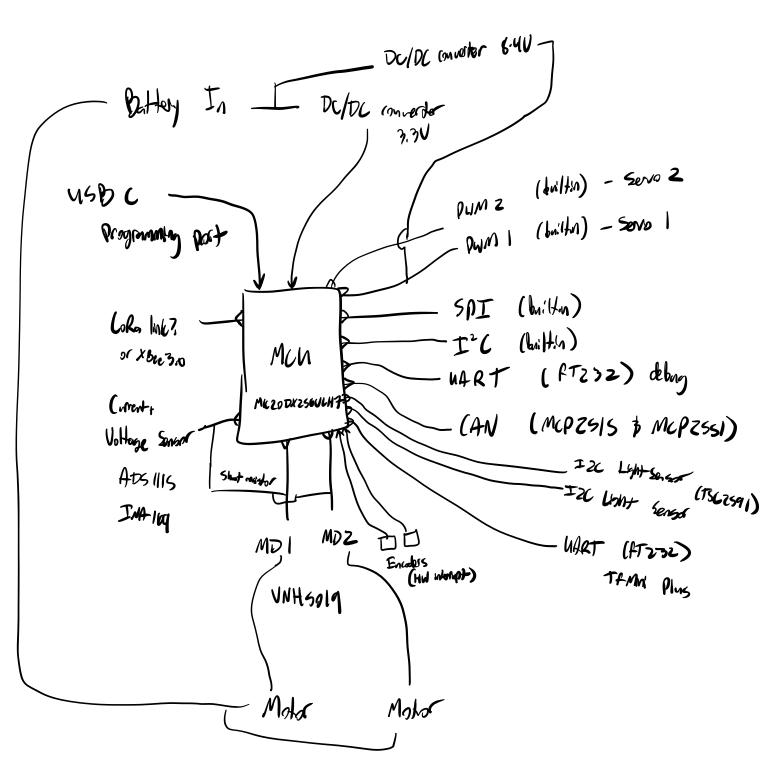
# Zing Which V3 Architecture

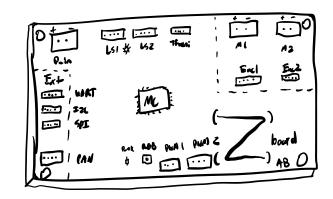
All External I/O - Locking JST-PH



# EE Tradeoffs!

- Part comt & cost
- future expandibility
- -42e
- Heat

# Sample Layart



SW:

Simple liver non-detendable scheduler

Nothing is safety-critical so this should be good enough

ISA - based encoder combing - hardwore interripts

# IZC Stack:

- Light sonsois (TSLZSai)
- Vollage/Conent Sensor (Abolls)
- External Flo

#### Qum ',

- Major controllers (DRU6871)

## SOI Stack i

- Laka vadio
- External I/O

## NAAT stack!

- Liter sussor
- Drogrammy port
- Extendral Flo

### CAN Stack:

- Extend Ilo

Addons!

- LiDo changing circuity

- GPS

- Datalogging

- RTC