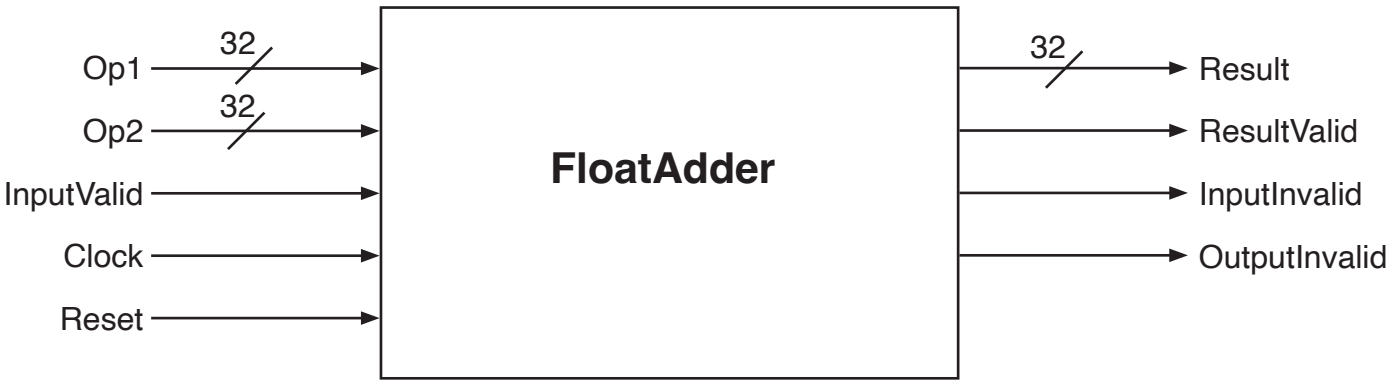
# 32-Bit IEEE Foating Point Adder Interface Description




**Inputs**:

- Op1
  - Type: IEEE Single-Precision Floating Point
  - Description: First input operand
- Op2
  - Type: IEEE Single-Precision Floating Point
  - Description: Second input operand
- InputValid
  - Type: Active-high synchronous control signal
  - Description: Control signal to the floating point adder to signify that the inputs contain valid data. Once InputValid is asserted, the adder de-asserts ResultValid and begins a computation with the provided input operands.

**Outputs:**

- Result
  - Type: IEEE Single-Precision Floating Point
  - Description: Addition result
- ResultValid
  - Active-high status signal
  - Description: Status signal which indicates that the value currently on the Result output contains the valid result of the previous addition which was performed.
- InputInvalid
  - Active-high status signal
  - Description: Status signal which indicates that the supplied inputs are either NaN, DeNorm, or Inf.
- OutputInvalid
  - Active-high status signal
  - Description: Status signal which indicates that the value currently on the Result output is NaN, DeNorm, or Inf.