

	3,5 LCD		Rasp Pi	
	3,3V	1	3,3V	
	5V	2	5V	
	NC	3	GPIO2 Serial Data (I2C)	
	5V	4	5V	
	NC	5	GPIO3 Serial Clock (I2C)	
	GND	6	GND	
	NC	7	GPIO 4	
	NC	8	GPIO 14 (UART TX)	
	GND	9	GND	
	NC	10	GPIO 15 (UART RX)	
Touch Interrupt, detektiert Berührung	TP_IRQ	11	GPIO 17 Chip Enable-CE1 (SPI1)	
	NC	12	GPIO 18 Chip Enable_CE0 (SP1) [PWM]	
	NC	13	GPIO 27	
	GND	14	GND	
	NC	15	GPIO 22	
	NC	16	GPIO 23	
	3,3V	17	3,3V	
Instruction/Data Register Selection	LCD_RS	18	GPIO 24	? Interaktionspin
SPI data input of LCD/Touch Panel	LCD_SI/TP_SI	19	GPIO 10 MOSI (SPI 0)	
	GND	20	GND	
SPI data output of Touch Panel	TP_SO	21	GPIO 09 MISO (SPI 0)	
Reset	RST	22	GPIO 25	? Interaktionspin
SPI Clock of LCD/Touch Panel	LCD_SCK/TP_SCK	23	GPIO 11 SCLK (SPI 0)	Clock
LCD chip selection, low active	LCD_CS	24	GPIO 8 Chip Enable-CE0 (SPI0)	? Interaktionspin
	GND	25	GND	
Touch Panel chip selection, low active	TP_CS	26	GPIO 7 Chip Enable-CE1 (SPI0)	? Interaktionspin