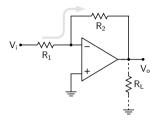
Op-Amp Circuits: Part 5



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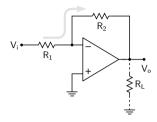
Department of Electrical Engineering Indian Institute of Technology Bombay



$$V_o = A_V(V_+ - V_-)$$
 (1)

Since the Op Amp has a high input resistance, $i_{R1}=i_{R2}\text{, and we get,} \label{eq:irresistance}$

$$V_{-} = V_{i} \, \frac{R_{2}}{R_{1} + R_{2}} + V_{o} \, \frac{R_{1}}{R_{1} + R_{2}} \eqno(2)$$

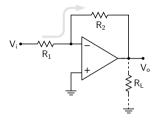


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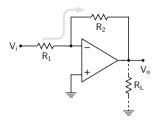


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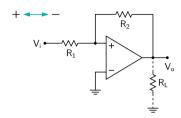


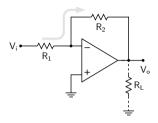
$$V_{o} = A_{V}(V_{+} - V_{-}) \hspace{0.5cm} \text{(1)} \\$$

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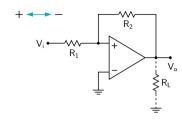


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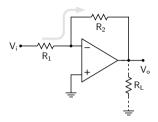
Since the Op Amp has a high input resistance, $i_{\text{R1}}=i_{\text{R2}}\text{, and we get,}$

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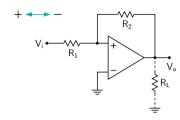


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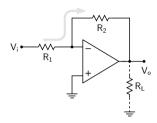
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$$\begin{array}{ccccc} \mathsf{V_i} \uparrow & \rightarrow & \mathsf{V_+} \uparrow & \rightarrow & \mathsf{V_o} \uparrow & \rightarrow & \mathsf{V_+} \uparrow \\ & \mathsf{Eq.} \ 3 & \mathsf{Eq.} \ 1 & \mathsf{Eq.} \ 3 \end{array}$$



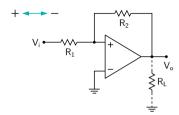
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The circuit reaches a stable equilibrium.

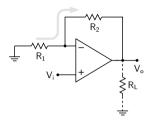


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$$V_i \uparrow \rightarrow \boxed{V_+ \uparrow} \rightarrow V_o \uparrow \rightarrow \boxed{V_+ \uparrow}$$

Eq. 3 Eq. 1 Eq. 3

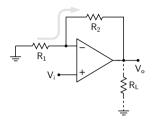
We now have a positive feedback situation. As a result, V_o rises (or falls) indefinitely, limited finally by saturation.



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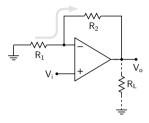


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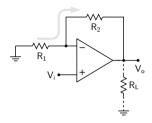


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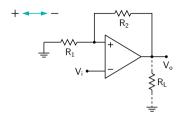


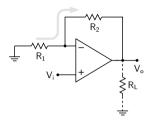
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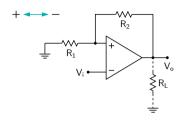


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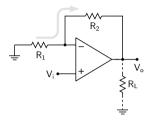
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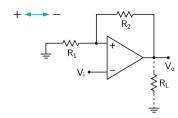


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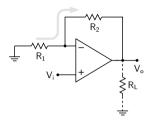
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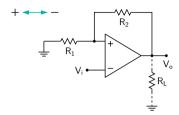
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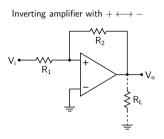
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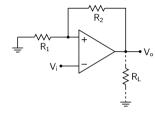


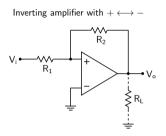
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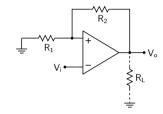
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We now have a positive feedback situation. As a result, $V_{\rm o}$ rises (or falls) indefinitely, limited finally by saturation.

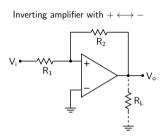


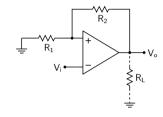




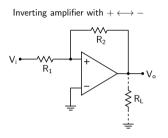


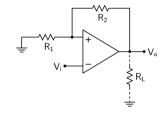
* Because of positive feedback, both of these circuits are unstable.



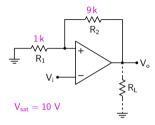


- * Because of positive feedback, both of these circuits are unstable.
- * The output at any time is only limited by saturation of the op-amp, i.e., $V_o=\pm V_{\rm sat}$.

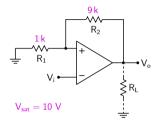




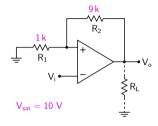
- * Because of positive feedback, both of these circuits are unstable.
- * The output at any time is only limited by saturation of the op-amp, i.e., $V_o=\pm V_{\rm sat}$.
- * Of what use is a circuit that is stuck at $V_{\rm o}=\pm V_{\rm sat}$? It turns out that these circuits are actually useful! Let us see how.



Because of positive feedback, V_o can only be $+V_{\rm sat}$ (if $V_+>V_-$) or $-V_{\rm sat}$ (if $V_+< V_-$).



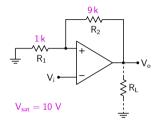
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Case (i):
$$V_o = +V_{\text{sat}} = +10 \,\text{V} \rightarrow V_+ = \frac{R_1}{R_1 + R_2} V_o = \frac{1 \,\text{k}}{1 \,\text{k} + 9 \,\text{k}} \, 10 \,\text{V} = 1 \,\text{V} \,.$$

$$(V_+ - V_-) = (1 - 5) = -4 \,\text{V} \rightarrow V_o = -V_{\text{sat}} \,.$$

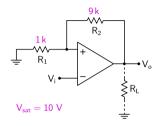


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This is inconsistent with our assumption ($V_o = +V_{\mathsf{sat}}$).



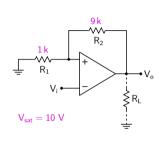
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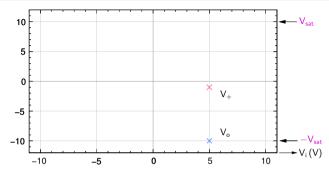
$$\begin{split} \text{Case (i): } & V_o = +V_{\text{sat}} = +10\,\text{V} \to V_+ = \frac{R_1}{R_1 + R_2}\,V_o = \frac{1\,\text{k}}{1\,\text{k} + 9\,\text{k}}\,10\,\text{V} = 1\,\text{V}\,. \\ & (V_+ - V_-) = (1 - 5) = -4\,\text{V} \to V_o = -V_{\text{sat}}\,. \end{split}$$

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Case (ii):
$$V_o = -V_{\rm sat} = -10\,{\rm V} \rightarrow V_+ = \frac{R_1}{R_1 + R_2} V_o = \frac{1\,{\rm k}}{1\,{\rm k} + 9\,{\rm k}} \times (-10\,{\rm V}) = -1\,{\rm V} \,.$$

$$(V_+ - V_-) = (-1 - 5) = -6\,{\rm V} \rightarrow V_o = -V_{\rm sat} \, ({\rm consistent})$$





Because of positive feedback, V_o can only be $+V_{\rm sat}$ (if $V_+>V_-$) or $-V_{\rm sat}$ (if $V_+< V_-$). Consider $V_i=5~V_-$.

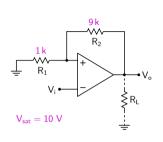
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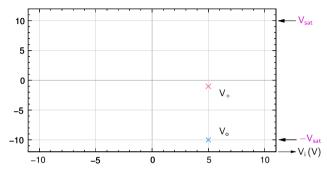
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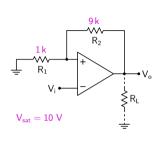
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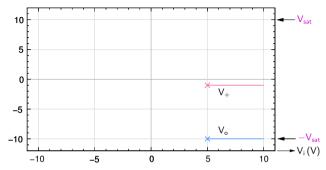
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 $(V_+ - V_-) = (-1 - 5) = -6\,{\rm V} \to V_o = -V_{\rm sat} \, ({\rm consistent})$

If we move to the right (increasing V_i), the same situation applies, i.e., $V_o = -V_{\text{sat}}$.





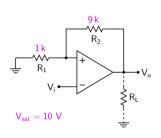
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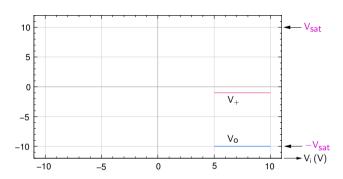
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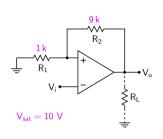
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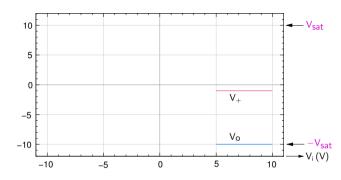
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$$V_o = -V_{\rm sat} = -10\,{\rm V} \to V_+ = \frac{R_1}{R_1 + R_2} V_o = \frac{1\,{\rm k}}{1\,{\rm k} + 9\,{\rm k}} \times (-10\,{\rm V}) = -1\,{\rm V} \,.$$
 $(V_+ - V_-) = (-1 - 5) = -6\,{\rm V} \to V_o = -V_{\rm sat} \, ({\rm consistent})$

If we move to the right (increasing V_i), the same situation applies, i.e., $V_o = -V_{\text{sat}}$.

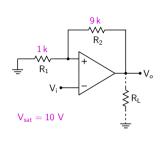


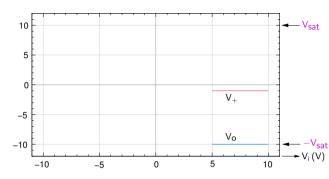






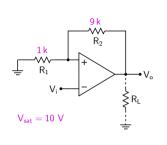
Consider decreasing values of V_i .

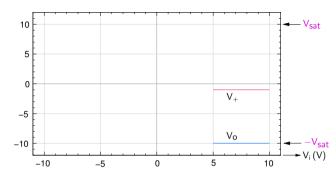




Consider decreasing values of V_i .

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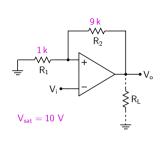


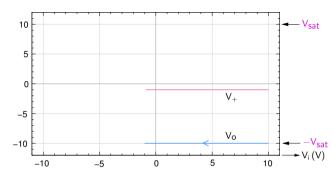


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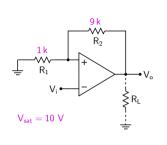


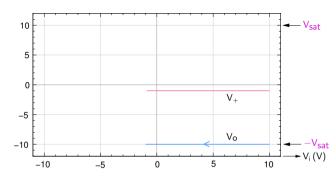


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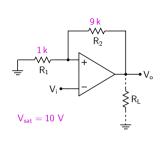


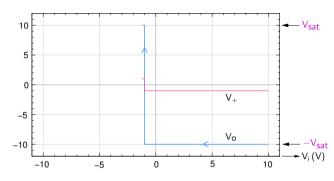
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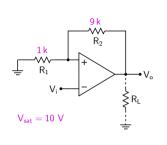


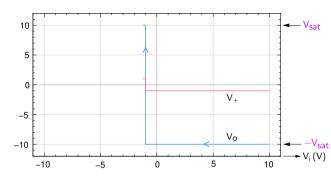
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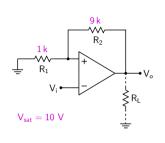
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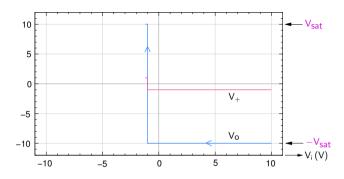
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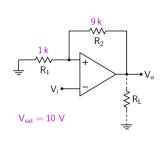
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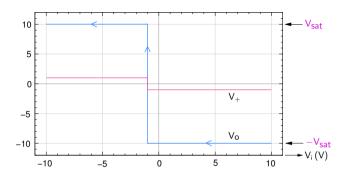
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Inverting Schmitt trigger





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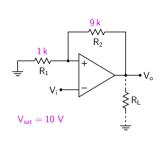
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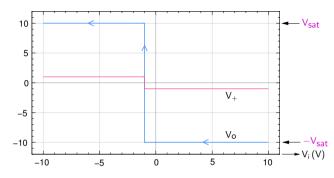
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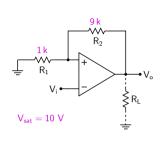
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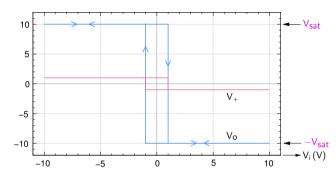
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Decreasing V_i further makes no difference to V_o (since $V_i = V_- < V_+ = +1 V$ holds).

Now, the threshold at which V_o flips is $V_i = +1 V$.

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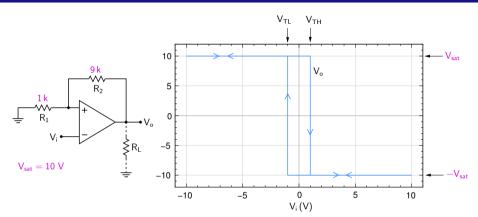
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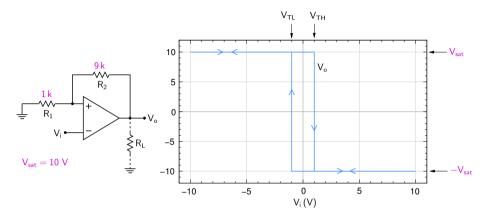
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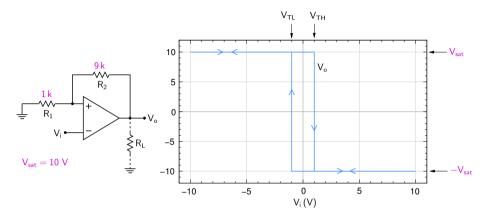
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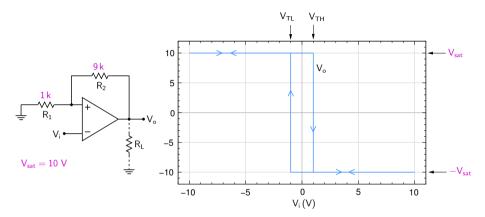




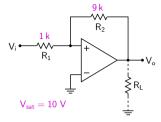
* The threshold values (or "tripping points"), V_{TH} and V_{TL} , are given by $\pm \left(\frac{R_1}{R_1 + R_2}\right) V_{\text{sat}}$.



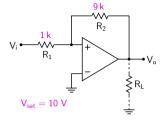
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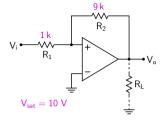
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- * $\Delta V_T = V_{TH} V_{TL}$ is called the "hysteresis width."



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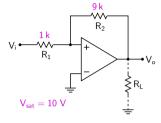


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Case (i):
$$V_o = -V_{sat} = -10 V$$

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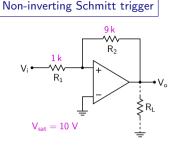
$$(V_+ - V_-) = (3.5 - 0) = 3.5 V \rightarrow V_o = +V_{sat}$$
.

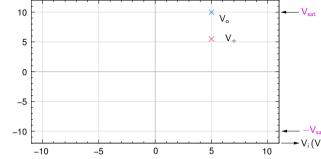


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Consider $V_i = 5 V$.

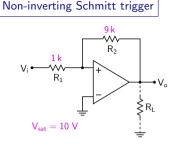
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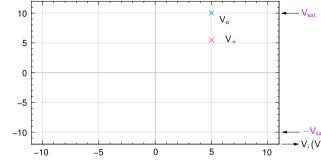
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 $(V_+-V_-)=(3.5-0)=3.5~V
ightarrow V_o=+V_{\rm sat}$. This is inconsistent with our assumption $(V_o=-V_{\rm sat})$.

Case (ii):
$$V_o = +V_{\text{sat}} = +10 \text{ V} \rightarrow V_+ = \frac{9 \text{ k}}{10 \text{ k}} \times 5 + \frac{1 \text{ k}}{10 \text{ k}} \times 10 = 4.5 + 1 = 5.5 \text{ V}.$$

$$(V_+ - V_-) = (5.5 - 0) = 5.5 \text{ V} \rightarrow V_o = +V_{\text{sat}} \text{ (consistent)}$$





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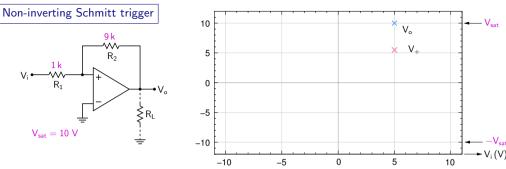
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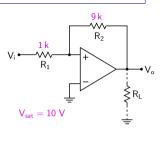
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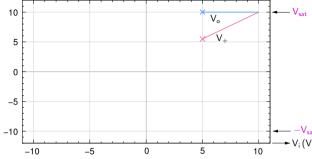
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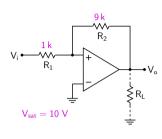
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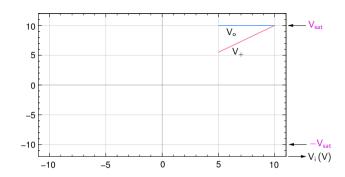
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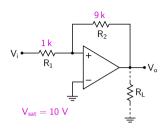
$${\sf Case \; (ii): \; \it V_o = +V_{\sf sat} = +10 \; \it V \, \rightarrow \, \it V_+ = \frac{9 \; k}{10 \; k} \times 5 + \frac{1 \; k}{10 \; k} \times 10 = 4.5 + 1 = 5.5 \; \it V \, .}$$

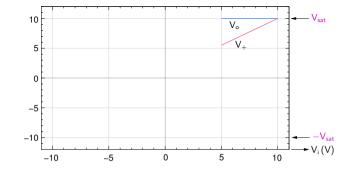
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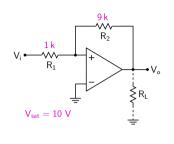


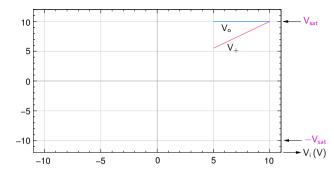






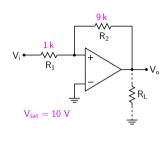
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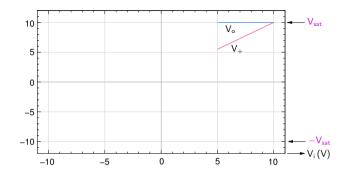




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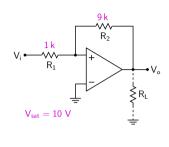
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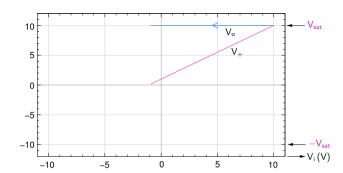




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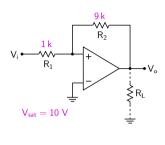
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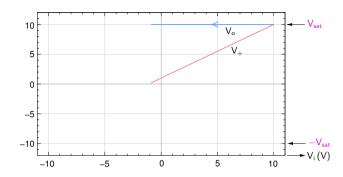




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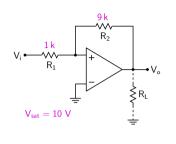


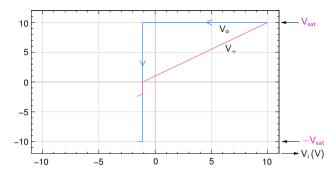


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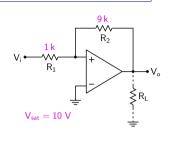


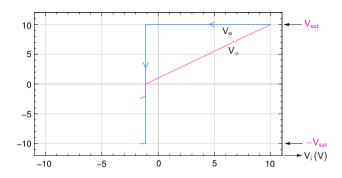


Consider decreasing values of V_i .

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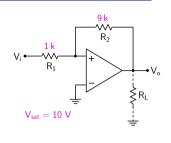


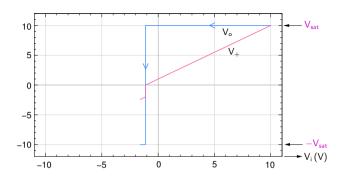
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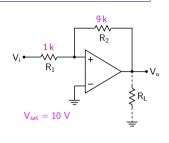
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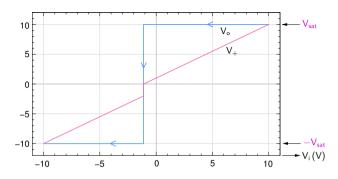
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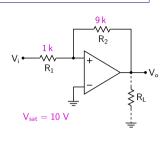
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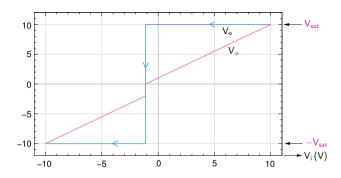
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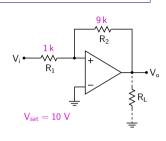
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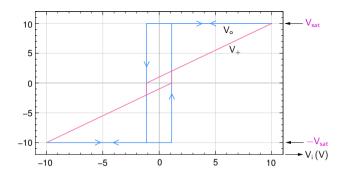
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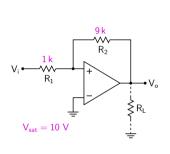
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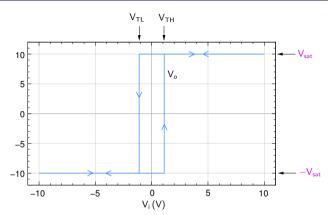
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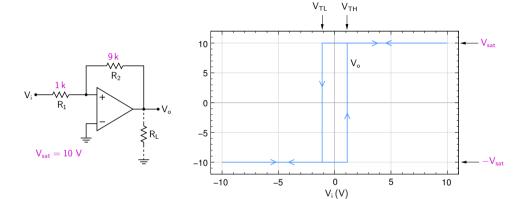
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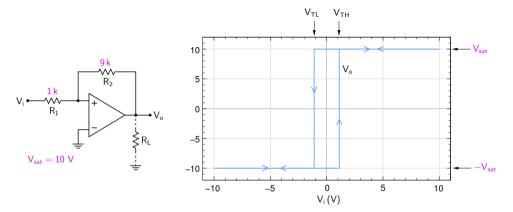
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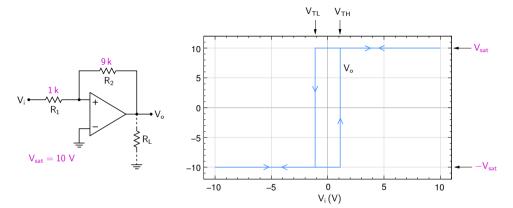




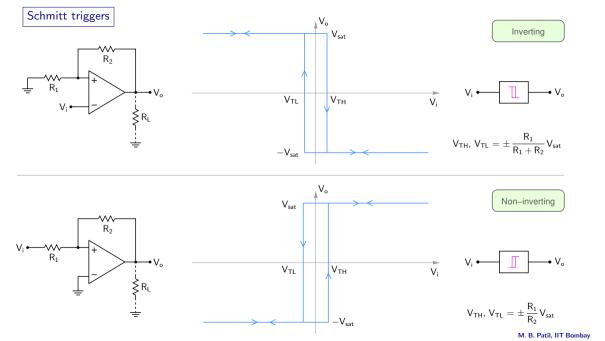
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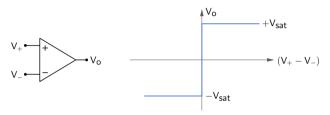


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- * $\Delta V_T = V_{TH} V_{TL}$ is called the "hysteresis width."



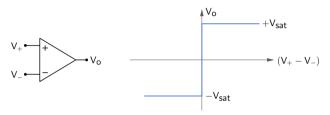


Comparators



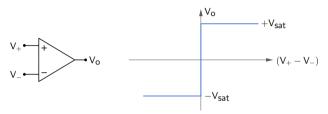
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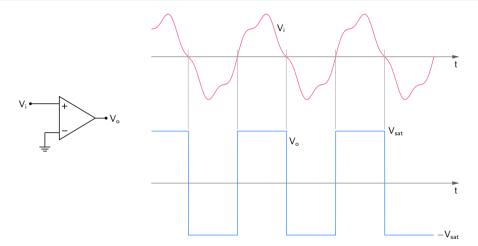
As seen earlier, the width of the linear region, $[V_{\rm sat}-(-V_{\rm sat})]/A_V$, is small $(\sim 0.1\,{\rm m\,V})$, and could be treated as 0.

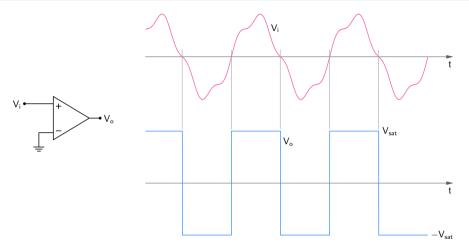


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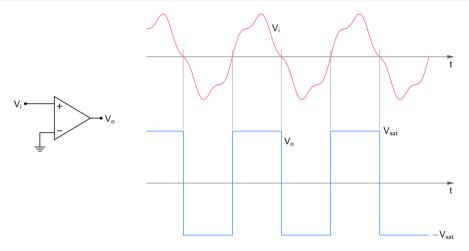
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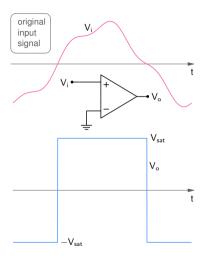


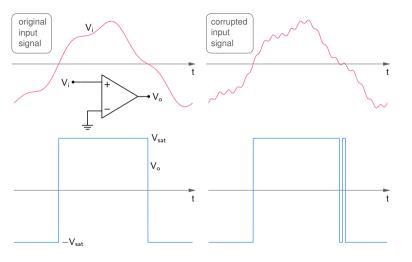
A comparator can be used to convert an analog signal into a digital (high/low) signal for further processing with digital circuits.

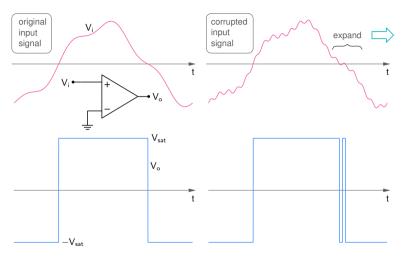


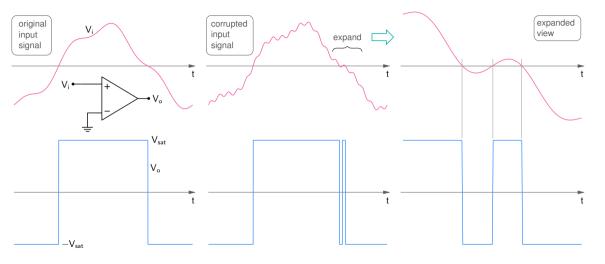
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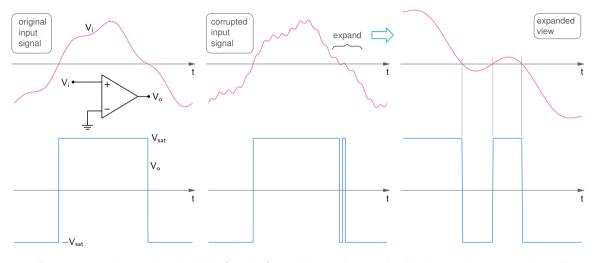
In practice, the input (analog) signal can have noise or electromagnetic pick-up superimposed on it. As a result, erroneous operation of the circuit may result.



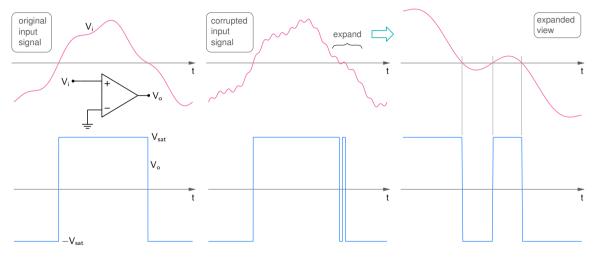






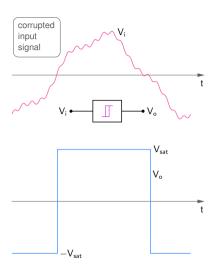


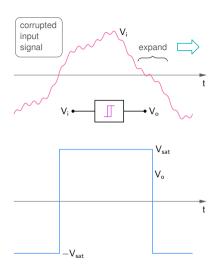
The comparator has produced multiple (spurious) transitions or "bounces," referred to as "comparator chatter."

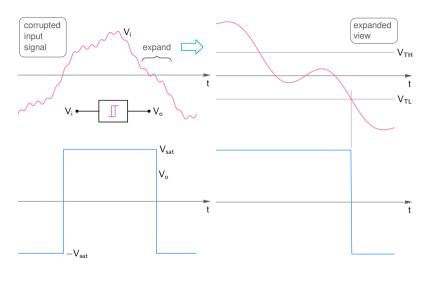


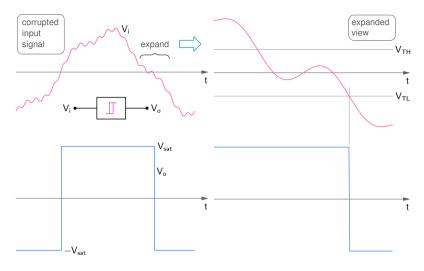
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A Schmitt trigger can be used to eliminate the chatter.

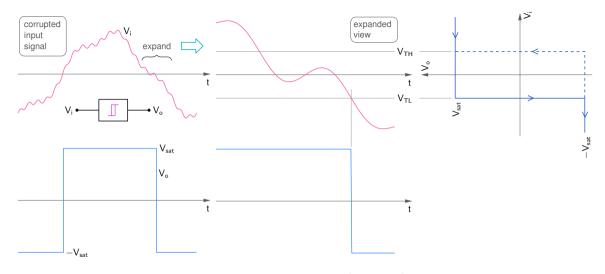




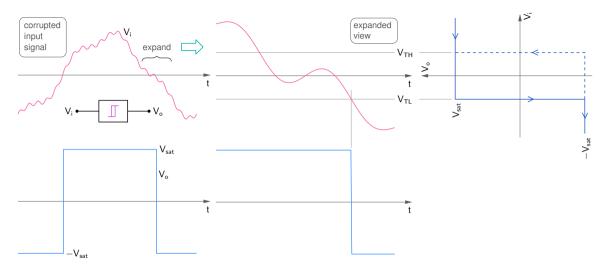




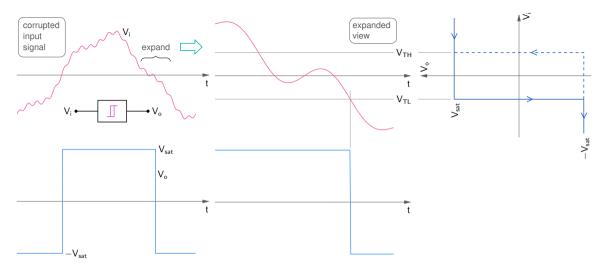
* While going from positive to negative values, V_i needs to cross V_{TL} (and not 0 V) to cause a change in V_o .



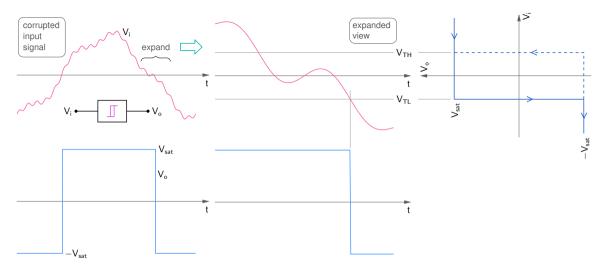
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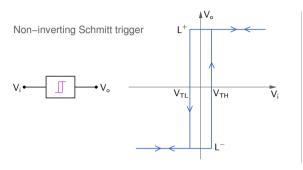
- * While going from positive to negative values, V_i needs to cross V_{TL} (and not 0 V) to cause a change in V_o .
- * In the reverse direction (negative to positive), V_i needs to cross V_{TH} .

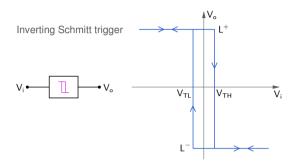


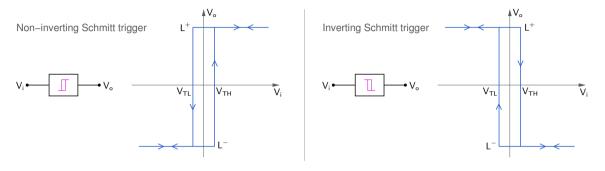
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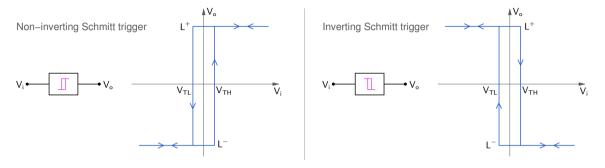
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- * The hysteresis width $(V_{TH} V_{TI})$ should be designed to be larger than the spurious excursions riding on V_i .



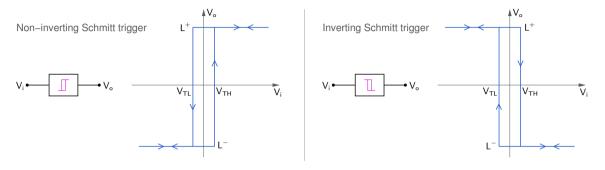




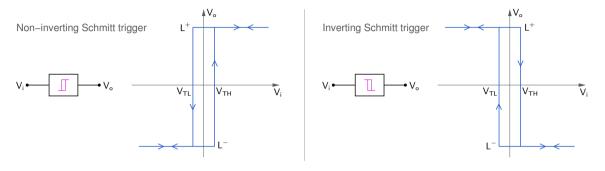
* A Schmitt trigger has two states, $V_o = L^+$ and $V_o = L^-$.



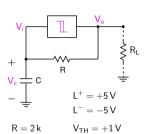
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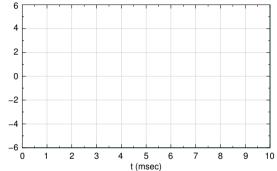


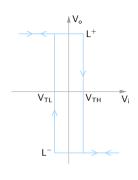
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- * The maximum operating frequency of these oscillators is typically \sim 10 kHz, due to op-amp speed limitations.

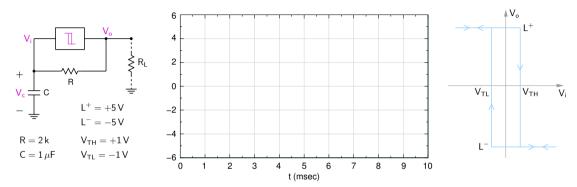


 $V_{\mathsf{TL}} = -1\,\mathsf{V}$

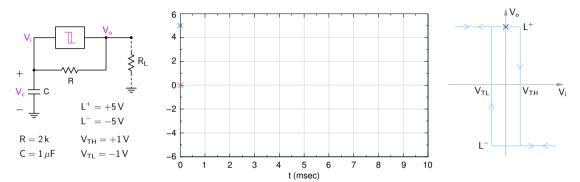
 $\mathsf{C}=1\,\mu\mathsf{F}$



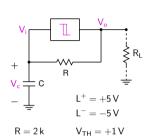




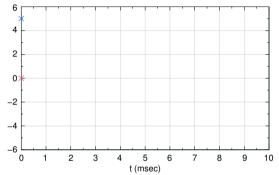
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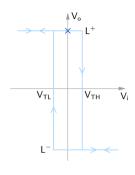


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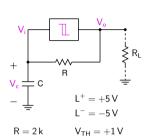
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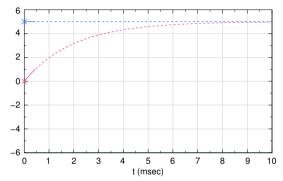


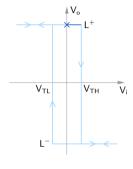
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The capacitor starts charging toward L^+ .



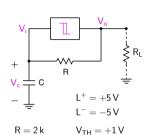
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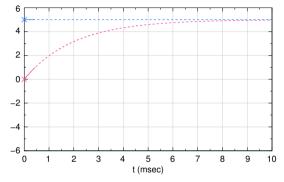


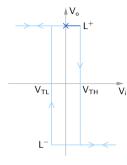
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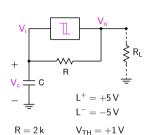




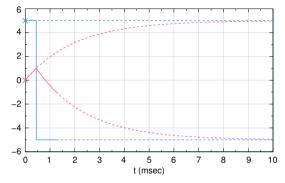
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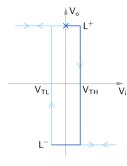
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When V_c crosses V_{TH} , the output flips. Now, the capacitor starts discharging toward L^- .



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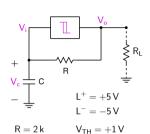




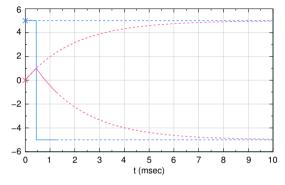
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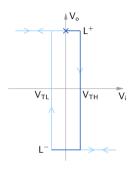
The capacitor starts charging toward L^+ .

When V_c crosses V_{TH} , the output flips. Now, the capacitor starts discharging toward L^- .



 $\mathsf{C} = 1\,\mu\mathsf{F} \qquad \mathsf{V}_{\mathsf{TI}} = -1\,\mathsf{V}$



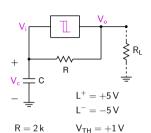


At t=0, let $V_o=L^+$, and $V_c=0$ V.

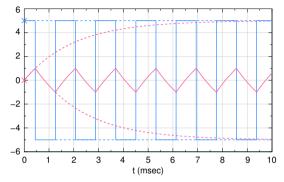
The capacitor starts charging toward L^+ .

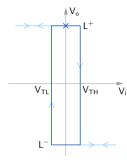
When V_c crosses V_{TH} , the output flips. Now, the capacitor starts discharging toward L^- .

When V_c crosses V_{TL} , the output flips again ightarrow oscillations.



 $\mathsf{C} = 1\,\mu\mathsf{F} \qquad \mathsf{V}_{\mathsf{TI}} = -1\,\mathsf{V}$



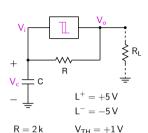


At t=0, let $V_o=L^+$, and $V_c=0$ V.

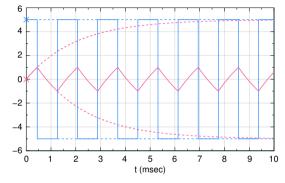
The capacitor starts charging toward L^+ .

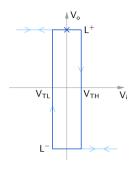
When V_c crosses V_{TH} , the output flips. Now, the capacitor starts discharging toward L^- .

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$$t=0$$
, let $V_o=L^+$, and $V_c=0$ V .

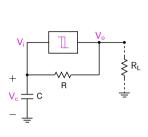
The capacitor starts charging toward L^+ .

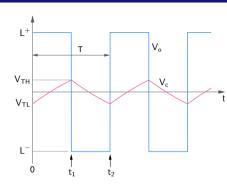
When V_c crosses V_{TH} , the output flips. Now, the capacitor starts discharging toward L^- .

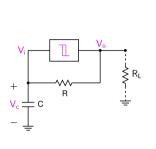
When V_c crosses V_{TL} , the output flips again o oscillations.

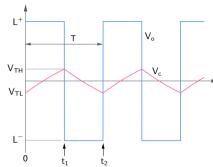
Note that the circuit oscillates on its own, i.e., without any input.

Q: Where is the energy coming from?

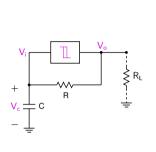


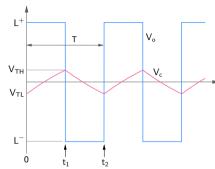






Charging: Let
$$V_c(t)=A_1 \exp(-t/\tau)+B_1$$
, with $\tau=RC$.
Using $V_c(0)=V_{TL},\ V_c(\infty)=L^+$, find A_1 and B_1 .
At $t=t_1,\ V_c=V_{TH}\to V_{TH}=A_1 \exp(-t_1/\tau)+B_1\to {\rm find}\ t_1$.





Charging: Let
$$V_c(t) = A_1 \exp(-t/\tau) + B_1$$
, with $\tau = RC$.

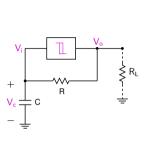
Using
$$V_c(0) = V_{TL}$$
, $V_c(\infty) = L^+$, find A_1 and B_1 .

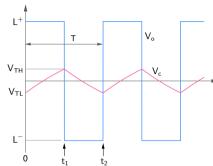
At
$$t = t_1$$
, $V_c = V_{TH} \rightarrow V_{TH} = A_1 \exp(-t_1/\tau) + B_1 \rightarrow \text{find } t_1$.

Discharging: Let
$$V_c(t) = A_2 \exp(-(t - t_1)/\tau) + B_2$$
.

Using
$$V_c(t_1) = V_{TH}, \ V_c(\infty) = L^-$$
, find A_2 and B_2 .

At
$$t = t_2$$
, $V_c = V_{TL} \to V_{TL} = A_2 \exp(-(t_2 - t_1)/\tau) + B_2 \to \text{find } (t_2 - t_1) \to t_2 \to T = t_2$.





Charging: Let
$$V_c(t) = A_1 \exp(-t/\tau) + B_1$$
, with $\tau = RC$.

Using
$$V_c(0) = V_{TL}$$
, $V_c(\infty) = L^+$, find A_1 and B_1 .

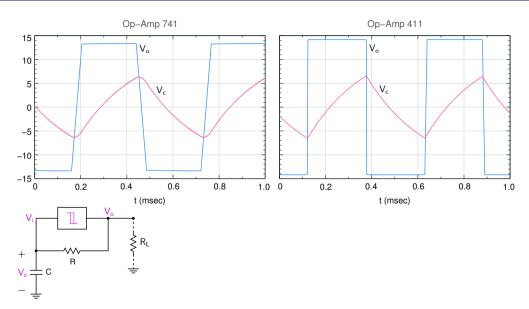
At
$$t = t_1$$
, $V_c = V_{TH} \rightarrow V_{TH} = A_1 \exp(-t_1/\tau) + B_1 \rightarrow \text{find } t_1$.

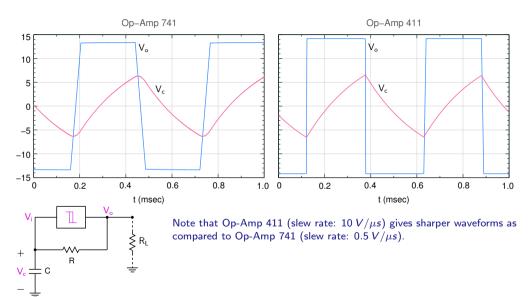
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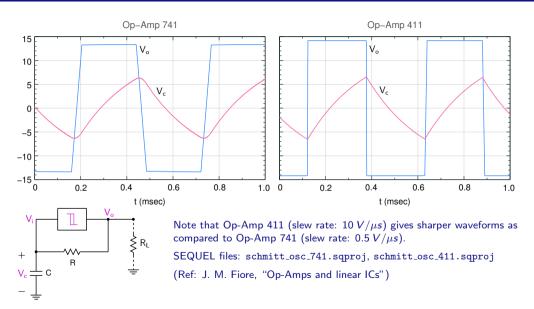
Using
$$V_c(t_1) = V_{TH}, \ V_c(\infty) = L^-$$
, find A_2 and B_2 .

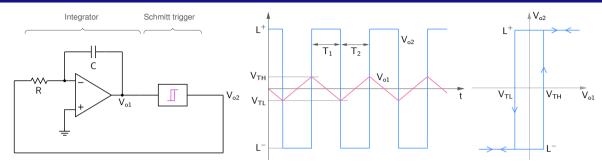
At
$$t = t_2$$
, $V_c = V_{TL} \rightarrow V_{TL} = A_2 \exp(-(t_2 - t_1)/\tau) + B_2 \rightarrow \text{find } (t_2 - t_1) \rightarrow t_2 \rightarrow T = t_2$.

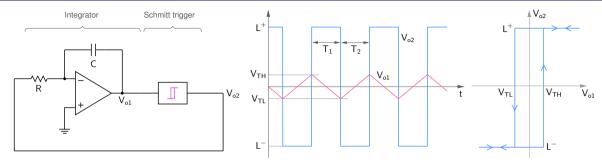
If
$$L^+ = L$$
, $L^- = -L$, $V_{TH} = V_T$, $V_{TL} = -V_T$, show that $T = 2 RC \ln \left(\frac{L + V_T}{L - V_T} \right)$.



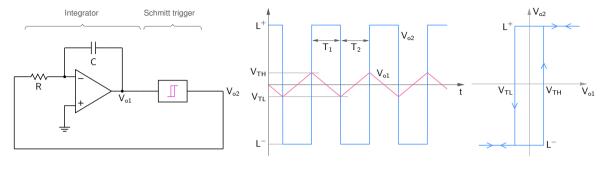






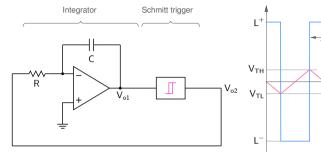


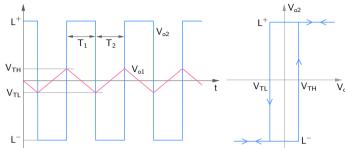
For the integrator,
$$V_{o1}=-rac{1}{RC}\int V_{o2}dt\equiv -rac{1}{ au}\int V_{o2}dt$$



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 decreases linearly, $V_{o2}=L^- o V_{o2}$ increases linearly.

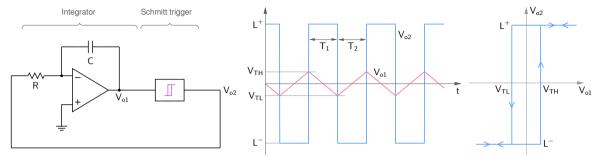




For the integrator,
$$V_{o1}=-rac{1}{RC}\int V_{o2}dt\equiv -rac{1}{\pi}\int V_{o2}dt$$

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ight|$$

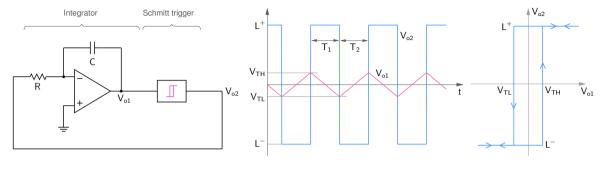


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$$T_1= au\;rac{V_{TH}-V_{TL}}{L^+}$$
 .



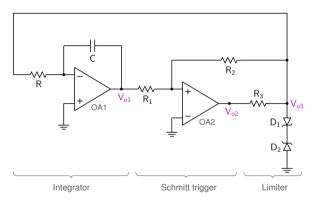
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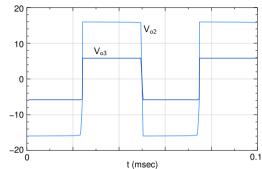
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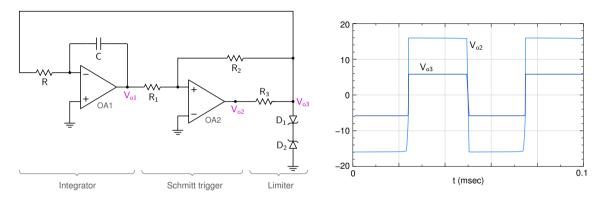
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$$T_1 = \tau \; \frac{V_{TH} - V_{TL}}{I^+} \, .$$

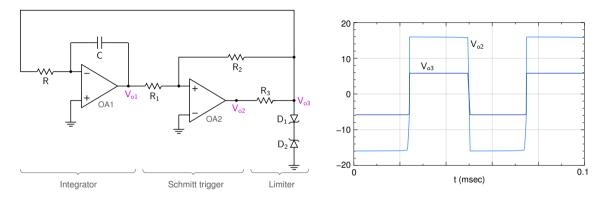
$$T_2 = \tau \; \frac{V_{TH} - V_{TL}}{-L^-} \; .$$



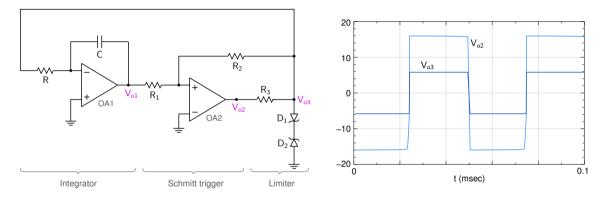




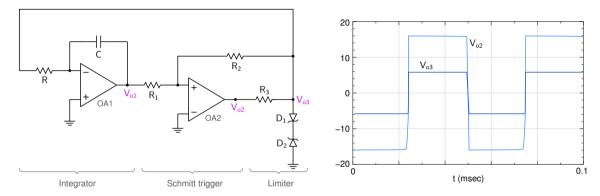
* When $V_{o2} = +V_{\text{sat}}$, D_1 is forward-biased (with a voltage drop of V_{on}), and D_2 is reverse-biased. The Zener breakdown voltage (V_Z) is chosen so that D_2 operates under breakdown condition. $V_{o3} = V_{\text{on}} + V_Z$.



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- * When $V_{o2}=-V_{\rm sat},~D_2$ is forward-biased (with a voltage drop of $V_{\rm on}$), and D_1 is reverse-biased. $\rightarrow V_{o3}=-V_{\rm on}-V_Z$.

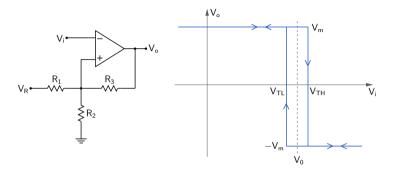


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- * R₃ serves to limit the output current for OA2.

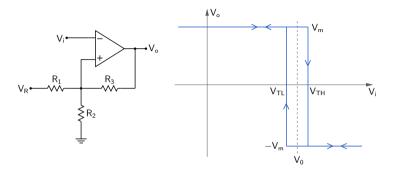


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SEQUEL file: opamp_osc_1.sqproj

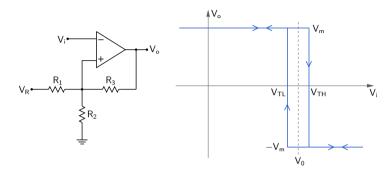


A Schmitt trigger circuit is shown in the figure along with its V_o - V_i relationship. Assume that $V_{\rm sat} \approx 14\,{\rm V}$ for the op-amp. The reference voltage V_R can be adjusted using a pot (not shown in the figure).



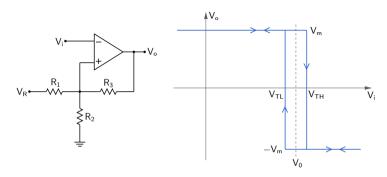
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* Design the circuit to obtain $V_0 = 2.5\,\mathrm{V}$ and $\Delta\,V_T = V_{TH} - V_{TL} = 0.4\,\mathrm{V}$.

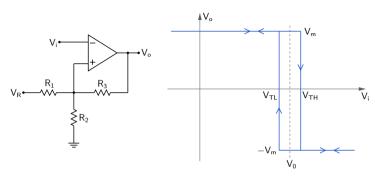


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- st Verify your design with simulation (and in the lab).

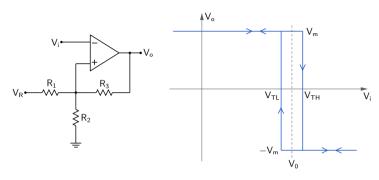


$$V_{+} = V_{R} \frac{(R_{2} \parallel R_{3})}{(R_{2} \parallel R_{3}) + R_{1}} \pm V_{m} \frac{(R_{1} \parallel R_{2})}{(R_{1} \parallel R_{2}) + R_{3}}.$$



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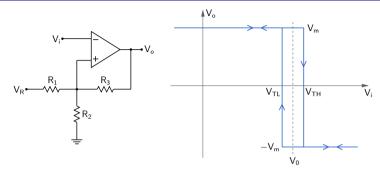
$$\Delta V_T = 0.4 \, \text{V} \ o \ 2 V_m \, \frac{(R_1 \parallel R_2)}{(R_1 \parallel R_2) + R_3} = 0.4 \, \text{V}.$$



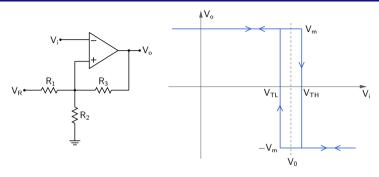
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$$V_0 = 2.5 \, \text{V} \rightarrow V_R \, \frac{(R_2 \parallel R_3)}{(R_2 \parallel R_3) + R_1} = 2.5 \, \text{V}.$$

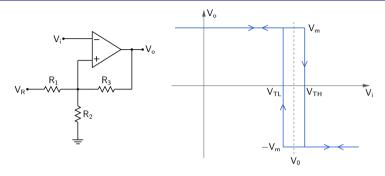


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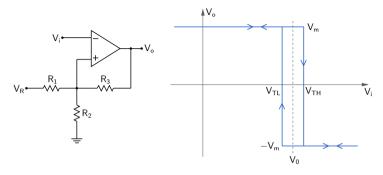
Let
$$R_1 = R_2 = 5 \text{ k} \rightarrow 2V_{\text{sat}} \frac{(R/2)}{(R/2) + R_3} = 0.4 \text{ V} \rightarrow R_3 = 172.5 \text{ k}.$$



$$\Delta V_T = 0.4 \,\text{V} \rightarrow 2 V_m \frac{(R_1 \parallel R_2)}{(R_1 \parallel R_2) + R_3} = 0.4 \,\text{V}.$$

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$$V_0 = 2.5 \,\text{V} \rightarrow V_R \, \frac{(R_2 \parallel R_3)}{(R_2 \parallel R_3) + R_1} = 2.5 \,\text{V} \rightarrow V_R = 5.07 \,\text{V}.$$



$$\Delta V_T = 0.4 \,\text{V} \ \rightarrow \ 2 V_m \, \frac{(R_1 \parallel R_2)}{(R_1 \parallel R_2) + R_3} = 0.4 \,\text{V}.$$

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(SEQUEL file: schmitt_1.sqproj)