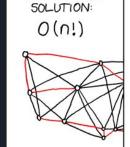
## TIME COST

STRATEGY A

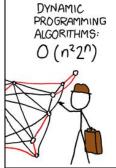
ANALYZING WHETHER STRATEGY A OR B IS MORE EFFICIENT



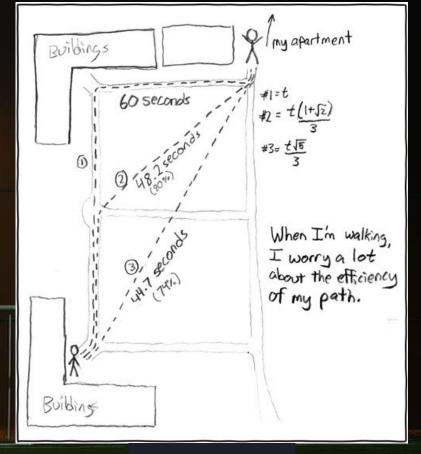
#### THE REASON I AM SO INEFFICIENT



BRUTE-FORCE







# Path Planning using Parallel Computing

High Performance Scientific Computing (ME 766) Prof Shivasubramanian Gopalakrishnan Spring 2021 Aaron John Sabu Athul C D Ayan Sharma Vaibhav Malviya

## Our Goals

03

Implement path planning techniques for a roadmap-based problem from one point (node) to another using algorithms such as Dijkstra's algorithm, Bellman-Ford algorithm, and Floyd-Warshall algorithm

O2 Incorporate shared memory parallel computing elements into the program using the implementation of OpenMP at suitable locations

Compare the performance of the algorithms with each other along with their respective implementations on OpenMP and CUDA

264346 nodes 733846 edges

# New York State Roadmap...



a <start> <end> <distance>

## The Serial Algorithms

#### Dijkstra's Algorithm

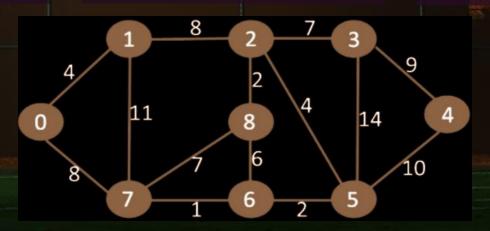
- Greedy Algorithm
- Select node with min distance and relax the connected edges

#### Bellman-Ford Algorithm

- Relaxes all the edges V-1 times
- Check for negative weight cycles in the end

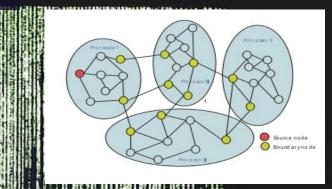
#### Floyd-Warshall Algorithm

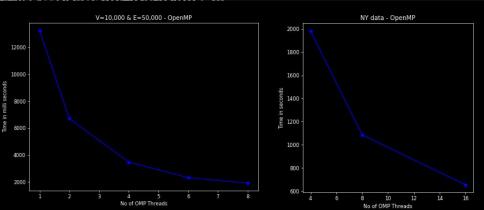
- Dynamic programming
- Shortest path in a directed weighted graph with positive or negative edge weights



## Dijkstra's Algorithm

- A very efficient serial implementation (using C++'s STL) of the algorithm just takes 3s to run on entire NY dataset.
- We developed a parallelizable implementation of the algorithm by distributing vertices to different threads of OpenMP.
- Speed up: 1.97, 3.81, 5.68, 6.89 for 2, 4, 6, 8 threads
- Parallel algorithm on NY dataset with 4, 8, and 16 threads took 1984, 1086, and 653.8sec respectively.
- Complexity: O((V+E)logV)

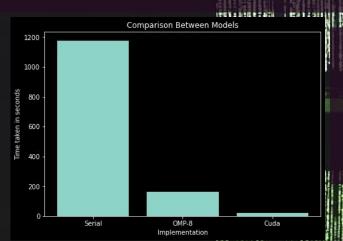






## Bellman-Ford Algorithm

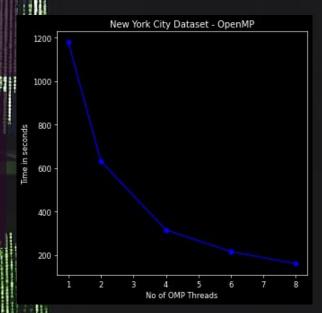
- Slower than Dijkstra's algo, but faster than Floyd-Warshall algo
  O(VE)
- Can detect and report negative cycles
- Not greedy: Processes all edges
- Not highly parallelizable: key loop is Sequential
- Speed up: achievable by non-sequential operations on edges
  - Running on a GPU/CUDA → significant speed up



- OpenMP also gives speed up by dividing the edges across different cores
- Speed up: 1.86, 3.72, 5.44,7.33 for 2, 4, 6, 8 threads







## Floyd-Warshall Algorithm

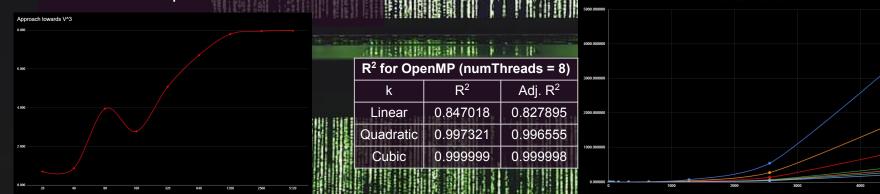
- Single execution → Shortest path lengths between all vertex pairs
- Worst time complexity  $O(V^3)$
- **Usefulness:** 
  - Small dense graphs
  - All-pairs minimum distance







R <sup>2</sup> for CUDA				
k	R <sup>2</sup>	Adj. R <sup>2</sup>		
Linear	0.918447	0.908253		
Quadratic	0.999986	0.999982		
Cubic	0.999988	0.999983		



# A Comparison of the Algorithms

	Dijkstra's	Bellman-Ford	Floyd-Warshall
Time Complexity	O((V+E)log(V))	O(VE)	O(V <sup>3</sup> )
Can Negative Cycles?	No	Yes	Yes
Suitable for :	Large/Medium	Medium/Small	Small
For NY Dataset:	Running time: 3sec	Running time: 1178s	Running time: ∞
Speed Up (8 threads):	6.89	7.33	7.965

## Future Prospects

- $\bullet$  More algorithms :  $A^*$  RRT
  - D\* Delta stepping
- More Versatile Platforms: OpenCL

MPI

- Multi-Agent Path Planning
  - Road Constraints
- Parallelization + Proper Hardware
  - = Real-time Path Generation

### References

- Tang et al., A Parallel Shortest Path Algorithm Based on Graph-Partitioning and Iterative Correcting, IEEE HPSC
- Crauser et al., A Parallelization of Dijkstra's Shortest Path Algorithm
- ➤ Li et al., Multi-Agent Path Finding for Large Agents
- Hong et al., Efficient Parallel Graph Exploration on Multi-Core CPU and GPU
- Agarwal et al., New Approach of Bellman Ford
  Algorithm on GPU using Compute Unified Design
  Architecture (CUDA)
- https://people.sc.fsu.edu/~jburkardt/c\_src/dijkstr a\_openmp/dijkstra\_openmp.html

