C:/my_designs/Instruction_Fe	tch/Instruct	ion_Fetch/src/IF.asdb C:/m	ny_designs/Instruction_F	etch/Instruction_Fetch/s	src/IF.awc					
Signal name	Value	8	16	24	32	40	48 56	. 64	72	· · ns
лгCLK	1 to 0									
⊞ <b>Љ</b> Instruction_I	09		00		01	X	02		03	
⊞ <b>Љ</b> Instruction	000	00	00200		0000220	X	0000240		0000260	
⊕ <b>Љ</b> Instruction	000	UUUUUUU	X	0000200	X	0000220	X	0000240		0000260
Cursor 1										

C:/my_designs/Instruction_F	etch/Instruc	tion_Fetch/src/IF.asdb C:/my_designs	s/Instruction_Fetch/Instruction_I	Fetch/src/IF.awc					
Signal name	Value	88	96 104	112	120	128	136 144	15	i2 ' ns
лг CLK	1 to 0								
⊞ <b>Љ</b> Instruction_I	09	04	X	05	X	06	X	07	
⊞ <b>Љ</b> Instruction	000	0000280	X	00002A0	X	00002C0	X	00002E0	
⊞ <b>Љ</b> Instruction	000	0000260	0000280	X	00002A0	X	00002C0		00002E0
Cursor 1									

		cion_Fetch/src/IF.asdb C:/my_designs/						
Signal name	Value	168	176 184	192	200	208 216	6	2 ' ' ns
лг CLK	1 to 0							
	09	08				09		
	000	0000300	X			0000320		
⊕ <b>፲</b> Instruction	000	00002E0	0000300	X	0000320	X	0000000	
Cursor 1				3/3 ( 160 ns - 24	10			240 ns