Forwarding Unit

Design Unit Header

```
library ieee;
use ieee.std_logic_1164.all;
```

regA(4:0)
regC(4:0)
regD(4:0)
regB(4:0)
W_EN

```
process (W_EN,regA,regB,
        begin
        if W_EN =
            if regD =
                Mux1_...
            else
                Mux1_...
        end if;
        if regD =
            Mux2_...
```

Mux2_Selector
Mux3_Selector
Mux1_Selector
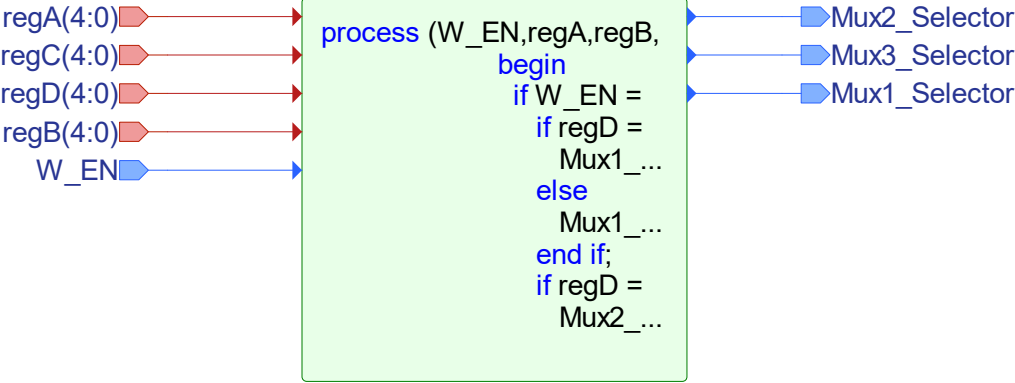
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