ID/EX Register

Design Unit Header

library ieee;
use ieee.std_logic_1164.all;
use ieee.NUMERIC_STD....

CLK

I_regB(4:0)
I_regA_Data(127:0)
I_regB_Data(127:0)
I_regC_Data(127:0)
I_regD(4:0)
I_ALUop(19:0)
I_regC(4:0)
I_W_EN
I_regA(4:0)

process (CLK)
    variable ...
    type stor is
    type stor2 is
    variable A...
    variable reg
    variable d...
begin
    if rising_e...
    W_EN :=

O_regC(4:0)
O_ALUop(19:0)
O_regB_Data(127:0)
O_regC_Data(127:0)
O_regA_Data(127:0)
O_regD(4:0)
O_regA(4:0)
O_regB(4:0)
O_W_EN
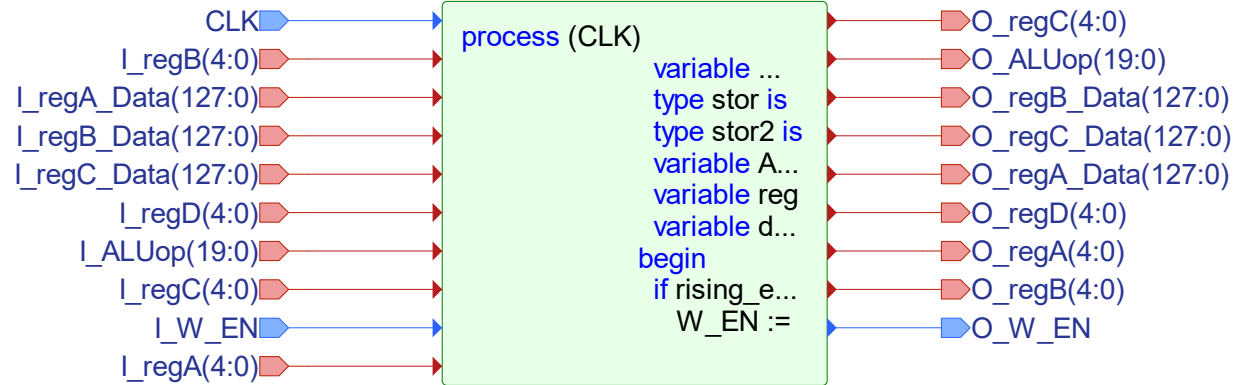
	(C)ALDEC. Inc 2260 Corporate Circle Henderson, NV 89074
Created:	5/2/2020
Title:	No Title
Revision:	1.0
Page:	1 / 1

ALDEC
THE DESIGN VERIFICATION COMPANY