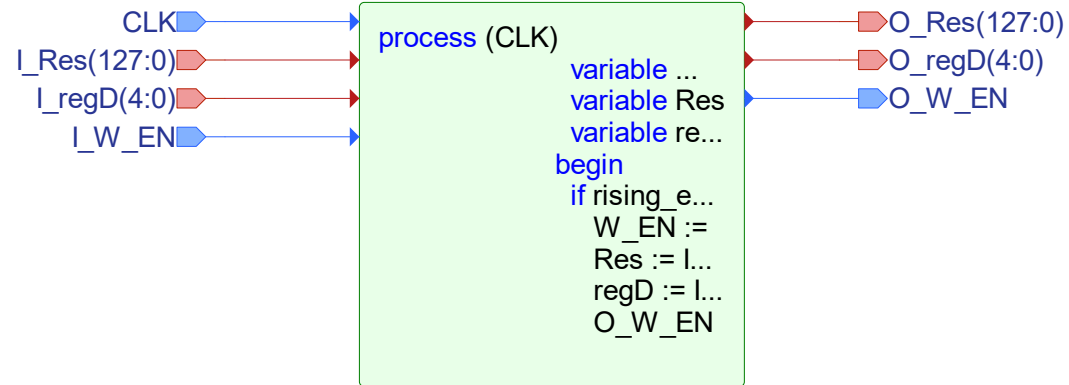



EX/WB Register

Design Unit Header

```
library ieee;  
use ieee.std_logic_1164.all;
```



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Title:	No Title
Revision:	1.0
Page:	1 / 1