

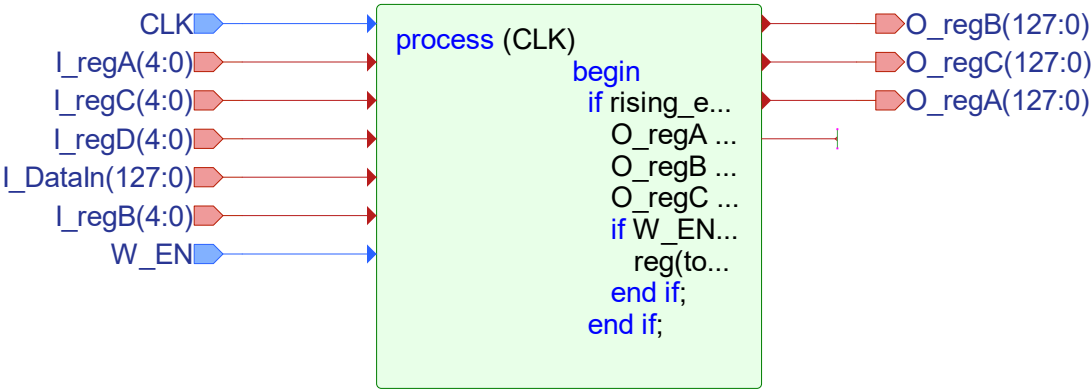
32 128 bits Register File

Design Unit Header

```
library ieee;  
use ieee.std_logic_1164.all;  
use ieee.NUMERIC_STD....
```

Architecture Declaration

```
--Added by Active-HDL. D...  
type stor is array (0 to 31)  
--End of extra code.
```



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