\PRDATA_reg[1] (FDCE) CLR \mem_reg_0_255_1_1 (RAM256X1S) WCLK \mem_reg_512_767_1_1 (RAM256X1S) WCLK PRDATA_reg[1]_D (LUT6) \mem_reg_256_511_1_1 (RAM256X1S)
A 0 WCLK 15 \mem_reg_768_1023_1_1 (RAM256X1S) PRDATA_reg[3] (FDCE) CLR \mem_reg_0_255_3_3 (RAM256X1S)
_A 0 WCLK mem_reg_512_767_3_3 (RAM256X1S) WCLK PRDATA_reg[3]_D (LUT6) mem_reg_256_511_3_3 (RAM256X1S) WCLK 14 15 \mem_reg_768_1023_3_3 (RAM256X1S) PRDATA_reg[2] (FDCE) CLR \mem_reg_0_255_2_2 (RAM256X1S) \mem_reg_512_767_2_2 (RAM256X1S) WCLK PRDATA_reg[2]_D (LUT6) mem_reg_256_511_2_2 (RAM256X1S)























