

AATHITHYA PR

ABOUT ME

ECE student majored in VLSI domain. I consider myself a responsible, orderly person and always awaits for challenges. I am looking forward to my first work experience.

CONTACT



Date of Birth 27-01-2004



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AREA OF INTEREST

District 638455

- Python
- Image Processing
- Web Development
- · IOT
- Semiconductor

PERSONAL TRAITS

- · Calm and Flexible
- Honest
- Team Player
- Effective Communication Skill
- · Diligent and Organized

PROFILE

LINKEDIN:

https://www.linkedin.com/in/aath ithya-pr-9912071b6/ GITHUB:

https://github.com/aathithya-27? tab=projects

EDUCATION

- R.M.K ENGINEERING COLLEGE(2021-2025 B.E in Electronics and Communication / Honors in VLSI Design / Anna University / RMK Engineering College / CGPD - 8.22
- SVISSS CBSE HSC - 87.5%(2019) SSC- 84.0%(2021)

SKILLS

- Languages: English, Tamil, Hindi.
- Technical(Intermediate Level): Python, C#, Open CV, Verilog, HTML, C.
- Development Tools

 Vs Code, Pycharm, GitHub, Microsoft Visual
 Studio, Jupyter.

ACHIEVEMENTS

- SIH(Smart India Hackathon) Participated
- Project with Macs info Solution and TVS for bike face lock system(in progress)
- National Throwball Player

PROJECTS

- Bike Face Lock System: To turn ON the bike after recognising the owner of the bike with a ESP-32 Cam
- Motion detection night light with ESP-32 Cam: To collect the photo and to turn the lights on when there is a Motion and sends the photo of the motion to user
- Image and Object Detection in Xilinx using Python: Instead of Verilog Language implementing Python language in FPGA-Board using PYNQ
- Contact Saver of Clients :A C# project to save all the client details

INTERNSHIPS

Macs Info Solution & TVS-30 days:

- Learnt how to work on Open CV, and Image Processing
- Learnt how to work on C# (WebDevelopment)