Assignment 0 - COL380

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1 Introduction

In this assignment, we will use perf tool to analyze the performance of a program. We will also use the perf tool to analyze the performance of a program after doing different optimizations like removing false sharing, removing memory leaks, etc. We will compare the performance of the program before and after the optimizations based on different metrics like cache misses, branch misses, etc.

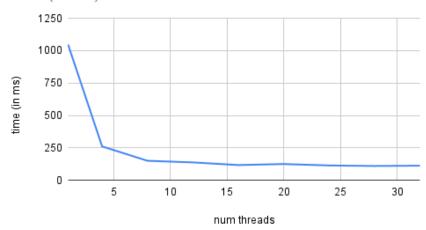
2 Setting up and Running Perf

I used css1 to check the runtime of the code. For the report files I have used perf on my local machine since it was not working on the css machine. Same issue was faced by a lot of students.

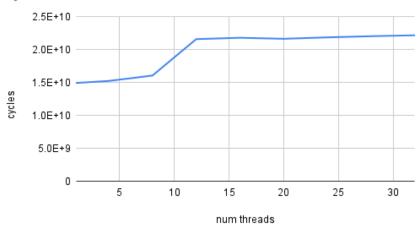
2.1 Perf stat

I have varied the number of threads as $1, 4, 8, \ldots, 32$ keeping the number of iterations fixed as 3. There is a significant decrease till we use 16 threads, because this is the maximum number of threads we can run in parallel on the css machine. After this point the run-time does not decrease significantly because they are not actually running on different threads. They are just running like concurrent processes after that and this doesn't reduce the run-time significantly. However using more and more threads adds the overhead of thread management and context switching. Hence we can observe an increase in the number of cycles required to run the program. So it is best to use not more than 16 threads.





cycles vs. num threads



2.2 Perf Record

2.2.1

After running the perf record make run command, we get the perf.data file. I have saved it with separate name as mentioned in the assignment.

2.2.2

We obtained the perf data after running the previous command. This is not readable by humans and hence we need to use perf to read it. It is done using the following command perf report -i <filename>

2.2.3

The instruction marked in red takes the most amount of time. perf marks the instructions which take the most amount of time (or whatever metric we are using). jg 93 instructions takes the most time.

2.2.4

Yes this can be easily done by adding -g flag in the CFLAGS during the compilation in the Makefile. Now after this we can see the annotated assembly code along with which source code it points to.

```
Activities | Terminal | Jan 15 19:1004 |
```

2.2.5

Make the following change: CFLAGS=-std=c++11 -02 -g and we can see the source code along with the assembly instructions.

3 Hotspot Analysis

3.1

As shown in the screenshot above, the report is now showing source code along with assembly code. I have saved the perf data file with the appropriate name as mentioned in the assignment.

3.2

Most amount of time is taken by the jg 93 instruction. This is corresponding to the within() function.

```
bool within(int val) const { // Return if val is within this range
          return(lo <= val && val <= hi);
0.25
                    0x4(%r11,%rax,8),%edx
0.42
            shl
                    $0x6,%rax
0.04
            add
                    %гЬр,%гах
           Z8classifyR4DataRK6Rangesj._omp_fn.0():
0.08
            MOV
                    %r13d,0x4(%r12)
          // and store the interval id in value. D is changed.
          counts[v].increase(tid); // Found one key in interval v
```

3.3

The above mentioned instruction is probably a hotspot because the function is getting called a lot of times. For every data item we are traversing through the entire list of ranges. Morever the excessive time being taken at this point is because of poor caache utilization and false sharing. The above need to be resolved for making it more efficient.

3.4

Yes, it is possible to optimize it further. One basic optimization possible in this case is that instead of using this as a function we can make it inline function. That is instead of doing another function call we can simply replace within() in the Range::range() function. This results in decent speedup because function calls are heavy. They require a lot of push and pop operations. Removing them will definitely make the code faster. The memory and cache related optimizations have been discussed in the further sections.

3.5

To get the list of events which perf can record, run the command perf list. I ran the following command: perf record -e branch-instructions, branch-misses, cache-misses, page-faults, cpu-cycles make run I have saved the perf.data file with the appropriate name as mentioned in the assignment.

4 Memory Profiling

4.1

I ran perf mem record make run and saved the report with appropriate name as mentioned.

4.2

Amongst the various hotspots, the following are the 2 major ones

1. The first hotspot is in the first for loop of classify function.

```
for(int i=tid; i<D.ndata; i+=numt) { // Threads together share-loop through all of Data add %ebx,%ecx
_ZN7Counter8increaseEj():

mov (%rax),%edx
add $0x1,%edx
mov %edx,(%rax)
_Z8classifyR4DataRK6Rangesj._omp_fn.0():

mov %ecx,%eax
0.01 cmp %ecx,(%r8)

→ jbe 33f0 <classify(Data&, Ranges const&, unsigned int) [clone ._omp_fn.0]+0xb0>
```

2. Second is in the last for loop of the classify function.

```
if(D.data[d].value == r) // If the data item is in this interval

cmp %eax,0x4(%rcx)

98.49 → jne 3321 <classify(Data&, Ranges const&, unsigned int) [clone ._omp_fn.1]+0x81>

D2.data[rangecount[r-1]+rcount++] = D.data[d]; // Copy it to the appropriate place in D2.

mov 0x18(%rbx),%r9

mov (%rcx),%rcx

mov %esi,%r10d

add $0x1,%est

0.12 add (%r11),%r10d

0.17 mov 0x8(%r9),%r9

mov %rcx,(%r9,%r10,8)
```

4.3

After reading the perf report I have analyzed different issues in the code. Their is false sharing happening and also instances of cache misses which can be avoided. The issues are:

- 1. In the first for loop of the classify function and the last for loop of it, data is being accessed in a cyclic fashion in the threads. All the threads are trying to update the values in the same cache line. So every time, the cache line has to be updated in all of them. This is an instance of false sharing. We are not saving any time since we are updating the same cache line in all the threads. This can be resolved by dividing the data into contiguous chunks instead of cyclic chunks.
- 2. Second issue is when we are updating the count[v].increase(tid). Since the size of counts[v] array is small, the entire array gets stored in the cache. But when we are updating the count[v].increase(tid) in one thread, it needs to be updated in the other ones as well. Hence another instance of false sharing. This can be resolved using padding.

4.4

1. As discussed in the above section about the instances of false sharing, I have tried to remove them by dividing the data into contiguous chunks instead of dividing the work in a cyclic manner. By allotting a big chunk to the thread we can avoid false sharing. ALl the adjacent array values belong to the same thread only and hence need not be updated in the other threads. This results in a significant speedup. I have implemented this change in both the first loop of classify function and the last loop of it. The time taken is 185.252ms which is a significant improvement over the initial time taken of 310.047ms.

2. I have also removed the separate function call to within() and made it inline. This also reduced rhe run time to some extent since function calls are heavy, they require some significant amount of push and pop operations.

After doing these optimizations, I ran the perf mem record make run command and saved the report with appropriate name as mentioned. The hotspots earlier are not taking as much time as they were taking earlier. This is evident from the following screenshot.

1. The first hotspot is in the first for loop of classify function. This is not taking any significant percent of time now.

2. Second is in the last for loop of the classify function. This is also not taking any significant percent of time now as compared to it was taking before.

```
int r = D.data[i].value;

movslq 0x4(%rsi,%rdx,8),%rcx

if ( start<=r && r< end ) D2.data[rangecount[r]++] = D.data[i];

cmp %ecx,%eax

→ jg 32fd <classify(Data&, Ranges const&, unsigned int) [clone ._omp_fn.1]+0x5d>

cmp %ecx,%r10d

→ jle 32fd <classify(Data&, Ranges const&, unsigned int) [clone ._omp_fn.1]+0x5d>

lea 0x0(%rbp,%rcx,4),%r8

mov 0x10(%rbx),%rdi
```

Thus the optimizations have been successful in reducing the time taken by the hotspot of the code. The mem accesses have also decreased. All this has collectively resulted in decreased time taken by the code.

4.5

4.5.1 Runtime

Initially the time taken was 310.047ms

```
./classify rfile dfile 1009072 4 10
319.743 ms
318.498 ms
313.901 ms
339.938 ms
310.047 ms
312.495 ms
311.574 ms
314.727 ms
314.727 ms
312.773 ms
310.254 ms
10 iterations of 1009072 items in 1001 ranges with 4 threads: Fastest took 310.047 ms, Average was 316.395 ms
```

After optimization time is 185.252ms

```
./classify rfile dfile 1009072 4 10
186.129 ms
185.941 ms
185.525 ms
185.523 ms
187.021 ms
187.108 ms
186.603 ms
186.603 ms
186.751 ms
186.799 ms
10 iterations of 1009072 items in 1001 ranges with 4 threads: Fastest took 185.252 ms, Average was 186.459 ms
[ perf record: Woken up 1 times to write data ]
[ perf record: Captured and wrote 0.216 MB perf.data (5600 samples) ]
cs1190452@css3:~/COL380/Assignment0/A0$
```

Hence we can see after doing the optimizations there is a significant decrease in the time taken.

4.5.2 Cache Misses

Initially there were 8k events of cache misses

```
Samples: 8K of event 'cache-misses', Event count (approx.): 4446008
Overhead
          Command
                     Shared Object
                                           Symbol
                    classify
                                               0x0000000000003321
          classify
          classify
                    classify
                                               0x0000000000003300
          classify
                     [unknown]
                                               0xffffffff8ddf90c0
          classify
                    classify
                                               0x00000000000033d3
          classify
                    [unknown]
                                               0xffffffff8d2c6c18
```

Now there are only 5k events of cache misses after optimization

```
5K of event 'cache-misses', Event count (approx.): 4559236
Samples:
Overhead
          Command
                    Shared Object
                                           Symbol
          classify
                    classify
                                               0x00000000000032fd
          classify
                    classify
                                               0x00000000000032ea
          classify
                    classify
                                               0x00000000000032d0
                    [unknown]
          classify
                                               0xffffffff8ddf90c0
          classify
                    classify
                                               0x00000000000032e3
                                               0x00000000000032d9
                    classify
                                               0x0000000000003399
```

Hence there have been a significant decrease in the number of cache misses, which has resulted in a significant decrease in the time taken.