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An Inexpensive Terminal Node Controller for Packet Radio

An inexpensive processor chip forms the basis for a simple-to-build TNC.

By Bob Ball, WB8WGA

Most hams who have tuned around on the 2-meter band at one time or another have heard the infamous *blurp* of packets being sent back and forth. Sometimes it is an informal QSO, sometimes an Amateur Packet Position beacon, or maybe someone sending info on a hot DX station. Packet radio has found a wide number of applications since its introduction in the 1980s.

Did you ever want to have an inexpensive way to monitor local packet activity? Or perhaps set up your own digipeater to get a communications link to a particular point? Or possibly, actually do some programming on

your own simple Terminal Node Controller (TNC)?

If so, this simple project might be the project you have wanted. The unit can easily be built for under \$25. After it is completed, the software supplied will:

- monitor local packet activity
- act as a complete digipeater
- send packet beacons at user defined intervals containing your defined text
- allow you to communicate in a round-table fashion using "converse" mode
- send APRS NMEA position reports when connected to a GPS receiver.

Note that this unit does *not* provide all functionality of the complete commercially available TNC units. It is a simplified version that captures enough functionality to get you on the air and have some fun with packet radio. If you want to expand your programming

knowledge and explore AX.25 a bit, the unit described will allow you to modify the software to support just about any application you might have been considering for packet radio. This could include, for example, anything from temperature monitoring, GPS interfaces or remote control applications. The list of possibilities goes on and on. All source code for the unit is provided as a starting point and programming is possible without buying an expensive programmer. We will provide additional information on programming the processor later in this article.

I got started on this project after looking at the functionality of some of the new microprocessor chips. They just keep getting more powerful while the cost keeps dropping. After checking out a few different ones, I became attracted to the *MicroChip* series, in particular, one designated the

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PIC16F88. This 18-pin dual-inline chip sells for about \$3 in single units. It provides several analog to digital (A/D) channels, includes a UART to talk to a terminal, a programmable Flash program memory and a programmable EEPROM memory to store call signs and options. It can execute a program instruction every 200 nsec. Amazing! It is certainly not the Intel 8080 I used back in the early 70s! After looking at the unit I began to wonder if it could form the basis of a simple TNC.

One of the things I discovered is that many of the existing PIC processor based packet TNCs use the

MX614P modem chip. Modem chips are great building blocks but do add to power consumption and cost. In addition, they can be difficult to obtain. Most of the existing TNC project designs use processor chips that have been replaced by more powerful and less expensive units.

This article will describe how to use the enhanced internal capability of these new processors to code and decode the digital signals. While the project described here is designed for packet radio, the project board and modemless techniques described should work for just about any digital

mode (RTTY, Pactor, etc). So, once you have built this unit for packet use, with a little work and ingenuity, it can be reprogrammed for enhanced packet radio or other amateur digital modes.

As with many ham projects, I was able to build on the published work of many others. In particular, Mike Berg, NØQBH,¹ has done some work on modem-less receiver design using an external comparator. A number of PIC programmers² have documented resistor ladder network designs for older technology PIC processors that gen-

¹Notes appear on page 25.

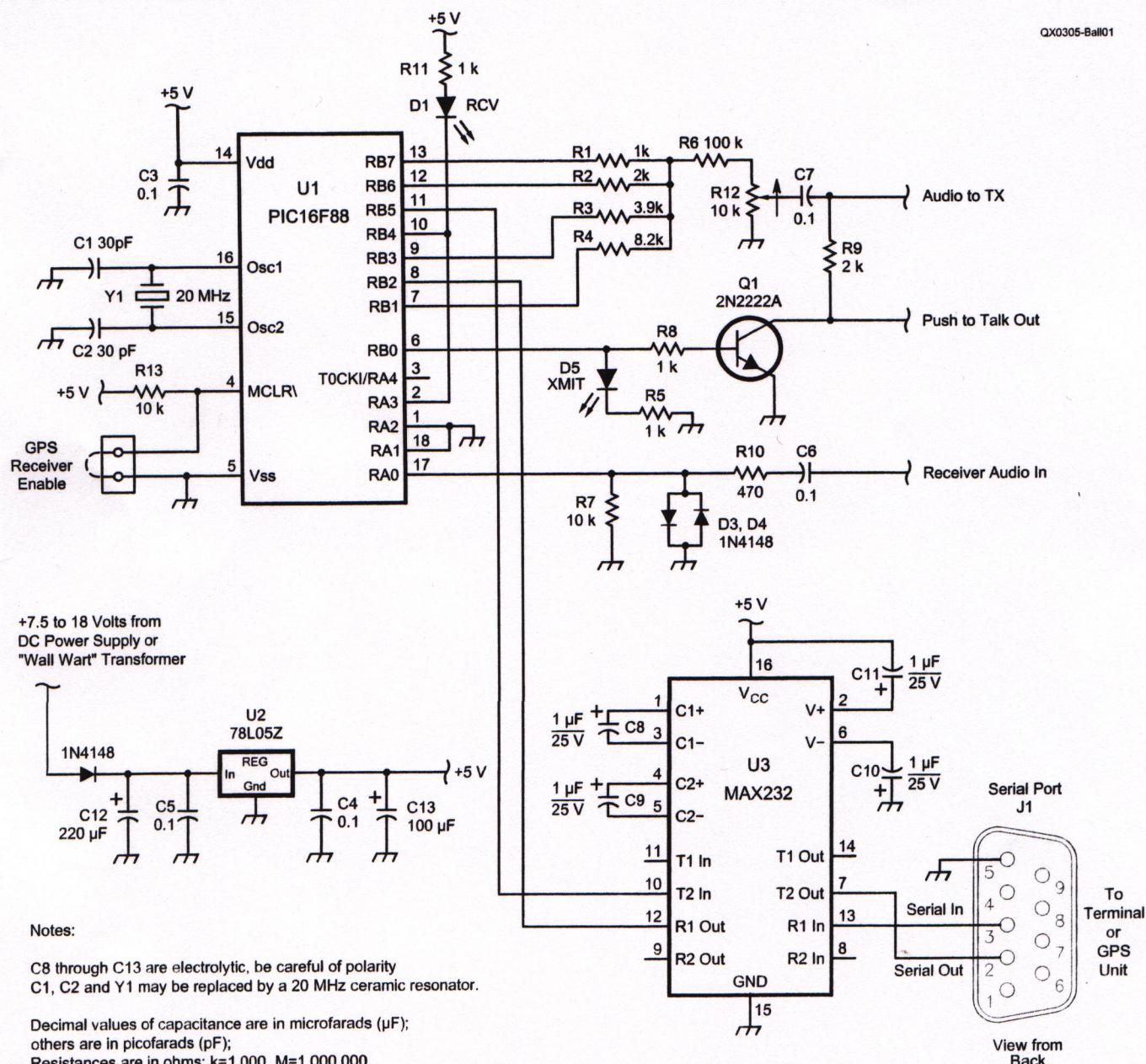


Figure 1—Schematic of the inexpensive TNC.

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erate a clean sine wave needed to transmit Audio Frequency Shift Keying (AFSK) tones for transmit. I have combined this existing receive and transmit work, added a command interface which is patterned after the Tucson Amateur Packet Radio (TAPR)³ terminal node controllers, and made use of the newest PIC technology to produce a simple TNC. It provides the following features:

- A command interface is provided that allows configurable options to be set and stored in EE ram and restored on subsequent resets.
- A monitor feature is included to allow all packets, no packets, or just packets addressed to my station to be displayed on the terminal attached to the serial port.
- Digipeating of packets (up to 255 bytes/packet) is supported.
- Alias capability allows the unit to function as RELAY type digipeater.
- Beacon capability with user set parameters is supported.
- Converse mode operation to transmit text typed from the serial port supporting "round table" chat type operation is provided.
- Support is provided to transmit APRS position beacons when a GPS unit is attached to the serial port.

Circuit Description

A schematic of the TNC is shown in Figure 1. It contains two integrated circuits, one for level conversion of the RS232 signals and the microprocessor. The microprocessor is the 18-pin DIP version of the PIC16F88. The popular MAX232 chip does the RS232 conversion.

Connections to the radio consist of audio input from the speaker jack, audio to the microphone circuit, and a connection to the push to talk circuit.

The serial port is used to connect to a standard terminal program or to a GPS receiver that outputs NMEA sentences. The terminal program is used to display received packets, send text while in the CHAT mode, and do some initial setup of the unit. Jumper J4 is used to specify what is connected on the serial port.

Note that after initial setup is complete and the station parameters are set in EERAM, the unit will run as a super simple remote digipeater without the MAX232 and serial port connections. With this configuration, it becomes a one-chip TNC.

Power to the unit can be anything from 7 to 18 V. Power from the 12 V radio can be used, or an inexpensive wall wart will do the job.

Receiving Packets

Packet radio, in common with many digital transmission techniques, uses *Audio Frequency Shift Keying* (AFSK) to send the stream of *ones* and *zeros*. It uses two frequencies, 1200 and 2200 Hz to represent a change in the value of the bit stream. For receiving purposes, this TNC uses a comparator to find zero crossings of the AFSK sine wave. The TNC calculates the interval between zero crossings to determine the frequency present. The software then converts the changes in tones to a data stream. To imagine this, think of a sine wave at either 1200 or 2200 Hz. If we clip the sine wave with a couple of diodes (D1 and D2 in the schematic), we get a square wave. The circuit is shown in Fig 2.

If the resulting square wave is fed to a zero-crossing-detector, a pulse is generated at every crossing. If the processor internally times the interval between these pulses, it can determine the frequency of the sine wave. The F88 processor has timers and an internal comparator circuit so all that decoding work can be done inside the chip. The software inside the PIC can also do some digital filtering and throw out frequencies outside of the passband of the audio. The resulting modem-less design is extremely sensitive and rivals the performance of the commercial modem chips like the MX614. My units routinely decode packets that are less than 1 S unit on my 2-meter radio from stations over 75 miles away.

It should be noted that the above technique could be used for sampling the frequency of any sine wave. It should work for any of the digital modes that use AFSK or for any applications that need to detect an audio tone of a certain frequency.

Transmitting Packets

To transmit, the software needs to generate either a 1200 or 2200 Hz sine wave. The frequency is changed to indicate the transmission of a digital one or zero.

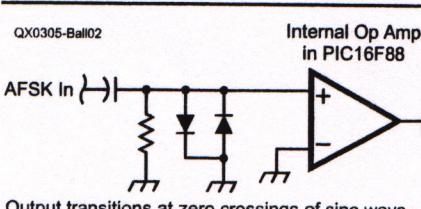


Figure 2—Determining the frequency of a sine wave using a zero crossing detector.

A sine wave is no more than a varying voltage level over time. If the processor can change the voltage level in the right amount and time frame, a sine wave with low distortion can be produced. The TNC must only generate a single tone at a time, although production of multiple simultaneous tones (i.e. DTMF) is possible. MicroChip has documented this technique in one of its application notes if you are interested in more details.

This generation can be done in software by controlling a resistor ladder network that has the correct values to generate a sine wave. If the voltage is changed often enough (say 32 times per audio cycle) at the right value, a sine wave is produced. Figure 3 shows the voltages that are generated at the wiper of R12 when the Ports RB7, RB6, RB5 and RB1 are taken through a binary sequence by the program at the correct frequency. Increasing the number of samples decreases the distortion of the sine wave but increases the real-time requirement. A sine wave adequate for this application can be generated using 32 points.

Software

Approximately 3000 words of program instruction reside on the PIC chip to do the job of taking the received packet, storing it, checking it for validity, sending the info to the serial port, and re-transmitting the packet as necessary. If you are not interested in studying or modifying the provided software, a copy of the working assembled software is provided that can be downloaded into the processor (see next section).

If you are interested in learning about PICs or how packets are formed, etc, you might want to modify the code to meet your needs. While the complete software structure will not be described here, a copy of the commented source code is available⁴ and can be used as a starting point for further experimentation. The software is organized by functional processes (ie receive packet, digipeat, command processor, etc) and scheduled in a round robin manner to make modification easy. The terminal interface is implemented as a table driven function, so that additional input commands can be easily added.

The software in this unit uses published snippets of code from many hams who have shared their work on the Web. References and credits are given to all the authors in the source software. Please remember to maintain these credits if you modify the

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Construction

Construction and layout of this unit is not critical. One option is to build on a standard perforated board. The unit shown in Figure 4⁶ was built on

standard RadioShack prototype board with two aluminum angle brackets for supports. As with all CMOS integrated circuits, use caution when inserting the ICs to avoid electro static damage.

A good wrist-grounding strap is very useful.

This unit runs at a relatively fast clock speed so bypassing of the 5 V power is important. The 0.1 μ F capaci-

Spreadsheet to Calculate Sine Wave Value for TNC Transmitter

Value	MSB				LSB		Ladder Out	Time t	To Transmitter
	V1	V2	V3	V4	Eo	Vo			
7	0	5	5	5	5	2.33	1	0.21	
6	0	5	5	0	2.00		2	0.18	
5		5		5	1.65		3	0.15	
4		5			1.32		4	0.12	
3			5	5	1.00		5	0.09	
2			5	5	0.68		6	0.06	
1				5	0.32		7	0.03	
1				5	0.32		8	0.03	
1				5	0.32		9	0.03	
1				5	0.32		10	0.03	
2				5	0.68		11	0.06	
3			5	5	1.00		12	0.09	
4			5		1.32		13	0.12	
5			5		1.65		14	0.15	
6			5	5	2.00		15	0.18	
7		5	5	5	2.33		16	0.21	
9	5		0	5	2.97		17	0.27	
10	5		5	0	3.33		18	0.30	
11	5	0	5	5	3.65		19	0.33	
12	5	5		0	3.97		20	0.36	
13	5	5	0	5	4.29		21	0.39	
14	5	5	5	0	4.65		22	0.42	
15	5	5	5	5	4.97		23	0.45	
15	5	5	5	5	4.97		24	0.45	
15	5	5	5	5	4.97		25	0.45	
15	5	5	5	5	4.97		26	0.45	
14	5	5	5	0	4.65		27	0.42	
13	5	5	0	5	4.29		28	0.39	
12	5	5	0	0	3.97		29	0.36	
10	5		5		3.33		30	0.30	
9	5	0	0	5	2.97		31	0.27	

Note when Vo is Plotted over time, a sine wave is produced

Figure 3—Sine wave generation on a PIC.

tors on each integrated circuit should be located right at the device with leads of one inch or less. I can't tell you how many times I have spent time debugging a digital circuit problem

only to find out I had noise on the 5 V line. Bypass capacitors are cheap and small, use them freely.

If you make your own board for this project, it is always good to do some

checkout before you install the integrated circuits. First apply 12 V and check for +5 on pin 14 of the PIC16F88 and pin 16 of the MAX232. If this looks good, install the chips using the wrist strap. Don't forget to program the microprocessor chip (see later section) before installation.

After building the prototype shown in Figure 4, I discovered another construction option, the popular Olimex prototyping board. This is the fastest way to construct the TNC.

Many projects need the basic processor, crystal, power circuit and serial connection. Olimex has provided a built and tested circuit board with this much of the circuit complete. It also includes an area to build the rest of the circuit. In the case of the TNC, approximately 1/2 of the components are already on the board.

The purchased board from Olimex contains the processor chip, all serial

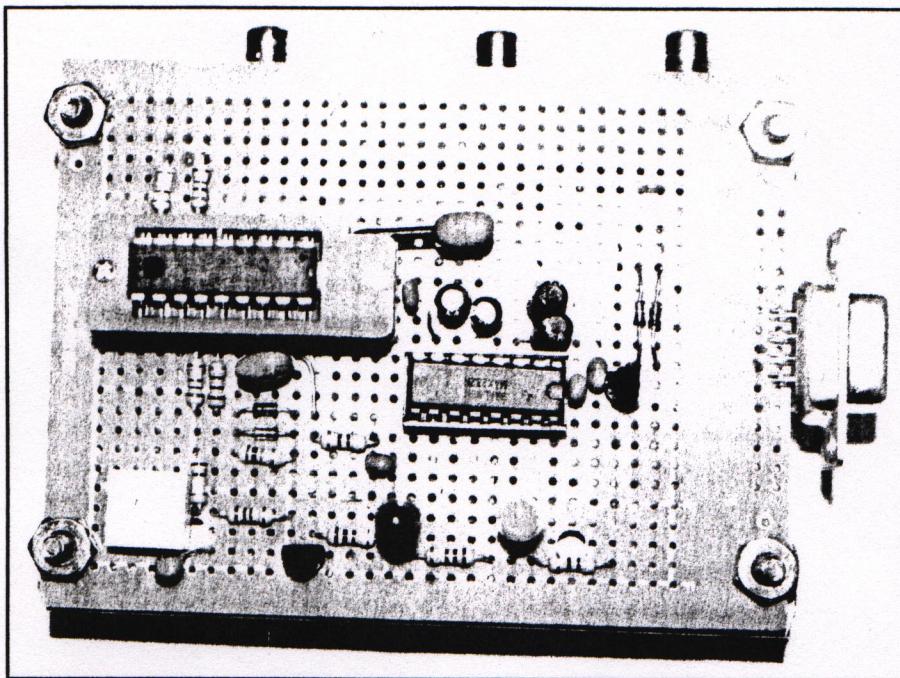


Figure 4—Breadboard style unit.

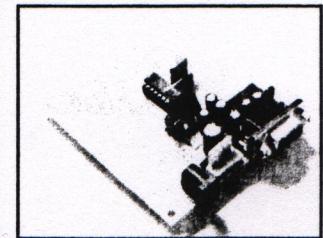


Figure 5—Olimex prototype board.

Except as indicated, decimal values of capacitance are in microfarads (μF); others are in picofarads (pF); resistances are in ohms; $k = 1,000$.
n.c. = No connection

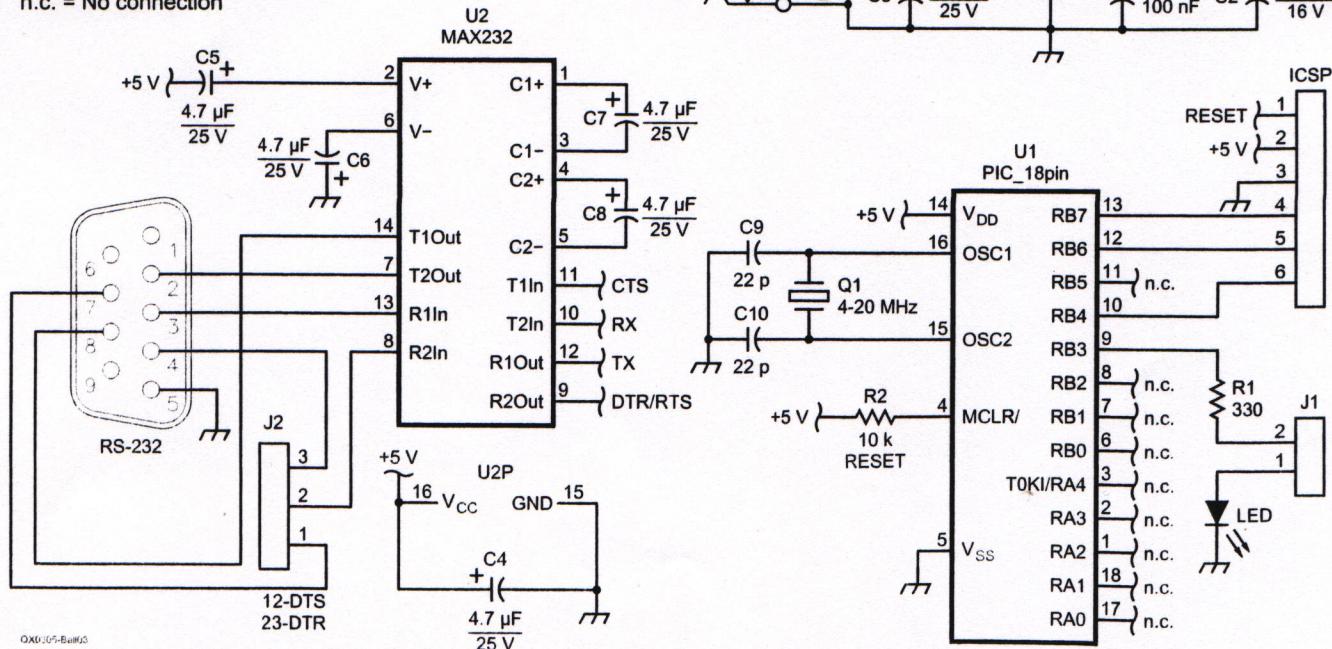


Figure 6—Schematic of the Olimex board.

Detailed Instructions for the Olimex Board

When you receive your Olimex board, it is a good idea to check it out before you add any wiring. I recommend installing the Boot Loader program which is supplied with a small checkout program that will flash the leads on the board.

Initial Checkout

1. Remove the supplied PIC16F88 and program it with the supplied Bloader Program. This should be the only time you will need an external programmer for this project. REMEMBER TO DISABLE THE MASTER CLEAR WHEN THE CHIP IS PROGRAMMED.
2. Reinstall the processor chip. With no serial port connected, power up the unit. The LEDs should alternately flash. If they do not, stop here and get a replacement board from your supplier.

Now go ahead and add the TNC wiring:

Serial connections

1. Remove J1
2. If GPS operation is desired, install wiring for GPS Receive Enable. Do not install jumper until set up is complete (see text).
3. Lead Labeled RX to Processor Pin 11
4. Lead Labeled TX to Processor Pin 8

TNC Receive Circuitry

1. Ground Pins 1 and 18 of Processor
2. Connect Processor Pins 2 and 10 to D1 Cathode
3. Connect R11 to 5V
4. Connect R10,D2, D3, R7 to Pin 17
5. Ground other lead of D2,D3, and R7
6. Connect R10 other end to C6

TX Receive Circuitry

1. Connect one end of R1,R2,R3, and R4 to Processor Pins 13,12,9, & 7 respectively.
2. Connect other end of R1,R2,R3, and R4 to R6
3. Connect other end of R6 to R12
4. Ground other end of R12
5. Connect one end of C7 to R12
6. Connect other end of C7 to output and R8
7. Connect Pin 6 of Processor to D5 Anode and other end of R8
8. Connect other end of D5 cathode to R5
9. Connect other end of D5 to ground
10. Connect R8 other end to Q1 Base
11. Connect Q1 Emitter to Ground
12. Connect Q1 Collector to R9 and PTT out
13. Connect other end of R9 to C7

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Wiring is Complete. Now install the TNC software

1. Remove Power from the TNC
2. Connect a serial cable from the serial port to your PC or terminal. Start up the terminal program.
3. Remove J4
4. Install the PC portion of the loader program called Screamer
5. Start up Screamer. Hit the program button and supply the file name for the TNC software. Screamer should say "Waiting for Broadcast"
6. Power up the TNC, Screamer should program the chip and indicate successful completion
7. Exit Screamer program on PC and Start Terminal Program
8. The command interpreter should be running on your TNC. Set your options, connect the radio, and your on the air.

Figure 7—Step-by-step wiring instructions for the Olimex board.