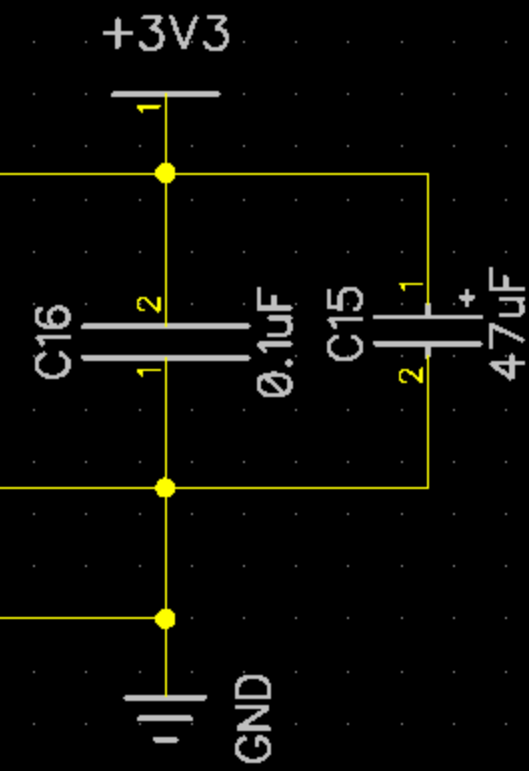


Port 0

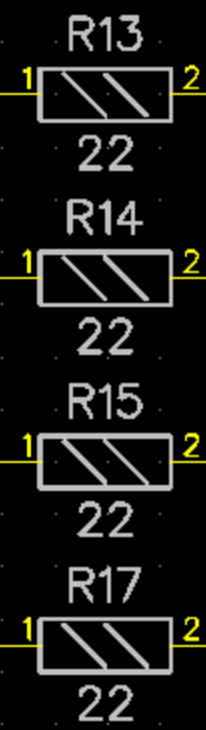
+3V3



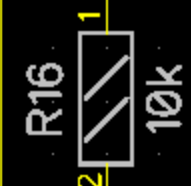
SDIO_D0
SDIO_D1
SDIO_D2
SDIO_D3
SDIO_CMD
SDIO_CLK



SWO
NRST
SWDIO
SWCLK
BOOT0



+3V3



Open to program

