



Pin 1 RED Red Signal
Pin 2 GREEN Green Signal
Pin 3 BLUE Blue Signal
Pin 4 ID2/RES Previously Monitor ID bit 2, reserved since E-DDC
Pin 5 GND Ground (HSync)
Pin 6 RED_RTN Red Return
Pin 7 GREEN_RTN Green Return
Pin 8 BLUE_RTN Blue Return
Pin 9 KEY/PWR Previously switch, now +5V DC, supplies power to EEPROM EDID chip in some monitors
Pin 10 GND Ground (VSync, DDC)
Pin 11 ID0/RES Previously Monitor ID bit 0, reserved since E-DDC
Pin 12 ID1/SDA Previously Monitor ID bit 1, I²C data from DC2
Pin 13 HSync Horizontal Sync
Pin 14 VSync Vertical Sync
Pin 15 ID3/SCL Previously Monitor ID bit 3, I²C clock since DDC2