AKBAR BAGHBANI

DIGITAL SIGNAL PROCESSING ENGINEER & FPGA DESIGNER

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March 10, 2025

Hiring Manager's Ellisys
912 Company Address
City, State Zip Code

Subject: Application for DSP Design Engineer Position

Dear Hiring Manager,

I am writing to express my keen interest in the DSP Design Engineer position at Ellisys. With around 20 years of experience in digital signal processing and FPGA design, I believe my expertise aligns closely with the requirements of this role. Throughout my career, I have specialized in digital down conversion DDS, filtering, and modulation/de-modulation techniques in DSP, developing and implementing complex DSP blocks for FPGA. Additionally, I have extensive experience in HDL programming and have contributed significantly to projects involving gigabit protocols such as PCIe and USB3, designing analyzers and exercisers for these high-speed interfaces.

What excites me about this opportunity is the strong alignment between my skills and Ellisys focus areas. I am highly self-motivated and quick to adapt, allowing me to efficiently learn and integrate new technologies as needed. My past experience as a team leader, where I managed a group of designers and developers, has also equipped me with strong leadership and collaboration skills.

I am eager to bring my expertise and passion to Ellisys and contribute meaningfully to your innovative projects. I would welcome the opportunity to discuss how my skills and experience can support your team's success.

Thank you for your time and consideration. I look forward to hearing from you.

Sincerely,

Akbar Baghbani