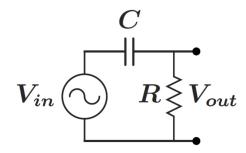
MAE 3120

HW 01

Due 02/06

1 1^{st} order high pass filter

The following RC circuit is a passive first-order high-pass filter.



(a) Using complex definiation of impedance prove that (show your steps):

$$\frac{V_{out}}{V_{in}} = \frac{i\omega R\dot{C}}{1 + i\omega RC}$$

(b) Introduce the cutoff frequency, f_c :

$$f_c = \frac{1}{2\pi RC}$$

and show that the gain, G, is:

$$G = \frac{|V_{out}|}{|V_{in}|} = \frac{1}{\sqrt{1 + \left(\frac{f_c}{f}\right)^2}}$$

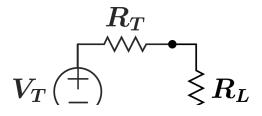
(c) Plot the Bode diagram of this filter:

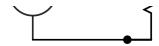
Note the phase is given as:

$$\phi = \arctan\left(\frac{f_c}{f}\right)$$

(d) Does the output lead or lag the input.

2 Optimum power transfer





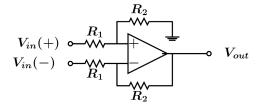
(a) Show that the power in the load, P_L is:

$$P_L = V_T \left(\frac{R_L}{R_L + R_T} \right) \left(\frac{V_T}{R_L + R_T} \right)$$

(b) Show that the load is optimum when:

$$R_L = R_T$$

3 Differencing Amplifier



Show that:

$$V_{out} = \left(\frac{R_2}{R_1}\right) \left(V_{in(+)} - V_{in(-)}\right)$$

4 Design of amplifier stages

One wishes to measure a pressure pulse with a very sensitive load cell. Unfortunately, the voltage output of the pressure transducer is ± 5 mV and the smallest range of our Data Acquisition (DAQ) board is ± 1 V with a desired bandwidth of 50 kHz. The GBP of your amplifier stage is 1 MHz.

- (a) To utilize the most of our DAQ board dynamic range, and hence increase our resolution, we wish to amplify our signal. What should be the gain to make the most of your DAQ system?
- (b) How many amplifier stages would you need to successfully amplify your signal?
- (c) Because experiments are never easy, we have high-power electrical components just next to the DAQ system, which create a lot of electromagnetic noise. Which types of amplifier stages will you use?
- (d) Present a diagram of your amplifier chain.

In []: ▶

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