

TamaGo

Bare metal Go for ARM/RISC-V SoCs

Secure embedded unikernels
with drastically reduced attack surface

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Information Security Engineer and Researcher

Founder: **INVERSE PATH** (acquired in 2017)

Head of Hardware Security:  

Maker of the USB armory



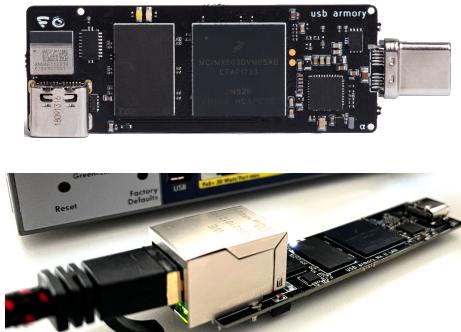
Speaker at too many conferences...

Background focused on security auditing and engineering on safety critical systems in the automotive, avionics, industrial domains.

Andrea Barisani



Motivation: USB armory firmware

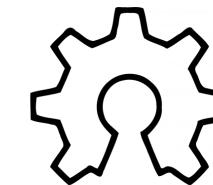


The USB armory is a tiny, but powerful, embedded platform for personal security applications.

Designed to fit in a pocket, laptops, PCs, server and networks (LAN model with PoE).

The USB armory targets the following primary applications:

- Encrypted storage solutions
- Hardware Security Module (HSM)
- Enhanced smart cards
- Electronic vaults (e.g. cryptocurrency wallets) and key escrow services
- Authentication, provisioning, licensing tokens
- USB firewall



open hardware



In an ideal world **you should be free to choose the programming language you prefer.**

In an ideal world **all compilers would generate machine code with the same efficiency.**

However in real world lower specs heavily dictate language choices:

Microcontroller (MCU) firmware == unsafe¹ low level languages (C)



Examples: cryptographic tokens, cryptocurrency wallets, hardware data diodes, lower specs IoT and “smart” appliances.

¹ **Pro tip:** certification does not matter.



In an ideal world using **higher level languages should not entail complex dependencies**.

In an ideal world **higher level languages should reduce complexity**.

Complexity should be reduced for the entire environment, not just being shifted away.

However in real world higher specs heavily dictate OS requirements:

System-on-Chip (SoC) firmware == complex OS + safe (or unsafe¹) languages



Examples: TEE applets, infotainment units, avionics gateways, home routers,
higher specs IoT and “smart” appliances.

¹ Privileged C-based apps running under Linux to “parse stuff” are very common, like your car infotainment/parking ECU.



When security matters software and hardware optimizations matter less.

This means that less constrained hardware (e.g. SoCs in favor of MCUs) and higher level code are perfectly acceptable.

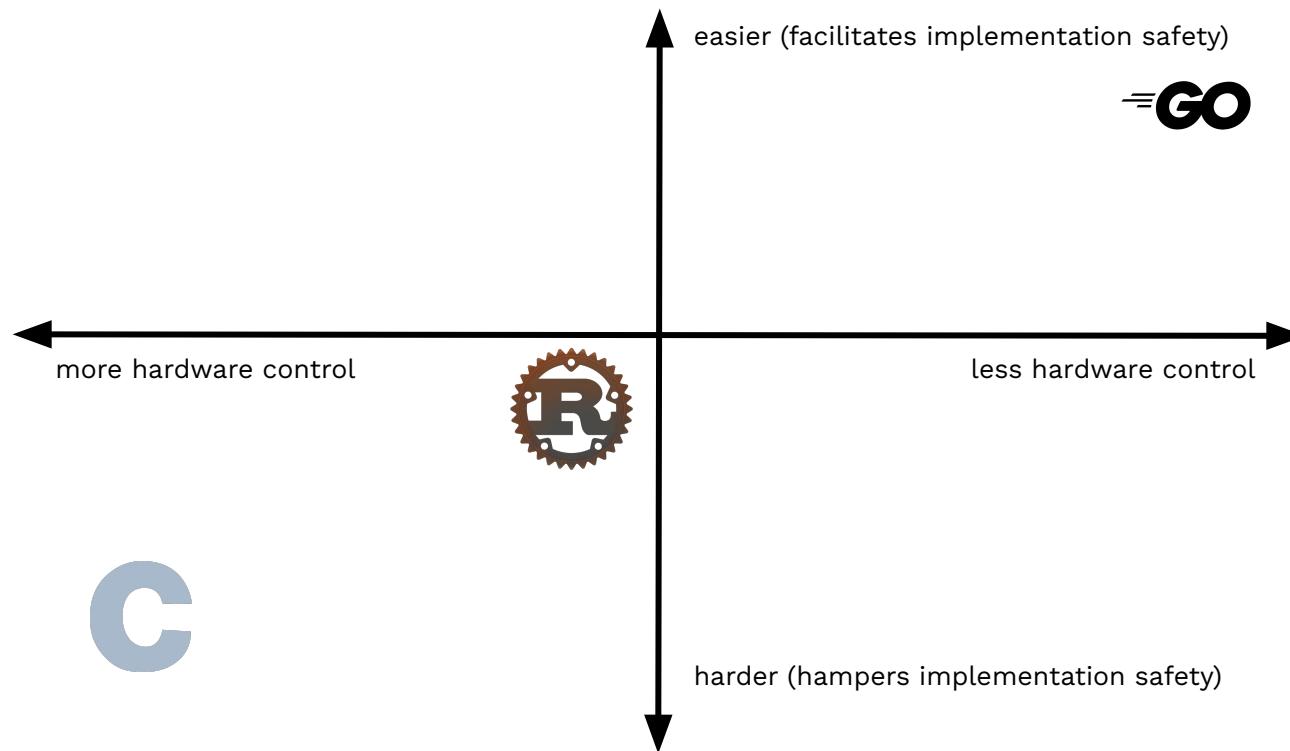
However high level programming typically entails several layers (e.g. OS, libraries) to serve runtime execution.

TamaGo spawns from the desire of **reducing the attack surface** of embedded systems firmware by **removing any runtime dependency on C code and inherently complex Operating Systems**.

In other words we want to **avoid shifting complexity around** and run a **higher level language**, such as Go in our effort, **directly on the bare metal**.



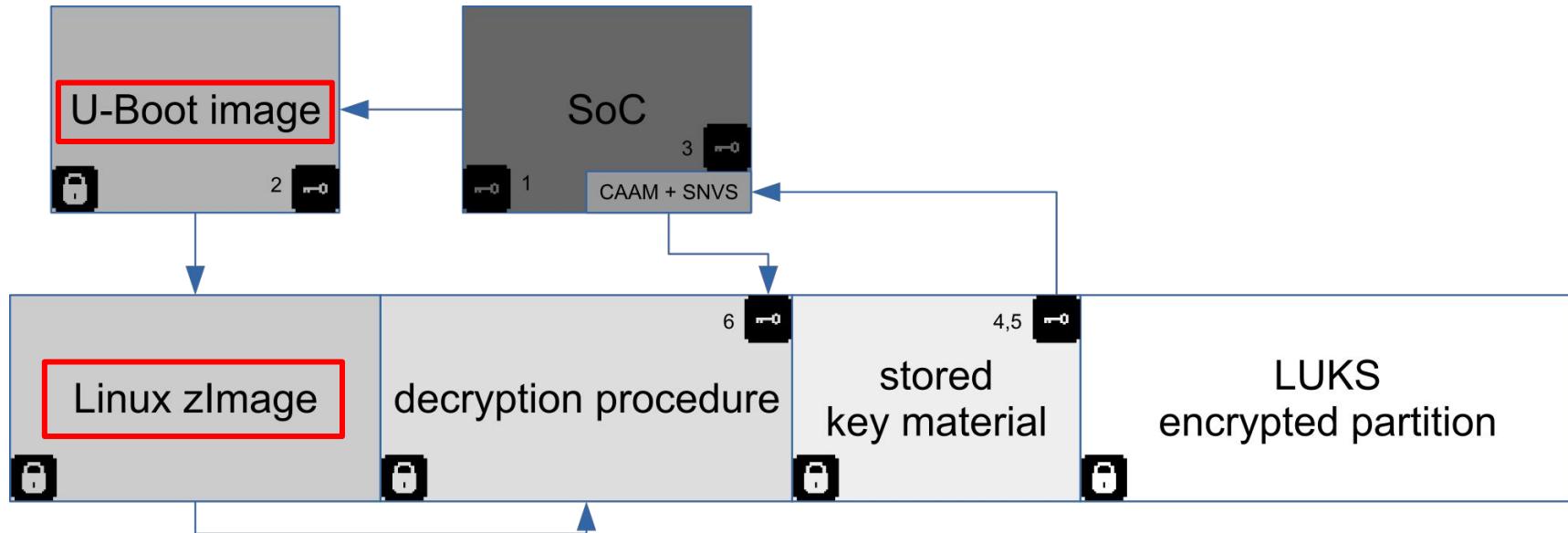
Speed vs Safety



Disclaimer: chart presented for discussion and not to claim that language X is better than language Y, also scale is subjective.



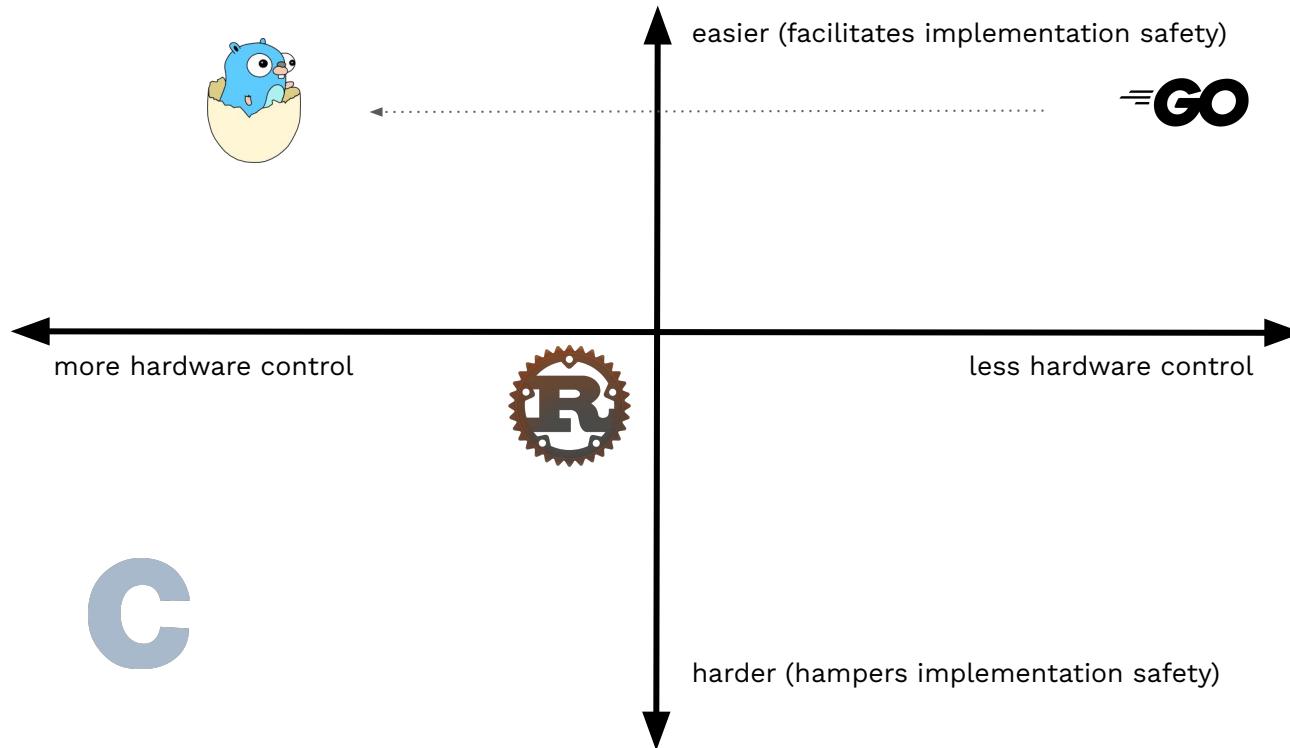
Reducing the attack surface



Typical secure booted firmware with authentication and confidentiality on an NXP i.M6UL.



Speed vs Safety



Disclaimer: chart presented for discussion and not to claim that language X is better than language Y, also scale is subjective.



Unikernels¹ are a single address space image to execute a “library operating system”, typically running under bare metal.

The focus is reducing the attack surface, carrying only strictly necessary code.

“True” unikernels are mostly unicorns, as a good chunk of available ones do not fit in this category and represent “fat” unikernels running under hypervisors and/or other (mini) OSes. And just shift around complexity (e.g. the app is PID 1).

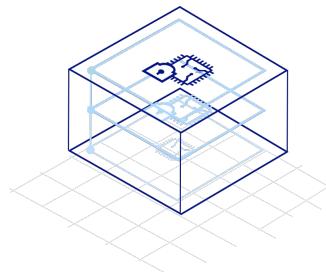
Apart from some exceptions there is always still a lot of C/dependencies involved in the underlying OS, drivers or hypervisor.

Running or importing *BSD kernels

Rump kernels (NetBSD based)
OSv (re-uses code from FreeBSD)

Running under hypervisor and 3rd party kernel

MirageOS (Solo5)
ClickOS (MiniOS)



Running under hypervisor

Nanos (Xen/KVM/Qemu) HalVM (Haskell, Xen)
LING (Erlang, Xen) RustyHermit (KVM)

Bare metal

GRISP (Erlang)
IncludeOS

¹ <https://en.wikipedia.org/wiki/Unikernel>



Unikernel security

From a security standpoint leveraging on Unikernels (whatever the kind) to run multiple applications or an individual C applications is not ideal¹.

Having an industry standard OS is necessary to support required security measures which otherwise are not present or rather primitive on most Unikernels.

Again, we want to **kill C** from the entire environment while keeping code efficiency, developing drivers having “only” to worry about interpreting reference manuals.

Unlike most unikernel projects we focus on **small embedded systems**, not the cloud.

We chose **Go** for its shallow learning curve, productivity, strong cryptographic library and standard library.

Languages like Rust have already proven they role in bare metal world, Go on the other hand needs to ... and it really can!

¹ <https://research.nccgroup.com/2019/02/04/assessing-unikernel-security/>



TamaGo in a nutshell

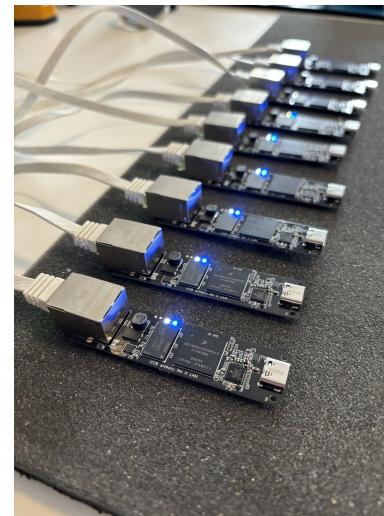
TamaGo is made of two main components.

- A **minimally¹** patched Go distribution to enable GOOS=tamago support, which provides freestanding execution on GOARCH=arm and GOARCH=riscv64 bare metal.
- A set of packages² to provide board support (e.g. hardware initialization and drivers).

TamaGo currently provides drivers for SoC families
NXP i.MX6UL (USB armory Mk II), BCM2835
(Raspberry Pi Zero, Pi 1, Pi 2) and SiFive FU540.

On the i.MX6UL we target development of security applications, TamaGo is fully integrated with our existing open source tooling for i.MX6 Secure Boot (HAB) image signing.

TamaGo also provides full hardware initialization removing the need for intermediate bootloaders.



¹ <https://github.com/usbarmory/tamago-go>

² <https://github.com/usbarmory/tamago>



Similar efforts

Past/existing efforts

(all these projects greatly supported us in proving feasibility and identify TamaGo unique approach, diversity is good)

Biscuit (inactive) - <https://github.com/mit-pdos/biscuit>

Go kernel for non-Go software underneath, larger scope, needs two C bootloaders,
hijacks GOOS=linux, only for GOARCH=amd64, redo memory allocation and threading.

G.E.R.T (inactive) - <https://github.com/ycoroneos/G.E.R.T>

ARM adaptation of Biscuit but without non-Go software support, needs two C bootloaders,
hijacks GOOS=linux for GOARCH=arm, redo memory allocation and threading.

AtmanOS (inactive) - <https://github.com/atmanos>

Similar to TamaGo but targets the Xen hypervisor, adds GOOS=atman but with limited runtime support.

TinyGo (active) - <https://github.com/tinygo-org>

LLVM based compiler (not original one) aimed at MCUs and minimal footprint, does not
support the entire runtime and Go language support differs from standard Go.

Embedded Go (inactive) - <https://github.com/embeddedgo>

Similar to TamaGo but targets ARMv7-M/ARMv8-M (w/ Thumb2) adding new support for it,
as not native to Go. Adds GOOS=noos GOARCH=thumb .

Egg OS (inactive) - <https://github.com/icexin/eggos>

Targets x86, uses native compiler and wraps GOOS=linux syscalls back to Go.



TamaGo not only proves that it is possible to have a bare metal Go runtime, but does so with **clean and minimal modifications against the original Go distribution²**.

Much of the effort has been placed to understand whether Go bare metal support can be achieved without complex re-implementation of memory allocation, threading, ASM/C OS primitives that would “pollute” the Go runtime to unacceptable levels.

Less is more. Complexity is the enemy of verifiability.

The acceptance of this (and similar) efforts hinges on maintainability, ease of review, clarity, simplicity and **trust**.

- ★ Designed to achieve upstream inclusion and with commitment to always sync to latest Go release.
- ★ ~5000 LOC of changes against Go distribution with clean separation from other GOOS support.
- ★ Strong emphasis on code reuse from existing architectures of standard Go runtime, see [Internals¹](#).
- ★ Requires only one import (“library OS”) on the target Go application.
- ★ Supports unencumbered Go applications with nearly full runtime availability.
- ★ In addition to the compiler, aims to provide a complete set of peripheral drivers for SoCs.

¹ <https://github.com/usbarmory/tamago/wiki/Internals>

² Which by the way is self-hosted and has reproducible builds.



Go distribution modifications¹

Glue code - patches to adds GOOS=tamago to the list of supported architectures and required stubs for unsupported operations. All changes are benign (no logic/function):

```
// +build aix darwin dragonfly freebsd js,wasm linux nacl netbsd openbsd solaris tamago
```

Re-used code - patches that clone original Go runtime functionality from an existing architecture to (e.g. js, wasip1) GOOS=tamago, either unmodified or with minimal changes:

- plan9 memory allocation is re-used with 2 LOC changed (brk vs simple pointer)
- js, wasm locking is re-used identically (with JS VM hooks removed)
- nacl in-memory filesystem is re-used (raw SD/MMC access implemented in imx6)

New code - basic syscall and memory layout support:

rt0_tamago_arm.s
sys_tamago_arm.s
os_tamago_arm.go

rt0_tamago_riscv64.s
sys_tamago_riscv64.go
os_tamago_riscv64.go

<https://github.com/golang/go/compare/go1.23.0...usbarmory:tamago1.23.0>

¹ As of tamago1.23.0 against go1.23.0

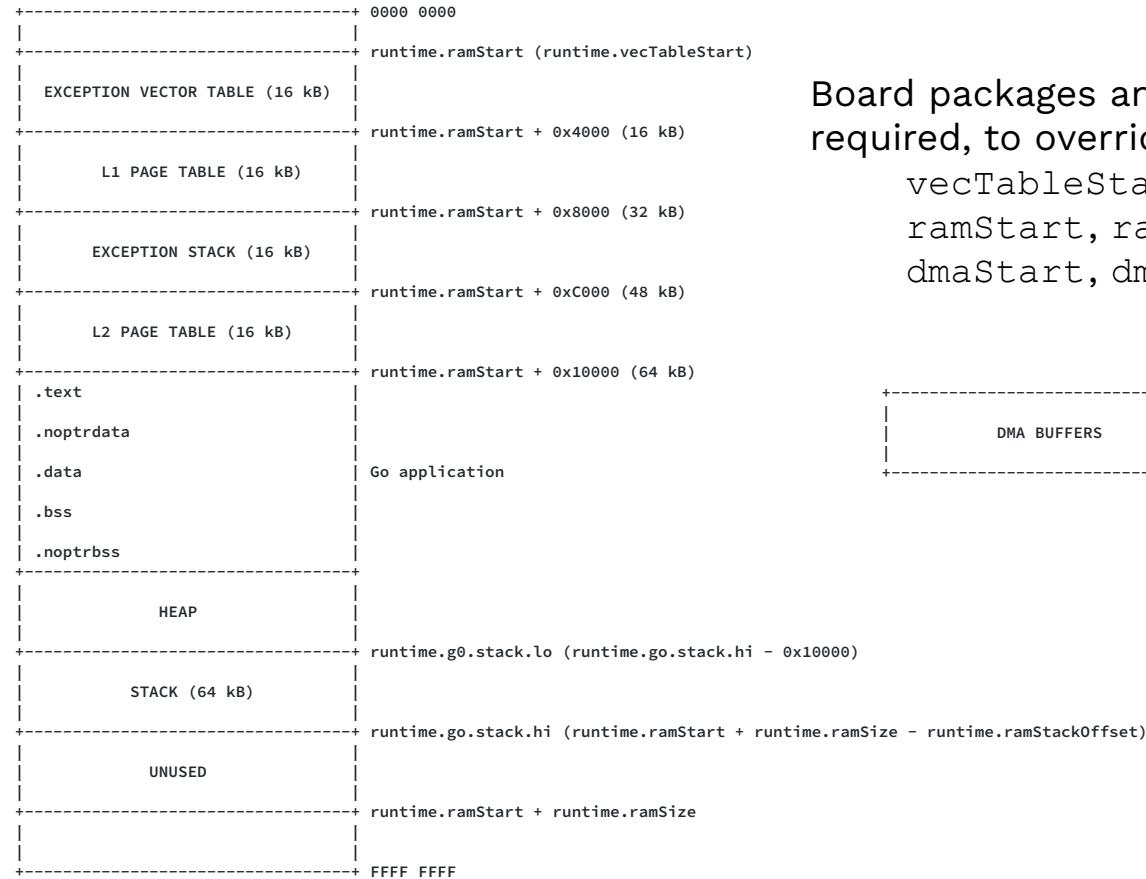


```
$ git diff go1.23.0 tamago1.23.0 --stat --stat-width "$(tput cols)" --color=always -- :(exclude)*tamago_test.go" | sort -t ' '| -n -k2 "$@"
```

315 files changed, 6468 insertions(+), 325 deletions(-)

src/cmd/go/internal/imports/build.go	1 +
...	
src/runtime/proc.go	14 +-
src/syscall/asm_tamago_arm.s	14 ++
src/syscall/asm_tamago_riscv64.s	14 ++
src/internal/syscall/unix/nonblocking_tamago.go	15 ++
src/os/sys_tamago.go	15 ++
src/os/pipe_tamago.go	16 ++
src/runtime/malloc.go	16 +-
src/runtime/sigqueue_tamago.go	16 ++
src/time/zoneinfo_tamago.go	17 ++
src/os/signal/signal_tamago.go	23 +++
src/runtime/mem_tamago.go	24 +++
src/syscall/zsyscall_tamago_arm.go	25 +++
src/syscall/zsyscall_tamago_riscv64.go	25 +++
src/internal/goos/zgoos_tamago.go	27 +++
src/crypto/rand_tamago.go	29 +++
src/os/exec/lp_tamago.go	29 +++
src/os/dirent_tamago.go	30 +++
src/os/user/lookup_tamago.go	35 ++++
src/net/sockopt_tamago.go	37 +++
src/runtime/rt0_tamago_riscv64.s	42 +++
src/internal/syscall/unix/net_tamago.go	44 +++
src/os/stat_tamago.go	51 +++++
src/time/sys_tamago.go	54 +++++
src/testing/testing_tamago.go	55 +++++
src/runtime/rt0_tamago_arm.s	56 +++++
src/testing/testing_tamago.s	64 ++++++
src/testing/run_example_tamago.go	76 +++++++
src/runtime/sys_tamago_riscv64.s	122 ++++++*****
src/runtime/lock_tamago.go	169 ++++++*****
src/net/net_tamago.go	210 ++++++*****
src/runtime/sys_tamago_arm.s	220 ++++++*****
src/runtime/os_tamago_riscv64.go	226 ++++++*****
src/runtime/os_tamago_arm.go	251 ++++++*****
src/syscall/fd_tamago.go	261 ++++++*****
src/syscall/syscall_tamago.go	328 ++++++*****
src/syscall/tables_tamago.go	494 ++++++*****
src/syscall/fs_tamago.go	872 ++++++*****
src/syscall/net_tamago.go	954 ++++++*****

TamaGo memory layout



Board packages and applications are free, if required, to override:

vecTableStart,
ramStart, ramSize,
dmaStart, dmaSize



Go runtime support

```
// the following variables must be provided externally
var ramStart uint32
var ramStackOffset uint32
var ramSize uint32

// the following functions must be provided externally
func hwinit()
func printk(byte)
func getRandomData([]byte)
func initRNG()
func nanotime1() int64
```

MMU initialization and exception handling are all performed outside the Go runtime in tamago architecture (e.g. arm) package.

This means low-level APIs (e.g. TrustZone) can all be implemented as a regular package.

The Go runtime modification is architecture independent for the most part.

Example of separation between Go runtime, SoC and board packages with pre-defined hooks using go:linkname.

```
package imx6ul

//go:linkname ramStart runtime.ramStart
var ramStart uint32 = 0x80000000

// ramSize defined in board package
//go:linkname ramStackOffset runtime.ramStackOffset
var ramStackOffset uint32 = 0x100
```

```
package usbarmory

//go:linkname ramSize runtime.ramSize
var ramSize uint32 = 0x20000000 // 512 MB

//go:linkname printk runtime.printk
func printk(c byte) {
    imx6ul.UART2.Write(c)
}
```



Go runtime support

```
os_tamago_arm.go (Go runtime)
//go:linkname syscall_now syscall.now
func syscall_now() (sec int64, nsec int32) {
    sec, nsec, _ = time_now()
    return
}

imx6.go (imx6 package)
//go:linkname nanotime1 runtime.nanotime1
func nanotime1() int64 {
    return int64(ARM.TimerFn() * ARM.TimerMultiplier)
}

timer.s (arm package)

// func read_gtc() int64
TEXT .read_gtc(SB,$0-8
    // Cortex™-A9 MPCore® Technical Reference Manual
    // 4.4.1 Global Timer Counter Registers, 0x00 and 0x04
    // p214, Table 2-1, ARM MP Global timer, IMX6DQRM
    MOVW $0x00a00204, R1
    MOVW $0x00a00200, R2
read:
    MOVW      (R1), R3
    MOVW      (R2), R4
    MOVW      (R1), R5
    CMP       R5, R3
    BNE      read

    MOVW      R3, ret_hi+4(FP)
    MOVW      R4, ret_lo+0(FP)

    RET
```

A small set of low-level functions are integrated directly with Go Assembly.

This follows existing patterns in the Go runtime.

In the example ARM Generic Timers (ARM Cortex-A7) are used to support ticks and time related functions.

Overall initialization code accounts for less than 500 lines of code.



Go runtime support

```
//go:linkname syscall
func syscall(number, a1, a2, a3 uintptr) (r1, r2, err uintptr) {
    switch number {
        case 1: // SYS_WRITE
            r1 := write(a1, unsafe.Pointer(a2), int32(a3))
            return uintptr(r1), 0, 0
        default:
            throw("unexpected syscall")
    }

    return
}

//go:nosplit
func writel(fd uintptr, buf unsafe.Pointer, count int32) int32 {
    if fd != 1 && fd != 2 {
        throw("unexpected fd, only stdout/stderr are supported")
    }

    c := uintptr(count)

    for i := uintptr(0); i < c; i++ {
        p := (*byte)(unsafe.Pointer(uintptr(buf) + i))
        printk(*p)
    }

    return int32(c)
}
```

Only the `write` syscall is required for the overwhelming majority of basic runtime support.

As shown before, `printk` is provided by the application to define the standard output writing function (e.g. UART).

```
imx6_clk: changing ARM core frequency to 900 MHz
imx6_clk: changing ARM core operating point to 575000 uV
imx6_clk: 450000 uV -> 575000 uV
imx6_clk: waiting for PLL lock
imx6_clk: 396 MHz -> 900 MHz
imx6_soc: i.MX6ULL (0x65, 0.1) @ freq:900 MHz - native:true
```



Go low level access

```

func (hw *BEE) Init() {
    hw.mu.Lock()
    defer hw.mu.Unlock()

    hw.ctrl = hw.Base + BEE_CTRL
    hw.addr0 = hw.Base + BEE_ADDR_OFFSET0
    hw.addr1 = hw.Base + BEE_ADDR_OFFSET1
    hw.key = hw.Base + BEE_AES_KEY0_W0
    hw.nonce = hw.Base + BEE_AES_KEY1_W0

    // enable clock
    reg.Set(hw.ctrl, CTRL_CLK_EN)
    // disable reset
    reg.Set(hw.ctrl, CTRL_SFTRST_N)

    // disable
    reg.Clear(hw.ctrl, CTRL_BEE_ENABLE)
}

func (hw *BEE) generateKey() (err error) {
    // avoid key exposure to external RAM
    key, err := dma.NewRegion(uint(hw.key), aes.BlockSize, false)

    if err != nil {
        return
    }

    addr, buf := key.Reserve(aes.BlockSize, 0)

    if n, err := rand.Read(buf); n != aes.BlockSize || err != nil {
        return errors.New("could not set random key")
    }

    if addr != uint(hw.key) {
        return errors.New("invalid key address")
    }

    return
}

```

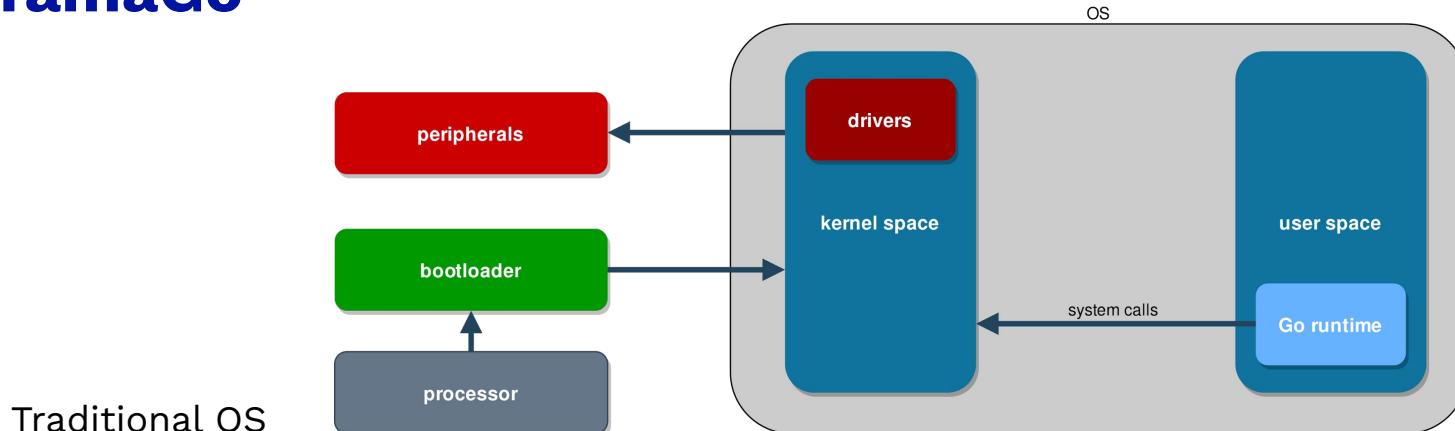
Example: BEE initialization

Go's `unsafe` can be easily identified to spot areas that require care (e.g. pointer arithmetic), it is currently used only in register and DMA memory manipulation primitives.

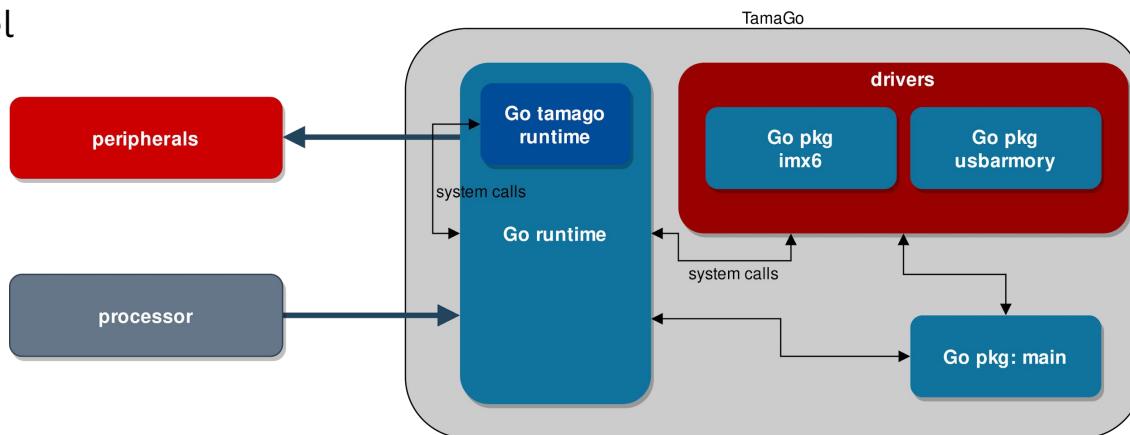
There are overall only 3 occurrences of `unsafe` used in `dma` and `reg` packages.

Applications are never required to use any `unsafe` function.





TamaGo unikernel



Developing, building and running

The full Go runtime is supported¹ without any specific changes required on the application side (Rust on bare metal², for comparison, requires `#![no_std] pragma`).

```
package main

import (
    _ "github.com/usbarmory/tamago/board/usbarmory/mk2"
)

func main() {
    // your code
}
```

```
GO_EXTLINK_ENABLED=0 CGO_ENABLED=0 GOOS=tamago GOARM=7 GOARCH=arm \
${TAMAGO} build -ldflags "-T 0x80010000 -E _rt0_arm_tamago -R 0x1000"
```

All Go ecosystem features in terms of build reproducibility, dependency management, profiling, debugging, remain intact.

Firmware can be compiled just as easily on Linux, Windows, macOS.

1. The application requires a single import for the board package to enable necessary initializations.
2. Go code can be written with very few limitations and the SoC package exposes driver APIs.
3. go build can be used as usual (reproducible builds!) with few linker flags to define entry point.
4. The SoC package supports native loading (no bootloader required!).

¹ <https://github.com/usbarmory/tamago/wiki/Compatibility>

² <https://rust-embedded.github.io/book/intro/no-std.html>



Go distribution testing for GOOS=tamago

```
$ cd tamago-go
$ GO_TEST_TIMEOUT_SCALE=8 GO_BUILDER_NAME="tamago" GOOS=tamago GOARCH=arm ./bin/go tool dist test

##### Testing packages.
ok    archive/tar      1.465s
ok    archive/zip     156.222s
ok    bufio      0.683s
ok    bytes      88.814s
ok    cmp       0.106s
ok    compress/bzip2  0.299s
ok    compress/lzw   0.501s
ok    compress/zlib   3.909s
ok    container/heap  0.116s
ok    container/list  0.103s
ok    container/ring  0.099s
ok    context      0.195s
ok    crypto       0.105s
ok    crypto/aes    0.175s
ok    crypto/cipher  0.124s
ok    crypto/des    0.149s
ok    crypto/dsa    814.124s
ok    crypto/ecdh    3.932s
ok    crypto/ecdsa   79.698s
ok    crypto/ed25519  4.154s
ok    crypto/elliptic 14.773s
ok    crypto/hmac    0.218s
...
ok    cmd/internal/src    0.138s
ok    cmd/internal/test2json 0.115s
ok    cmd/link      0.216s
ok    cmd/link/internal/benchmark 0.161s
ok    cmd/link/internal/ld    0.346s
ok    cmd/link/internal/loader 0.268s
ok    cmd/nm       0.254s
ok    cmd/objdump   0.249s
ok    cmd/pack      0.224s
ok    cmd/pprof      0.321s
ok    cmd/relnote   0.164s
ok    cmd/trace      0.257s
ok    cmd/vet      0.290s
```

Demonstrates importing, execution and test compliance for the entire Go stdlib.

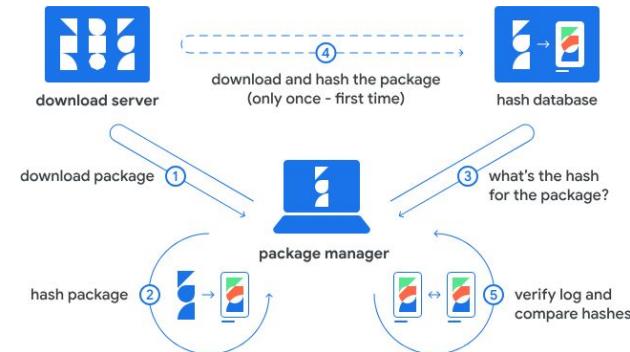
Runs under Linux (native ARM or using qemu-arm via binfmt_misc) bridging required external functions with Linux syscalls.

In-memory filesystem and networking support testdata access and network tests.

Reducing the attack surface

Block	LOCs	Driver support
ARM	900	CPU MMU, timer, exceptions, IRQ handling
BEE	130	OTF AES RAM encryption/decryption
CAAM	840	accel. AES/ECC/CMAC/SHA/TRNG, HUK derivation
DCP	450	accel. AES/SHA, HUK derivation
ENET	370	10/100-Mbps Ethernet driver, MII support
RNGB	80	True Random Number Generator
RPMB	230	Replay Protected Memory Block
RTIC	90	Run Time Integrity Checker
SNVS	180	tamper proof sensors
USB	1200	USB 2.0 in device mode
USDHC	1100	eMMC (HS200 speed) / SD (SDR104 speed)
MK2	680	USB armory Mk II board support package

The TamaGo firmware allows creation of true unikernels, incorporating in a single binary boot code, peripheral drivers, libraries and application code with minimal dependencies and all the benefits of the full Go ecosystem, including supply chain security, build reproducibility and debugging.



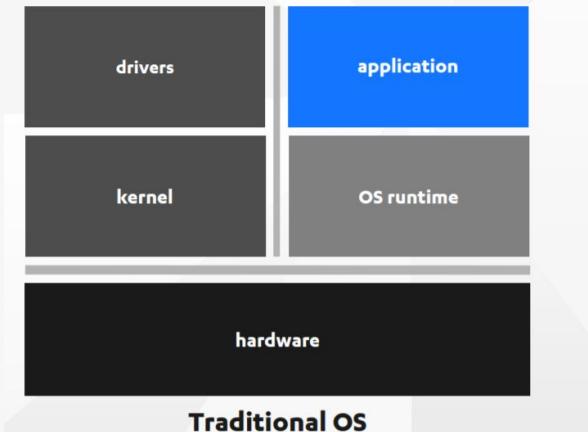
```

module github.com/usbarmory/GoTEE           go.mod
go 1.22.0

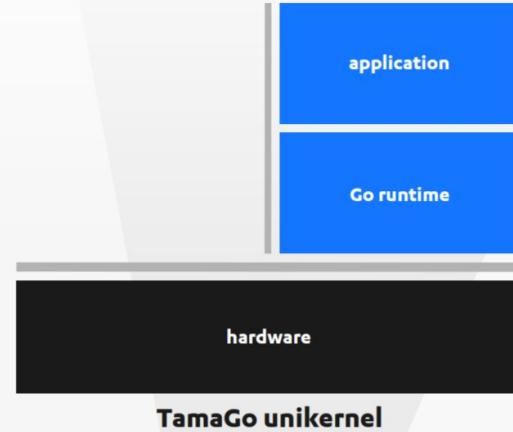
require github.com/usbarmory/tamago v0.0.0-20240104082716-7fdd041b36ef

```

Improving memory safety



Traditional OS



TamaGo unikernel



memory-unsafe programming language
memory-safe programming language

TamaGo allows a dramatic reduction of the attack surface by **removing any dependency on memory-unsafe languages** (e.g. C), Operating Systems and third party libraries.



programming language %



i.MX6ULZ driver: Data Co-Processor (DCP)

The DCP provides hardware accelerated crypto functions and use of the SoC unique OTPMK key for device unique encryption/decryption operations. The driver takes ~450 LOC.

```
workPacket := WorkPacket{}
workPacket.Control0 |= (1 << DCP_CTRL0 OTP_KEY)
...
workPacket.Control1 |= (AES128 << DCP_CTRL1_CIPHER_SELECT)
workPacket.Control1 |= (CBC << DCP_CTRL1_CIPHER_MODE)
workPacket.Control1 |= (UNIQUE_KEY << DCP_CTRL1_KEY_SELECT)

workPacket.BufferSize = uint32(len(diversifier))
workPacket.SourceBufferAddress = dma.Alloc(diversifier, 0)
defer dma.Free(workPacket.SourceBufferAddress)

workPacket.DestinationBufferAddress = dma.Alloc(key, 0)
defer dma.Free(workPacket.DestinationBufferAddress)

workPacket.PayloadPointer = dma.Alloc(iv, 0)
defer dma.Free(workPacket.PayloadPointer)

buf := new(bytes.Buffer)
binary.Write(buf, binary.LittleEndian, &workPacket)

pkt := dma.Alloc(buf.Bytes(), 0)
defer dma.Free(pkt)

reg.Write(HW_DCP_CH0CMDPTR, pkt)
reg.Set(HW_DCP_CHOSEMA, 0)
```

```
diversifier := []byte{0xde, 0xad, 0xbe, 0xef}
iv := make([]byte, aes.BlockSize)

key, err := imx6.DCP.DeriveKey(diversifier, iv)
```

```
-- i.mx6 dcp -----
imx6_dcp: derived test key 75f9022d5a867ad430440feec6611f0a
```

USB armory Mk II example DCP + SNVS run (w/ Secure Boot)

```
-- i.mx6 dcp -----
imx6_dcp: error, SNVS unavailable, not in trusted or secure state
```

USB armory Mk II example DCP + SNVS run (w/o Secure Boot)

Note that Go defined structs (such as `WorkPacket`) can be easily made C-compatible¹ if required.

On i.MX6UL a full CAAM driver is available.



i.MX6ULZ driver: Random Number Generator

The RNGB provides a hardware True Random Number Generator, useful to gather the initial seed on embedded systems without a battery backed RTC (and not much else¹). The driver takes ~80 LOC and is hooked as provider for crypto/rand.

```
var getRandomDataFn func([]byte)

//go:linkname getRandomData runtime.getRandomData
func getRandomData(b []byte) {
    getRandomDataFn(b)
}

func (hw *rngb) getRandomData(b []byte) {
    read := 0
    need := len(b)

    for read < need {
        if reg.Get(hw.status, HW_RNG_SR_ERR, 0x1) != 0 {
            panic("imx6_rng: panic\n")
        }

        if reg.Get(hw.status, HW_RNG_SR_FIFO_LVL, 0xf) > 0 {
            val := *hw fifo
            read = fill(b, read, val)
        }
    }
}
```

```
for i := 0; i < 10; i++ {
    rng := make([]byte, size)
    rand.Read(rng)
    fmt.Printf("%x\n", rng)
}
```

```
-- rng --
imx6_rng: self-test
imx6_rng: seeding
f90b00053a50b9edd42df027c982769d1a7d25445e31ce98486bd4a9676bef42
56bafecc32bf02fb9d09c2d8c607baa487e2283b6856486b42cdf954277d4d5
49fc0c03f8cbc45f7ae58ba71c0d561a91dbeae697d7bc511482697bf96b2f8
345db47ab3395272a9db9531f03160b3e1654b7e8b7267c1a3bc97206f3cb8c7
cb54154b105a2bd3938fdb99f1f2f5409c0be09dc5f64189f473ae905d264b25
275994ee93e0c779f3eb30d770eeabfc5ab0b8a5da68cc28a07dfbdb46a1e08
6215cc716b9ed577d3c6cd34d57f2dc3ed93c9b6aaedf120d68a4532393e1056
d691d7f93c57a54462f90ca76528beec4bda1a40220e5d5fb43986308f9013b
6ea213b27eb3e0e4243b3c872e7a07b7898d9f07ea205b8a50c30e62c7204602
4544d5dff957471972331532aaaf34eb5644bc430f854dd6593177640e07e4f00
```

USB armory Mk II example TRNG run

¹ https://media.ccc.de/v/32c3-7441-the_plain_simple_reality_of_entropy



i.MX6ULZ driver: USB

```
func buildDTD(n int, dir int, ioc bool, addr uint32, size int) (dtd *dTd) {  
    dtd = &dTD{  
  
        // interrupt on completion (ioc)  
        if ioc {  
            bits.Set(&dtd.Token, 15)  
        } else {  
            bits.Clear(&dtd.Token, 15)  
        }  
  
        // invalidate next pointer  
        dtd.Next = 0b1  
  
        // multiplier override (Mult0)  
        bits.SetN(&dtd.Token, 10, 0b11, 0)  
  
        // active status  
        bits.Set(&dtd.Token, 7)  
  
        // total bytes  
        bits.SetN(&dtd.Token, 16, 0xffff, uint32(size))  
  
        dtd._buf = addr  
        dtd._size = uint32(size)  
  
        for n := 0; n < DTD_PAGES; n++ {  
            dtd.Buffer[n] = dtd._buf + uint32(DTD_PAGE_SIZE*n)  
        }  
  
        buf := new(bytes.Buffer)  
        binary.Write(buf, binary.LittleEndian, dtd)  
        dtd._dtd = dma.Alloc(buf.Bytes()[0:DTD_SIZE], DTD_ALIGN)  
  
    }  
  
    return  
}
```

Example of Endpoint Transfer Descriptor (dTD) configuration.

A custom DMA allocator is used to copy structures on memory reserved for DMA operation, with required alignments.

```
addr = dma.Alloc(buf, align)  
defer dma.Free(addr)
```

Buffers can be also reserved by the application to spare re-allocation (automatic detection of slices already in DMA memory).

Using Go goroutines, channels, mutexes, interfaces freely in low level drivers is a delight!

All in ~1200 LOC !



i.MX6ULZ driver: USB networking



```
func configureEthernetDevice(device *usb.Device) {
    // Supported Language Code Zero: English
    device.SetLanguageCodes([]uint16{0x0409})

    // device descriptor
    device.Descriptor = &usb.DeviceDescriptor{}
    device.Descriptor.SetDefaults()
    device.Descriptor.DeviceClass = 0x2
    device.Descriptor.VendorId = 0x0525
    device.Descriptor.ProductId = 0xa4a2
    device.Descriptor.Device = 0x0001
    device.Descriptor.NumConfigurations = 1

    iManufacturer, _ := device.AddString(`TamaGo`)
    device.Descriptor.Manufacturer = iManufacturer

    iProduct, _ := device.AddString(`RNDIS/Ethernet Gadget`)
    device.Descriptor.Product = iProduct

    iSerial, _ := device.AddString(`0.1`)
    device.Descriptor.SerialNumber = iSerial

    // device qualifier
    device.Qualifier = &usb.DeviceQualifierDescriptor{}
    device.Qualifier.SetDefaults()
    device.Qualifier.DeviceClass = 2
    device.Qualifier.NumConfigurations = 2
}
```

```
func configureECM(device *usb.Device) {
...
    conf.Interfaces = append(conf.Interfaces, iface)

    ep1IN := &usb.EndpointDescriptor{}
    ep1IN.SetDefaults()
    ep1IN.EndpointAddress = 0x81
    ep1IN.Attributes = 2
    ep1IN.MaxPacketSize = 512
    ep1IN.Function = ECMTx

    iface.Endpoints = append(iface.Endpoints, ep1IN)

    ep1OUT := &usb.EndpointDescriptor{}
    ep1OUT.SetDefaults()
    ep1OUT.EndpointAddress = 0x01
    ep1OUT.Attributes = 2
    ep1OUT.MaxPacketSize = 512
    ep1OUT.Function = ECMRx

    iface.Endpoints = append(iface.Endpoints, ep1OUT)
}
```

```
func ECMTx(_ []byte, lastErr error) ([]byte) {
    // gvisor tcpip channel link
    pkt := <-link.C:
...
    // Ethernet frame header
    in = append(in, hostMAC...)
    in = append(in, deviceMAC...)
    in = append(in, proto...)
    // packet header
    in = append(in, hdr...)
    // payload
    in = append(in, payload...)
}

return
}

func ECMRx(out []byte, lastErr error) ([]byte) {
...
    pkt := tcpip.PacketBuffer{
        LinkHeader: hdr,
        Data: payload,
    }

    // gvisor tcpip channel link
    link.InjectInbound(proto, pkt)

    return
}
```

Example USB Ethernet (CDC ECM) driver integrated with Google netstack (gvisor.dev/gvisor/pkg/tcpip) for pure Go networking.

Developed in less than 2 hours and ~400 LOC.



i.MX6ULL driver: Ethernet networking



```
func (ring *bufferDescriptorRing) init(rx bool, n int) uint32 {
    ring.size = n
    ring.bds = make([]*bufferDescriptor, n)

    descSize := len(&bufferDescriptor{}).Bytes()
    ptr, desc := dma.Reserve(n*descSize, bufferAlign)

    dataSize := MTU + (bufferAlign - (MTU % bufferAlign))
    addr, data := dma.Reserve(n*dataSize, bufferAlign)

    for i := 0; i < n; i++ {
        off := dataSize * i

        bd := &bufferDescriptor{
            Addr: uint32(addr) + uint32(off),
            data: data[off : off+dataSize],
        }

        ...

        off = descSize * i
        bd.desc = desc[off : off+descSize]
        copy(bd.desc, bd.Bytes())

        ring.bds[i] = bd
    }

    return uint32(ptr)
}
```

```
func (hw *ENET) Rx() ([]byte) {
    hw.Lock()
    defer hw.Unlock()

    buf = hw.rx.pop()
    reg.Set(hw.rdar, RDAR_ACTIVE)

    return
}

func (hw *ENET) Tx(buf []byte) {
    hw.Lock()
    defer hw.Unlock()

    if len(buf) > MTU {
        return
    }

    hw.tx.push(buf)
    reg.Set(hw.tdar, TDAR_ACTIVE)
}

// enable IRQs, start interface
eth.EnableInterrupt(enet.IRQ_RXF)
eth.Start(false)

// hook interface into Go runtime
net.SocketFunc = iface.Socket
```

```
// interrupt handling
func handleEthernetInterrupt(eth *enet.ENEt) {
    for buf := eth.Rx(); buf != nil; buf = eth.Rx() {
        eth.RxHandler(buf)
        eth.ClearInterrupt(enet.IRQ_RXF)
    }
}

func StartInterruptHandler(eth *enet.ENEt) {
    imx6ul.GIC.Init(true, false)
    imx6ul.GIC.EnableInterrupt(eth.IRQ, true)

    arm.RegisterInterruptHandler()

    for {
        arm.WaitInterrupt()

        irq, end := imx6ul.GIC.GetInterrupt(true)

        if end != nil {
            end <- true
        }

        if irq == eth.IRQ {
            handleEthernetInterrupt(eth)
        }
    }
}
```



```
$ make qemu
```

```
tamago/arm (go1.22.1) • 5e1f6aa lcars@lambda on 2024-03-20 08:02:11 • i.MX6UL 1188 MHz (emulated)

ble          (time in RFC339 format)?
date         # BLE serial console
dcp          # show/change runtime date and time
dns          # benchmark hardware encryption
exit, quit   # resolve domain (requires routing)
help         # close session
i2c          # this help
info         # IC bus read
kem          # device information
led          # benchmark post-quantum KEM
md           # LED control
mmc          # memory display (use with caution)
mw           # MMC/SD card read
ntp          # memory write (use with caution)
otp          <host> # change runtime date and time w/ NTP
otp          <bank> <word> # OTP fuses display
rand         # gather 32 random bytes
reboot       # reset device
stack        # stack trace of current goroutine
stackall     # stack trace of all goroutines
test         # launch tests

> kem
Kyber1024 89248f2f33f7f4f7051729111f3049c409a933ec904aedadf035f30fa5646cd5 (287.799024ms)
Kyber768 a1e122cad3c24bc51622e4c242d8b8cbcd3f618fee42284006b5ca8f9ea02c2 (209.114896ms)
Kyber512 e9c2bd37133fcba0772f81559f14b1f58dcc1c816701be9ba6214d43baf4547 (149.049056ms)

> rand
db7d46647880be1e51731177b6f73645b71ca504242c97758df3a86842d93236

> md 80000000 96
00000000 18 f0 9f e5 18 f0 9f e5 18 f0 9f e5 18 f0 9f e5 |.....|
00000010 18 f0 9f e5 18 f0 9f e5 18 f0 9f e5 18 f0 9f e5 |.....|
00000020 04 d4 0f 80 38 d4 0f 80 6c d4 0f 80 a0 d4 0f 80 |...8.1.|
00000030 d4 d4 0f 80 00 00 00 00 08 d5 0f 80 3c d5 0f 80 |.....<..|
00000040 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 |.....|
00000050 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 |.....|
```

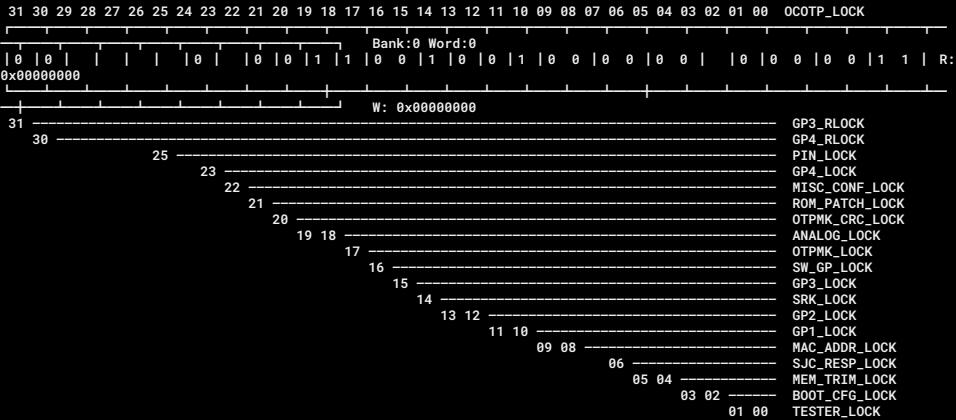
```
> ntp time.google.com
2024-03-20T08:02:11Z
```

```
> info
Runtime .....: go1.22.1 tamago/arm
RAM .....: 0x80000000-0x9f600000 (502 MiB)
Board .....: UA-MKII
SoC .....: i.MX6ULZ 1188 MHz (emulated)
```

```
$ ssh 10.0.0.1
```

```
tamago/arm (go1.22.1) • 5e1f6aa lcars@lambda on 2024-03-20 10:00:48 • i.MX6ULL 900 MHz
```

```
> otp 0 0
OTP bank:0 word:0 val:0x00324003
```



```
> dns www.golang.org
[142.251.215.238 2607:f8b0:400a:805::200e]
```

```
> dcp 65536 10
Doing aes-128 cbc for 10s on 65536 blocks
6201 aes-128 cbc's in 10.00086575s
```

```
> info
Runtime .....: go1.22.1 tamago/arm
RAM .....: 0x80000000-0x9f600000 (502 MiB)
Board .....: UA-MKII-v
SoC .....: i.MX6ULLZ 900 MHz
SSM Status ...: state:0b101 clk:false tmp:false vcc:false hac:4294967295
Boot ROM hash: 1727a0f46dbde555b583e9a138ae359389974b7be4369ffd4a252a8730f7e59b
Secure boot ...: true
Unique ID ....: FE186D5AB312430B
SDP .....: true
Temperature ...: 48.333332
```

```

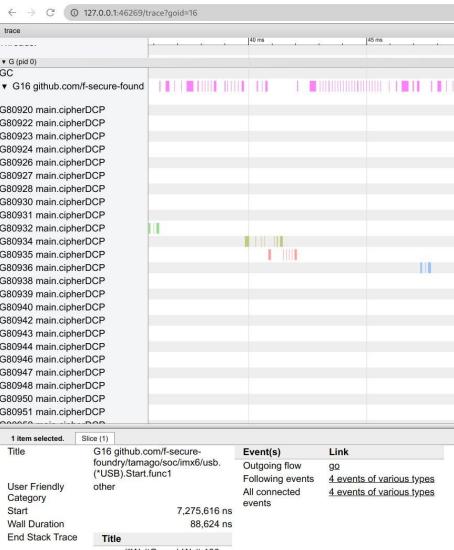
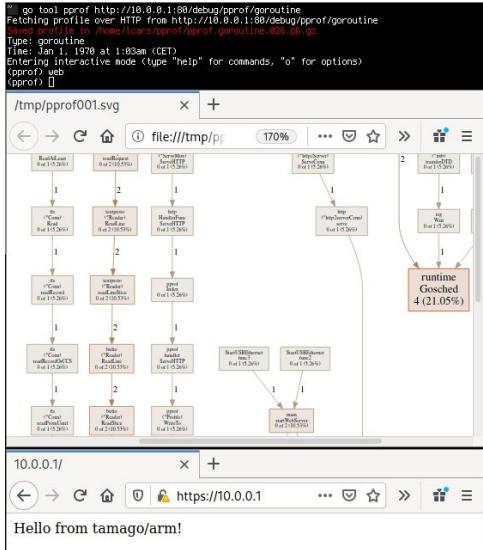
> tailscale $YOURKEY
tsnet --- [v1] using fake (no-op) tun device
tsnet --- [v1] using fake (no-op) OS network configurator
tsnet --- [v1] using fake (no-op) DNS configurator
tsnet --- dns: using dns.noopManager
tsnet --- link state: interfaces.State{defaultRoute= ifs={} v4=false v6=false}
tsnet --- magicsock: disco key = d:xxxxxxxxxxxxxx
tsnet --- Creating WireGuard device...
tsnet --- Bringing WireGuard device up...
tsnet --- wg: [v2] UDP bind has been updated
tsnet --- wg: [v2] Interface state was Down, requested Up, now Up
tsnet --- Bringing router up...
tsnet --- [v1] warning: fakeRouter.Up: not implemented.
tsnet --- Clearing router settings...
tsnet --- [v1] warning: fakeRouter.Set: not implemented.
tsnet --- Starting network monitor...
tsnet --- Engine created.
tsnet --- tsnet running state path /tsnet-tamago/tailscaled.state
tsnet --- pm: migrating "_daemon" profile to new format
tsnet --- [vJSON]1{"Hostinfo":{"IPNVersion":"1.49.0-dev20230906-t7a0be7f2c-dirty","OS":"tamago","Package":"tsnet","Hostname":"tamago","GoArch":"arm","GoArchVar":"7","GoVersion":"go1.21.0"}}
tsnet --- logpolicy: using UserCachedDir, "/Tailscale"
tsnet --- [v1] netmap packet filter: (not ready yet)
tsnet --- tsnet starting with hostname "tamago", varRoot "/tsnet-tamago"
tsnet --- Start
tsnet --- generating new machine key
"
netmap: self: [0xGGm] auth=machine-authorized u=xxxxxxxxxx@gmail.com [100.xxx.xx.82/32 fd7a:xxxx:xxxx:xxxx:xxxx:xxxx:xxxx:xxxx/128]
tsnet --- control: [v1] mapRoutine: netmap received: state:synchronized
tsnet --- control: [v1] sendStatus: mapRoutine-got-netmap: state:synchronized
tsnet --- active login: xxxxxxxxx@gmail.com
tsnet --- [v1] netmap packet filter: 1 filters
tsnet --- [v1] magicsock: got updated network map; 3 peers
tsnet --- [v2] netstack: registered IP 100.xxx.xx.82/32
tsnet --- [v2] netstack: registered IP fd7a:xxxx:xxxx:xxxx:xxxx:xxxx:xxxx:xxxx/128
...
tsnet --- peerapi: serving on http://100.xxx.xx.82:63151
tsnet --- peerapi: serving on http://[fd7a:xxxx:xxxx:xxxx:xxxx:xxxx:xxxx:xxxx]:63151
tsnet --- netcheck: UDP is blocked, trying ICMP
tsnet --- control: [v1] HostInfo:
{"IPNVersion":"1.49.0-dev20230906-t7a0be7f2c-dirty","BackendLogID":"xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx9b2efd","OS":"tamago","Package":"tsnet","Hostname":"tamago","GoArch":"arm","GoArchVar":"7","GoVersion":"go1.21.0","Services":[{"Proto":"peerapi4","Port":63151},{"Proto":"peerapi6","Port":63151}],"Userspace":true,"UserspaceRouter":true}
tsnet --- control: [v1] PollNetMap: stream=false ep=[]

starting web server at 100.117.90.82:80
tsnet --- control: [v1] successful lite map update in 316ms
starting ssh server (SHA256:xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx) at :22

```

Machine Details			
Information about this machine's network. Used to debug connection issues.			
Creator	[REDACTED]	Created	Jun 15, 2023 at 1:06 PM GMT+2
Machine name	tamago Copy	Last seen	1:06 PM GMT+2
OS hostname	tamago	Key expiry	6 months from now
OS	Tamago		
Tailscale version	1.38.4-dev20230612	CLIENT CONNECTIVITY	
Tailscale IPv4	100 [REDACTED] Copy	Varies	—
Tailscale IPv6	fd7a [REDACTED] Copy	Halpinning	—
ID	nf4 [REDACTED]	IPv6	—
Endpoints	—	UDP	—
Relays	—	UHP	—
		PCP	—
		NAT-PMP	—

Debugging



```
(g) arm-none-eabi-gdb+
Output/Messages

Breakpoint 1, main.main () at /mnt/git/public/tamago-example/example.go:206
206 func main() {
    Assembly main
    main.main+0 ldr r1, [r0, #0]
    main.main+4 cmp sp, r1
    main.main+8 bls sp@0x4314c <main.main+344>
    main.main+12 str lr, [sp, #-0x8]! <main.main+344>

Registers
    R0 0x00000000    r1 0x00000000    R7 0x00000000
    R1 0x00000000    r2 0x00000000    R8 0x00000000
    R2 0x00000000    r3 0x00000000    R9 0x00000000
    R3 0x00000000    r4 0x00000000    R10 0x00000000
    R4 0x00000000    r5 0x00000000    R11 0x00000000
    R5 0x00000000    r6 0x00000000    R12 0x00000000
    R6 0x00000000    r7 0x00000000    R13 0x00000000
    R7 0x00000000    r8 0x00000000    R14 0x00000000
    R8 0x00000000    r9 0x00000000    R15 0x00000000
    R9 0x00000000    r10 0x00000000    R16 0x00000000
    R10 0x00000000   r11 0x00000000    R17 0x00000000
    R11 0x00000000   r12 0x00000000    R18 0x00000000
    R12 0x00000000   r13 0x00000000    R19 0x00000000
    R13 0x00000000   r14 0x00000000    R20 0x00000000
    R14 0x00000000   r15 0x00000000    R21 0x00000000
    R15 0x00000000   r16 0x00000000    R22 0x00000000
    R16 0x00000000   r17 0x00000000    R23 0x00000000
    R17 0x00000000   r18 0x00000000    R24 0x00000000
    R18 0x00000000   r19 0x00000000    R25 0x00000000
    R19 0x00000000   r20 0x00000000    R26 0x00000000
    R20 0x00000000   r21 0x00000000    R27 0x00000000
    R21 0x00000000   r22 0x00000000    R28 0x00000000
    R22 0x00000000   r23 0x00000000    R29 0x00000000
    R23 0x00000000   r24 0x00000000    R30 0x00000000
    R24 0x00000000   r25 0x00000000    R31 0x00000000
    R25 0x00000000   r26 0x00000000    R32 0x00000000
    R26 0x00000000   r27 0x00000000    R33 0x00000000
    R27 0x00000000   r28 0x00000000    R34 0x00000000
    R28 0x00000000   r29 0x00000000    R35 0x00000000
    R29 0x00000000   r30 0x00000000    R36 0x00000000
    R30 0x00000000   r31 0x00000000    R37 0x00000000
    R31 0x00000000   r32 0x00000000    R38 0x00000000
    R32 0x00000000   r33 0x00000000    R39 0x00000000
    R33 0x00000000   r34 0x00000000    R40 0x00000000
    R34 0x00000000   r35 0x00000000    R41 0x00000000
    R35 0x00000000   r36 0x00000000    R42 0x00000000
    R36 0x00000000   r37 0x00000000    R43 0x00000000
    R37 0x00000000   r38 0x00000000    R44 0x00000000
    R38 0x00000000   r39 0x00000000    R45 0x00000000
    R39 0x00000000   r40 0x00000000    R46 0x00000000
    R40 0x00000000   r41 0x00000000    R47 0x00000000
    R41 0x00000000   r42 0x00000000    R48 0x00000000
    R42 0x00000000   r43 0x00000000    R49 0x00000000
    R43 0x00000000   r44 0x00000000    R50 0x00000000
    R44 0x00000000   r45 0x00000000    R51 0x00000000
    R45 0x00000000   r46 0x00000000    R52 0x00000000
    R46 0x00000000   r47 0x00000000    R53 0x00000000
    R47 0x00000000   r48 0x00000000    R54 0x00000000
    R48 0x00000000   r49 0x00000000    R55 0x00000000
    R49 0x00000000   r50 0x00000000    R56 0x00000000
    R50 0x00000000   r51 0x00000000    R57 0x00000000
    R51 0x00000000   r52 0x00000000    R58 0x00000000
    R52 0x00000000   r53 0x00000000    R59 0x00000000
    R53 0x00000000   r54 0x00000000    R60 0x00000000
    R54 0x00000000   r55 0x00000000    R61 0x00000000
    R55 0x00000000   r56 0x00000000    R62 0x00000000
    R56 0x00000000   r57 0x00000000    R63 0x00000000
    R57 0x00000000   r58 0x00000000    R64 0x00000000
    R58 0x00000000   r59 0x00000000    R65 0x00000000
    R59 0x00000000   r60 0x00000000    R66 0x00000000
    R60 0x00000000   r61 0x00000000    R67 0x00000000
    R61 0x00000000   r62 0x00000000    R68 0x00000000
    R62 0x00000000   r63 0x00000000    R69 0x00000000
    R63 0x00000000   r64 0x00000000    R70 0x00000000
    R64 0x00000000   r65 0x00000000    R71 0x00000000
    R65 0x00000000   r66 0x00000000    R72 0x00000000
    R66 0x00000000   r67 0x00000000    R73 0x00000000
    R67 0x00000000   r68 0x00000000    R74 0x00000000
    R68 0x00000000   r69 0x00000000    R75 0x00000000
    R69 0x00000000   r70 0x00000000    R76 0x00000000
    R70 0x00000000   r71 0x00000000    R77 0x00000000
    R71 0x00000000   r72 0x00000000    R78 0x00000000
    R72 0x00000000   r73 0x00000000    R79 0x00000000
    R73 0x00000000   r74 0x00000000    R80 0x00000000
    R74 0x00000000   r75 0x00000000    R81 0x00000000
    R75 0x00000000   r76 0x00000000    R82 0x00000000
    R76 0x00000000   r77 0x00000000    R83 0x00000000
    R77 0x00000000   r78 0x00000000    R84 0x00000000
    R78 0x00000000   r79 0x00000000    R85 0x00000000
    R79 0x00000000   r80 0x00000000    R86 0x00000000
    R80 0x00000000   r81 0x00000000    R87 0x00000000
    R81 0x00000000   r82 0x00000000    R88 0x00000000
    R82 0x00000000   r83 0x00000000    R89 0x00000000
    R83 0x00000000   r84 0x00000000    R90 0x00000000
    R84 0x00000000   r85 0x00000000    R91 0x00000000
    R85 0x00000000   r86 0x00000000    R92 0x00000000
    R86 0x00000000   r87 0x00000000    R93 0x00000000
    R87 0x00000000   r88 0x00000000    R94 0x00000000
    R88 0x00000000   r89 0x00000000    R95 0x00000000
    R89 0x00000000   r90 0x00000000    R96 0x00000000
    R90 0x00000000   r91 0x00000000    R97 0x00000000
    R91 0x00000000   r92 0x00000000    R98 0x00000000
    R92 0x00000000   r93 0x00000000    R99 0x00000000
    R93 0x00000000   r94 0x00000000    R100 0x00000000
    R94 0x00000000   r95 0x00000000    R101 0x00000000
    R95 0x00000000   r96 0x00000000    R102 0x00000000
    R96 0x00000000   r97 0x00000000    R103 0x00000000
    R97 0x00000000   r98 0x00000000    R104 0x00000000
    R98 0x00000000   r99 0x00000000    R105 0x00000000
    R99 0x00000000   r100 0x00000000    R106 0x00000000
    R100 0x00000000   r101 0x00000000    R107 0x00000000
    R101 0x00000000   r102 0x00000000    R108 0x00000000
    R102 0x00000000   r103 0x00000000    R109 0x00000000
    R103 0x00000000   r104 0x00000000    R110 0x00000000
    R104 0x00000000   r105 0x00000000    R111 0x00000000
    R105 0x00000000   r106 0x00000000    R112 0x00000000
    R106 0x00000000   r107 0x00000000    R113 0x00000000
    R107 0x00000000   r108 0x00000000    R114 0x00000000
    R108 0x00000000   r109 0x00000000    R115 0x00000000
    R109 0x00000000   r110 0x00000000    R116 0x00000000
    R110 0x00000000   r111 0x00000000    R117 0x00000000
    R111 0x00000000   r112 0x00000000    R118 0x00000000
    R112 0x00000000   r113 0x00000000    R119 0x00000000
    R113 0x00000000   r114 0x00000000    R120 0x00000000
    R114 0x00000000   r115 0x00000000    R121 0x00000000
    R115 0x00000000   r116 0x00000000    R122 0x00000000
    R116 0x00000000   r117 0x00000000    R123 0x00000000
    R117 0x00000000   r118 0x00000000    R124 0x00000000
    R118 0x00000000   r119 0x00000000    R125 0x00000000
    R119 0x00000000   r120 0x00000000    R126 0x00000000
    R120 0x00000000   r121 0x00000000    R127 0x00000000
    R121 0x00000000   r122 0x00000000    R128 0x00000000
    R122 0x00000000   r123 0x00000000    R129 0x00000000
    R123 0x00000000   r124 0x00000000    R130 0x00000000
    R124 0x00000000   r125 0x00000000    R131 0x00000000
    R125 0x00000000   r126 0x00000000    R132 0x00000000
    R126 0x00000000   r127 0x00000000    R133 0x00000000
    R127 0x00000000   r128 0x00000000    R134 0x00000000
    R128 0x00000000   r129 0x00000000    R135 0x00000000
    R129 0x00000000   r130 0x00000000    R136 0x00000000
    R130 0x00000000   r131 0x00000000    R137 0x00000000
    R131 0x00000000   r132 0x00000000    R138 0x00000000
    R132 0x00000000   r133 0x00000000    R139 0x00000000
    R133 0x00000000   r134 0x00000000    R140 0x00000000
    R134 0x00000000   r135 0x00000000    R141 0x00000000
    R135 0x00000000   r136 0x00000000    R142 0x00000000
    R136 0x00000000   r137 0x00000000    R143 0x00000000
    R137 0x00000000   r138 0x00000000    R144 0x00000000
    R138 0x00000000   r139 0x00000000    R145 0x00000000
    R139 0x00000000   r140 0x00000000    R146 0x00000000
    R140 0x00000000   r141 0x00000000    R147 0x00000000
    R141 0x00000000   r142 0x00000000    R148 0x00000000
    R142 0x00000000   r143 0x00000000    R149 0x00000000
    R143 0x00000000   r144 0x00000000    R150 0x00000000
    R144 0x00000000   r145 0x00000000    R151 0x00000000
    R145 0x00000000   r146 0x00000000    R152 0x00000000
    R146 0x00000000   r147 0x00000000    R153 0x00000000
    R147 0x00000000   r148 0x00000000    R154 0x00000000
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    R149 0x00000000   r150 0x00000000    R156 0x00000000
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    R155 0x00000000   r156 0x00000000    R162 0x00000000
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    R157 0x00000000   r158 0x00000000    R164 0x00000000
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    R159 0x00000000   r160 0x00000000    R166 0x00000000
    R160 0x00000000   r161 0x00000000    R167 0x00000000
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    R164 0x00000000   r165 0x00000000    R171 0x00000000
    R165 0x00000000   r166 0x00000000    R172 0x00000000
    R166 0x00000000   r167 0x00000000    R173 0x00000000
    R167 0x00000000   r168 0x00000000    R174 0x00000000
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    R173 0x00000000   r174 0x00000000    R180 0x00000000
    R174 0x00000000   r175 0x00000000    R181 0x00000000
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    R176 0x00000000   r177 0x00000000    R183 0x00000000
    R177 0x00000000   r178 0x00000000    R184 0x00000000
    R178 0x00000000   r179 0x00000000    R185 0x00000000
    R179 0x00000000   r180 0x00000000    R186 0x00000000
    R180 0x00000000   r181 0x00000000    R187 0x00000000
    R181 0x00000000   r182 0x00000000    R188 0x00000000
    R182 0x00000000   r183 0x00000000    R189 0x00000000
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    R185 0x00000000   r186 0x00000000    R192 0x00000000
    R186 0x00000000   r187 0x00000000    R193 0x00000000
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    R191 0x00000000   r192 0x00000000    R198 0x00000000
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    R193 0x00000000   r194 0x00000000    R200 0x00000000
    R194 0x00000000   r195 0x00000000    R201 0x00000000
    R195 0x00000000   r196 0x00000000    R202 0x00000000
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    R200 0x00000000   r201 0x00000000    R207 0x00000000
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    R202 0x00000000   r203 0x00000000    R209 0x00000000
    R203 0x00000000   r204 0x00000000    R210 0x00000000
    R204 0x00000000   r205 0x00000000    R211 0x00000000
    R205 0x00000000   r206 0x00000000    R212 0x00000000
    R206 0x00000000   r207 0x00000000    R213 0x00000000
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    R209 0x00000000   r210 0x00000000    R216 0x00000000
    R210 0x00000000   r211 0x00000000    R217 0x00000000
    R211 0x00000000   r212 0x00000000    R218 0x00000000
    R212 0x00000000   r213 0x00000000    R219 0x00000000
    R213 0x00000000   r214 0x00000000    R220 0x00000000
    R214 0x00000000   r215 0x00000000    R221 0x00000000
    R215 0x00000000   r216 0x00000000    R222 0x00000000
    R216 0x00000000   r217 0x00000000    R223 0x00000000
    R217 0x00000000   r218 0x00000000    R224 0x00000000
    R218 0x00000000   r219 0x00000000    R225 0x00000000
    R219 0x00000000   r220 0x00000000    R226 0x00000000
    R220 0x00000000   r221 0x00000000    R227 0x00000000
    R221 0x00000000   r222 0x00000000    R228 0x00000000
    R222 0x00000000   r223 0x00000000    R229 0x00000000
    R223 0x00000000   r224 0x00000000    R230 0x00000000
    R224 0x00000000   r225 0x00000000    R231 0x00000000
    R225 0x00000000   r226 0x00000000    R232 0x00000000
    R226 0x00000000   r227 0x00000000    R233 0x00000000
    R227 0x00000000   r228 0x00000000    R234 0x00000000
    R228 0x00000000   r229 0x00000000    R235 0x00000000
    R229 0x00000000   r230 0x00000000    R236 0x00000000
    R230 0x00000000   r231 0x00000000    R237 0x00000000
    R231 0x00000000   r232 0x00000000    R238 0x00000000
    R232 0x00000000   r233 0x00000000    R239 0x00000000
    R233 0x00000000   r234 0x00000000    R240 0x00000000
    R234 0x00000000   r235 0x00000000    R241 0x00000000
    R235 0x00000000   r236 0x00000000    R242 0x00000000
    R236 0x00000000   r237 0x00000000    R243 0x00000000
    R237 0x00000000   r238 0x00000000    R244 0x00000000
    R238 0x00000000   r239 0x00000000    R245 0x00000000
    R239 0x00000000   r240 0x00000000    R246 0x00000000
    R240 0x00000000   r241 0x00000000    R247 0x00000000
    R241 0x00000000   r242 0x00000000    R248 0x000
```

GoKey - The bare metal Go smart card

The GoKey application implements a composite USB OpenPGP 3.4 smartcard and FIDO U2F token, written in pure Go (~2500¹ LOC).

It allows to implement a radically different security model for smartcards, taking advantage of TamaGo to safely mix layers and protocols not easy to combine.

For instance authentication can happen over SSH instead of plaintext PIN transmission over USB.

	Trust anchor	Data protection	Runtime	Application	Requires tamper proofing	Encryption at rest
traditional smartcard	flash protection	flash protection	JCOP	JCOP applets	Yes	No
USB armory with GoKey	secure boot	SoC security element	TamaGo	Go application	No	Yes

```
host ~ gpg --card-status
Reader ...: USB armory Mk II (Smart Card Control) (0.1) 00 00
Application ID ...: D276000124010304F5EC09320C0000
Application type ...: OpenPGP
Version .....: 3.4
Manufacturer ...: W / T H Secure
Serial number ...: D276000124010304F5EC09320C
Name of cardholder: Alice
Language prefs ...: (not set)
Salutation ....:
URL of public key: (not set)
Login PIN .....: forced
Signature PIN ...: forced
Key attributes ...: rsa4096 rsa4096 rsa4096
Max. PIN lengths : 254 127 127
PIN retry counter: 1 0 0
Signature counter : 0 0 0
Created on ...: 0SEC DE84 43FA 5C01 9C7A 51A2 E9C8 5194 3E46 C2B5
                 created: 2020-04-03 15:10:30
Signature key...: 656B E354 EE12 BFFF 988B 1607 556B 9659 5A2C D776
                 created: 2020-04-03 15:01:49
Authentication key: (none)
General key info.: rsa4096/E9C851943E46C2B5 2020-04-03 Alice <alice@wonderland
rsa4096/CB874C5E15EA0B created: 2020-04-03 expires: 2022-04-03
rsa4096/556B96595A2CD776 created: 2020-04-03 expires: 2022-04-03
rsa4096/E9C851943E46C2B5 created: 2020-04-03 expires: 2022-04-03
rsa4096/2E831B5E996EE83D created: 2020-04-03 expires: 2022-04-03
ssh alice@10.0.0.10
host ~ ssh alice@10.0.0.10
host ~ gpg --card-status
GoKey
exit, quit          # close session
help               # this help
init               # initialize card
rand               # gather 32 bytes from TRNG via crypto/rand
reboot             # restart
status             # display card status
lock (all|sig|dec) # key lock
unlock (all|sig|dec) # key unlock, prompts decryption passphrase
resizing terminal (pty-size:80x66)
> unlock all
Passphrase:
VERIFY: 05 EC DE B4 43 FA 5C 01 9C 7A 51 A2 E9 C8 51 94 3E 46 C2 B5 unlocked
VERIFY: 65 6B E3 54 EE 12 FB FB 98 8B 16 07 55 6B 96 59 5A 2C D7 76 unlocked
> exit
logout
closing ssh connection
Connection to 10.0.0.10 closed.
host ~ gpg --decrypt secret.asc
gpg: encrypted by 4096-bit RSA key, ID 556B96595A2CD776, created 2020-04-03
      "Alice <alice@wonderland"
cheshire wrote:
  "Where do you want to go?"
alice wrote:
  "I don't know"
cheshire wrote:
  "Then, it really doesn't matter, does it?"
host ~
```



Demo: GoKey



<https://github.com/usbarmory/gokey>

<https://youtu.be/WeO2eiYSeWM>

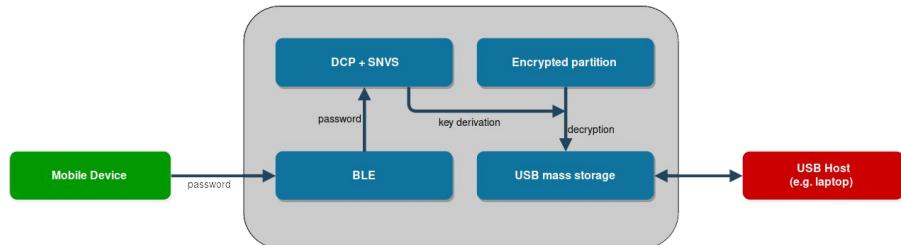
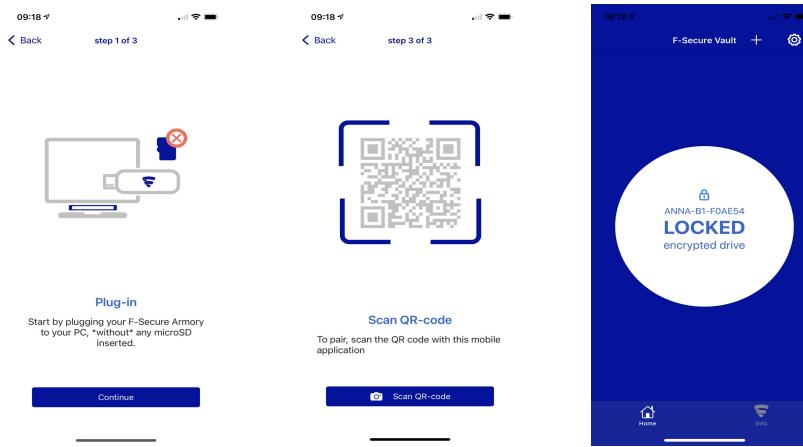


Armory Drive - Encrypted USB Mass Storage

Armory Drive implements the **easiest to use encrypted drive solution** allowing secure access to any microSD card.

Unlike existing encrypted drive solutions the key is unlocked with **3 factors** (user + mobile phone + armory) and **over Bluetooth**. No trust (or driver requirements) are delegated to the host.

It consists of **~3000 LOC** of pure TamaGo code and an iOS app.



It uses Google Firmware Transparency framework to enable firmware update authentication on the installer as well as the device itself.

armory-boot - USB armory boot loader

A primary signed boot loader (~400 LOC) to launch authenticated Linux kernel images on secure booted¹ USB armory boards, replacing U-Boot.

```
func boot(kernel []byte, dtb []byte, cmdline string) {
    dma.Init(dmaStart, dmaSize)
    mem, _ := dma.Reserve(dmaSize, 0)

    dma.Write(mem, kernel, kernelOffset)
    dma.Write(mem, dtb, dtbOffset)

    image := mem + kernelOffset
    params := mem + dtbOffset

    arm.ExceptionHandler = func(n int) {
        if n != arm.SUPERVISOR {
            panic("unhandled exception")
        }
    }

    usbarmory.LED("blue", false)
    usbarmory.LED("white", false)

    imx6ul.ARM.DisableInterrupts()
    imx6ul.ARM.FlushDataCache()
    imx6ul.ARM.Disable()

    exec(image, params)
}

svc()
}
```

```
func verifySignature(buf []byte, s []byte) (valid bool, err error) {
    sig, err := DecodeSignature(string(s))

    if err != nil {
        return false, fmt.Errorf("invalid signature, %v", err)
    }

    pub, err := NewPublicKey(PublicKeyStr)

    if err != nil {
        return false, fmt.Errorf("invalid public key, %v", err)
    }

    return pub.Verify(buf, sig)
}

func verifyHash(buf []byte, s string) bool {
    // use hardware acceleration
    sum, _ := imx6ul.DCP.Sum256(buf)

    if hash, err := hex.DecodeString(s); err != nil {
        return false
    }

    return bytes.Equal(sum[:], hash)
}
```

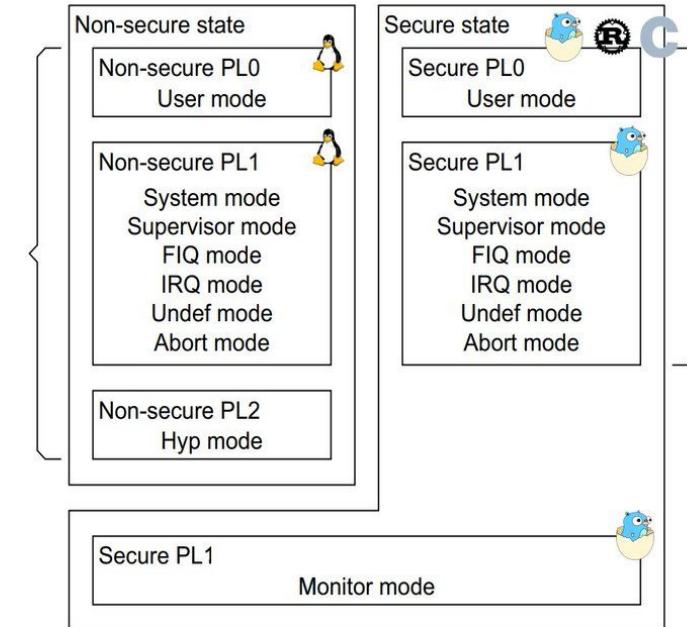


GoTEE - Trusted Execution Environment

The GoTEE framework implements concurrent instantiation of TamaGo based unikernels in privileged and unprivileged modes, interacting with each other through monitor mode and custom system calls.

With these capabilities GoTEE implements a pure Go Trusted Execution Environment (TEE) bringing Go memory safety, convenience and capabilities to bare metal execution within TrustZone Secure World.

It supports any freestanding user mode applets (e.g. TamaGo, C, Rust) and any “rich” OS running in NonSecure World (e.g. Linux).



```
> gotee
PL1 tamago/arm (go1.18.3) • TEE system/monitor (Secure World)
PL1 loaded applet addr:0x9c000000 size:4719809 entry:0x9c06f188
PL1 loaded kernel addr:0x80000000 size:4384184 entry:0x8006db70
PL1 starting mode:SYS ns:true sp:0x00000000 pc:0x8006db70
PL1 starting mode:USR ns:false sp:0x9e000000 pc:0x9c06f188
PL1 tamago/arm (go1.18.4) • system/supervisor (Normal World)
PL1 in Normal World is about to perform DCP key derivation
PL1 in Normal World successfully used DCP (df3eed2a50c9dd22daf7cf864f27bb90)
PL1 in Normal World is about to yield back
    r0:00000000  r1:814243f0  r2:00000001  r3:00000000
    r4:00000000  r5:00000000  r6:00000000  r7:8146bf14
    r8:00000007  r9:00000034  r10:814040f0  r11:802e9b21  cpsr:600001d6 (MON)
    r12:00000000  sp:8146bf54  lr:80185518  pc:80185648  spsr:600001df (SYS)
PL1 stopped mode:SYS ns:true sp:0x8146bf54 lr:0x80185518 pc:0x80185648 err:exit
PL0 tamago/arm (go1.18.4) • TEE user applet (Secure World)
PL0 obtained 16 random bytes from PL1: 10e742f0dad15db3f00aea14ee4a5acc
PL1 loaded kernel addr:0x80000000 size:4384184 entry:0x8006db70
PL1 re-launching kernel with TrustZone restrictions
PL1 starting mode:SYS ns:true sp:0x00000000 pc:0x8006db70
PL1 tamago/arm (go1.18.4) • system/supervisor (Normal World)
PL1 in Normal World is about to perform DCP key derivation
    r0:02280000  r1:814683a0  r2:8143c588  r3:00000001
    r4:00000000  r5:00000000  r6:00000000  r7:8146bf14
    r8:00000007  r9:00000044  r10:814040f0  r11:802e9b21  cpsr:200001d6 (MON)
    r12:00000000  sp:8146bf28  lr:80180398  pc:80011340  spsr:200001df (SYS)
PL1 stopped mode:SYS ns:true sp:0x8146bf28 lr:0x80180398 pc:0x80011340 err:DATA_ABORT
PL1 in Secure World is about to perform DCP key derivation
PL1 in Secure World successfully used DCP (df3eed2a50c9dd22daf7cf864f27bb90)
```

```
$ ssh 10.0.0.1
PL1 tamago/arm (go1.18.3) • TEE system/monitor (Secure World)

help                                # this help
reboot                               # reset the SoC/board
stack                                # stack trace of current goroutine
stackall                             # stack trace of all goroutines
md <hex offset> <size>              # memory display (use with caution)
mw <hex offset> <hex value>        # memory write (use with caution)

gotee                                # TrustZone test w/ TamaGo unikernels
linux <uSD|eMMC>                   # boot NonSecure USB armory Debian image

dbg                                    # show ARM debug permissions
csl                                    # show config security levels (CSL)
csl <periph> <slave> <hex csl>      # set config security level (CSL)
sa                                     # show security access (SA)
sa <id> <secure|nonsecure>         # set security access (SA)

> dbg
| type          | implemented | enabled |
|-----|-----|-----|
| Secure non-invasive | 1 | 0 |
| Secure invasive | 1 | 0 |
| Non-secure non-invasive | 1 | 1 |
| Non-secure invasive | 1 | 0 |

> linux eMMC
armory-boot: loading configuration at /boot/armory-boot-nonsecure.conf
PL1 loaded kernel addr:0x80000000 size:7603616 entry:0x80800000
PL1 launching Linux
PL1 starting mode:SVC ns:true sp:0x00000000 pc:0x80800000
Booting Linux on physical CPU 0x0
Linux version 5.15.52-0 (usbarmory@usbarmory) arm-linux-gnueabihf-gcc (Ubuntu 9.4.0-1ubuntu1~20.04.1)
```

GoTEE in the wild: space!

On May 22nd 2023 (UTC 05:00:32) the USB armory Mk II got a lift to space!

On February 27th 2024 (UTC 07:27:00) the same unit went back to space.

GoTEE supervised a TamaGo based unikernel (acting as Trusted Applet) and a full Linux instance isolated in NonSecure World to test Post Quantum Key exchanges.

For this occasion TamaGo and GoTEE have been updated with full watchdog and interrupt support.

The payload remained operation for the entire flight duration performing exactly 400 PQC key exchanges. As far as we know this is the first time bare metal Go executed in space.



Andrea Barisani
@AndreaBarisani

...

Yesterday (UTC 05:00:32) @WithSecure USB armory got a lift (~225 km apogee) from the MAPHEUS-13 rocket launched from the Esrange Space Center.

Thanks to @DLR_de @adesso_SE for this amazing collaboration!

Our bare metal GoTEE performed Post Quantum key exchange in space!



GoTEE in the wild: Armored Witness

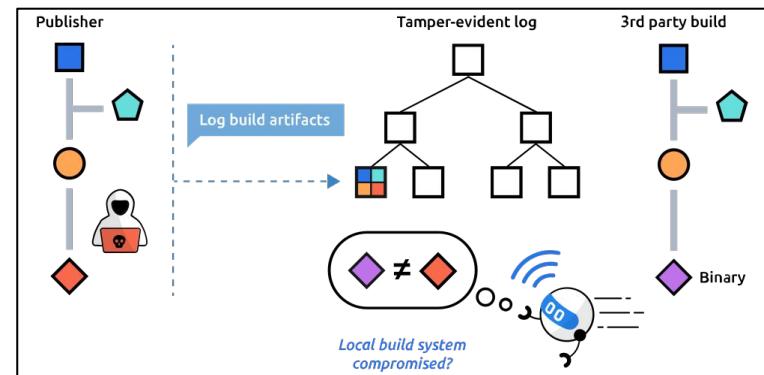
The applet observes public transparency logs verifying that they're operating in an append-only fashion, and counter-signing those checkpoints which it has determined are consistent with all previous checkpoints its seen from the same log.

The counter-signed checkpoints are sent to a **distributor**, which then collates counter-signatures for a given checkpoint from one or more Armored Witness devices, and serves them via a public API.

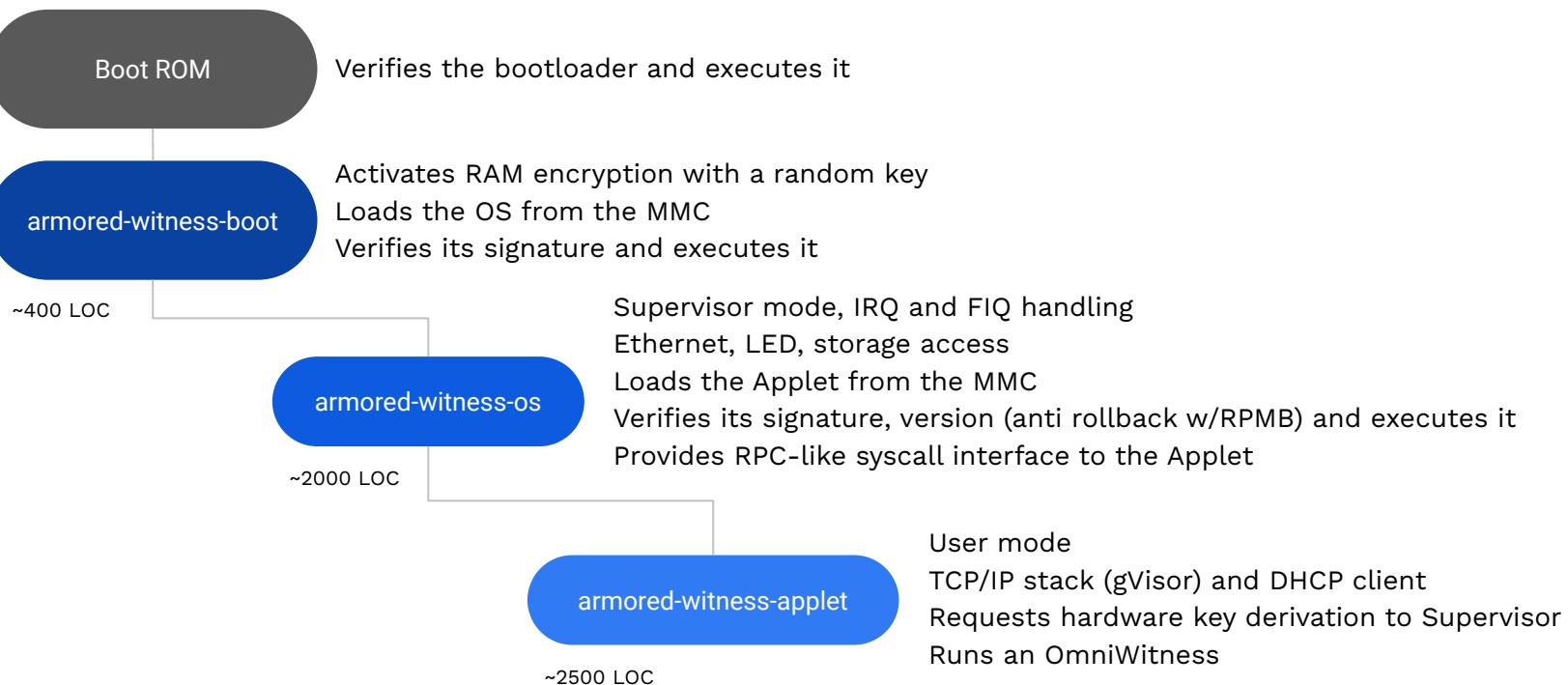
The benefit of this system comes through **removing trust from log operators** to behave honestly, and **placing some of that trust in the witnesses**.

Splitting the trust across multiple parties in this way means that **a larger number of parties must collude to hide malfeasance**, and as other witness implementations/networks start to appear, the number of parties required to collude increases correspondingly.

However, **we can minimise the amount of trust required** to be placed in the Armored Witness by having it be **as transparent as possible** too.



Armored Witness architecture



All firmware verification past the Boot ROM stage verify, through FT proof bundles, the presence of one or more trusted signatures (multi party signing) on the release manifest. The release manifest includes the binary hash, log checkpoint, the index of the manifest in the log and its corresponding inclusion proof.

The OS and Applet must be published on the log to be usable on the device.

<https://github.com/transparency-dev/armored-witness>

Minimising trust

All firmware is open source, written in TamaGo, and is build-reproducible by anyone. All firmware is logged to a Firmware Transparency log at build and release time.

The provision tool will only use firmware artefacts discovered in the FT log in order to program devices. The on-device self-update process requires that updated firmware is hosted in the FT log.

The verify tool can be used by *custodians* to inspect the device, extract the firmware components from it, and verify that they are present in the FT log.

The verify_build command continuously monitors the contents of the FT log, and tests that every logged firmware is indeed reproducibly built.

Google and WithSecure are able to quickly become aware of misuse of their signing identities to release unauthorised firmware updates.

Anyone can verify that each firmware can be rebuilt consistently with what is running, logged and its source.

```
{
  "schema_version": 0,
  "component": "TRUSTED_APPLET",
  "git": {
    "tag_name": "0.3.1709910063-incompatible",
    "commit_fingerprint": "9651fc25839d9937acc041057cf3906f26fc1ae5"
  },
  "build": {
    "tamago_version": "1.22.0",
    "envs": [
      "FT_LOG_URL=https://api.transparency.dev/armored-witness-firmware/ci/log/2",
      "FT_BIN_URL=https://api.transparency.dev/armored-witness-firmware/ci/artefacts/2",
      "LOG_ORIGIN=transparency.dev/armored-witness/firmware_transparency/ci/2",
      "LOG_PUBLIC_KEY=transparency.dev-aw-ftlog-ci-2+f77c6276+AZXqiaARpwF4MonOxx46kuiIRjrML0PDTm+c7BLaAmT6",
      "APPLET_PUBLIC_KEY=transparency.dev-aw-applet-ci+3ff32e2c+AV1fgxtyjXuPjPfi0/7qtBEB1PGGxCyxqr6zppoLoz3",
      "OS_PUBLIC_KEY1=transparency.dev-aw-os1-ci+7a0eaef3+AcsgvrmrcKIBs21H2Bm2fWb6oFWn/9MmLGNc6NLtyzeQ",
      "OS_PUBLIC_KEY2=transparency.dev-aw-os2-ci+af8e4114+AbBJk5MgxRB+68KhGojhUdSt1ts5GAdRIT1Eq9zEkgQh",
      "REST_DISTRIBUTOR_BASE_URL=https://api.transparency.dev/ci",
      "BEE=1",
      "DEBUG=1",
      "SRK_HASH=b8ba457320663bf006accd3c57e06720e63b21ce5351cb91b4650690bb08d85a"
    ]
  },
  "output": {
    "firmware_digest_sha256": "1LPLT5T02+Ln71cByKhVvNFyAL47Iz00SGoXNKVSCvU="
  }
}

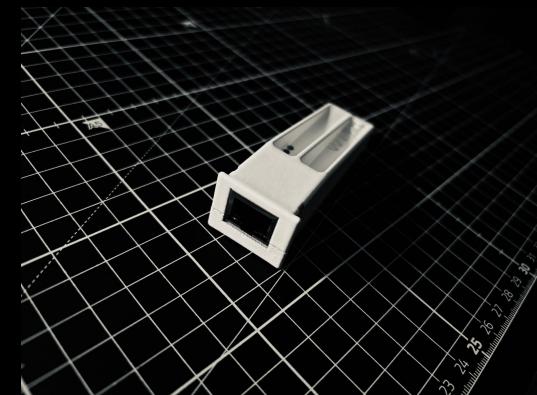
-- transparency.dev-aw-applet-ci
P/MuLoFW8473+PNMa58SZA2/rw1aEaIaLTw/aNfdawSiyFEcDjGksYqCTFMnHHGAhhbfnITkkktL1...
```

The entire Software Bill of Materials (SBOM) can be managed with Go ecosystem tools (e.g. go.mod + go.sum, go mod graph).

```

boot: tamago/arm • i.MX6UL
boot: starting kernel@1007dee8
os: tamago/arm • TEE security monitor (Secure World system/monitor)
os: loading applet from MMC storage
os: SM applet verification pub:RWQiFth4tAgsVQT5caaZGJGgUzZFnwCdeVHe5XpobGWc9XzCJmjJ56t0
os: SM applet loaded addr:0x20000000 entry:0x20081700 size:15323136
os: SM applet started mode:USR sp:0x30000000 pc:0x20081700 ns:false
ta: tamago/arm • TEE user applet
ta: SM starting network
ta: SNVS - Deriving hardware key
ta: Opening storage - CardInfo: {BlockSize:512 Blocks:8388608}
os: SM registering applet event handler g:0x21803900 p:0x21826000
ta: MAC:26:76:04:d4:4d:db IP:10.0.0.1/24 GW:10.0.0.0/24 DNS:8.8.8.8:53
ta: Starting witness...
ta: TA starting ssh server (SHA256:IH0WYxV66dvixx0PDhAalAvWg+sQC...) at 10.0.0.1:22
ta: Feeder "go.sum database tree" goroutine started
ta: Feeder "Armory Drive Prod 2" goroutine started
ta: Feeder "rekor.sigstore.dev - 3904496407287907110" goroutine started
ta: Feeder "rekor.sigstore.dev - 2605736670972794746" goroutine started
ta: Feeder "developers.google.com/android/binary_transparency/0" goroutine started
ta: "sum.golang.org: go.sum database tree" grew - @0: → @19895786: 3c9b8f49f56cb...
ta: "armory-drive-log: Armory Drive Prod 2" grew - @0: → @2: 02a14ca4a7313d868a4...
ta: "rekor.sigstore.dev: 2605736670972794746" grew - @0: → @36541297: 0d491271b9...
ta: "pixel_transparency_log: ./binary_transparency/0" grew - @0: → @235: f98458...
ta: "rekor.sigstore.dev: 3904496407287907110" grew - @0: → @4163431: 4d006aa46ef...
ta: "lvfs: lvfs" grew - @0: → @12749: 6ac429c550b8b28b7c65b6e61c99c9c76303d14012...
ta: No checkpoint

```



\$ ssh 10.0.0.1

TA tamago/arm (go1.22.1) • TEE user applet (User Mode)

date	(time in RFC339 format)?	# show/change runtime date and time
dns	<fqdn>	# resolve domain (requires routing)
exit, quit		# close session
hab	<hex SRK hash>	# secure boot activation (*irreversible*)
help		# this help
led	(white blue yellow green) (on off)	# LED control
mmc	<hex offset> <size>	# MMC card read
reboot		# reset device
stack		# stack trace of current goroutine
stackall		# stack trace of all goroutines
status		# status information

> status

----- Trusted Applet -----

Runtime: go1.22.1 tamago/arm

----- Trusted OS -----

Serial number: 3bee6cda358f0c33

Secure Boot: false

Revision: 70ffeda

Build: lcars@lambda on 2024-03-21 08:50:01

Version: 1696495801 (2024-03-21 08:50:01 +0000 UTC)

Runtime: go1.22.1 tamago/arm

Link: true

Witness/Identity: DEV:ArmoredWitness-still-tree+e1f17ea8+AZvDSL1C0...

Witness/IP: 10.0.0.1

```

$ ssh armory
tamago/arm (go1.22.1) • 4112c8d lcars@lambda on 2024-03-08 12:06:34 • i.MX6UL 528 MHz

ntp      <host>                                # change runtime date and time via NTP
tailscale <auth key> (verbose)?                # start network servers on Tailscale tailnet
witness
wormhole (send <path>|recv <code>)          # transfer file through magic wormhole

> ntp time.google.com
2024-03-08T12:06:58Z
> witness
starting omniwitness on :8080 (tamago-example-ephemeral-witness+599e290c+Afszq5oPlHBvi/cyjEkGGGHS+r96hhedJK4C71BdqP1G)

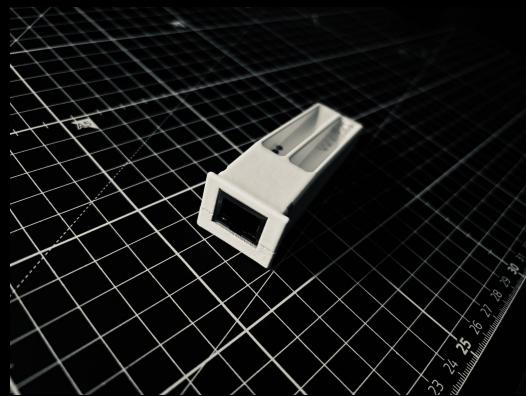
I0308 12:07:04.123431 Feeder "lvfs" goroutine started
I0308 12:07:04.135195 Feeder "go.sum database tree" goroutine started
I0308 12:07:04.142646 Feeder "Armory Drive Prod 2" goroutine started
I0308 12:07:04.148453 Feeder "rekor.sigstore.dev - 3904496407287907110" goroutine started
I0308 12:07:04.157244 Feeder "developers.google.com/android/binary_transparency/0" goroutine started

> witness
Armory Drive Prod 2 2 AqFMpKcxPYaKTmihsFbQvb758iSzJvvJBX5thVJ7r/k=                                // log checkpoint root hash
- armory-drive-log FlQbj/vNC0bZUS8GUCMAwA4A3GOMRU+ZkhVTpmGXTuFST75f2v92/021lu6euikoSbFTlzMmCNDT6wor5VB/X5z91bMAA= // log signature
- tamago-example-ephemeral-witness 6SKM5Gj/6mUAAAALhy5ei/8YVJBYTxZWSP8p2ST+868EDQkXUtz40JRSwtz1Y0qQ4W6o4g+vneP9rNkiEAN/gQGXGqd9Jz2HQ5JDw==

rekor.sigstore.dev - 3904496407287907110 4163431 TQBqpG78tcfdudkAsSE3VMUMySucNAXGw1YdnWovMjk=
- rekor.sigstore.dev wNI9ajBGAiEA8NoGSE0tPoD0tk5kNKQdM4Sxv4L5551vMsbvavFkD1ICIQDW1QsPAS1jGQAqjwOqpWft0m+Iw5P/Kd2ImoUdMgez4g==
- tamago-example-ephemeral-witness 6SKM5Gj/6mUAAAAPv8+I7roAQgSLMOxxIJ7jZ32mAttz1m1ZfiLyqogUjni59h478pruCKgJbK5sDzQL9JFeVygO3G4te5bkHDg==

developers.google.com/android/binary_transparency/0 324 vSoSuFDnfUfaKNJne2AtZjQD1CPORv+BLqnSXJE4phE=
- pixel_transparency_log csh42zBFAiEAK7GYrVxnXVZW9UDGMk3vdEOwbHB12EMUZ0XQ0zz7e3MCIDH0puL0uvx305pyHW132jJd1ldkClzcS/VUVtbMogsE
- tamago-example-ephemeral-witness 6SKM5Gj/6mUAAAAdGRDGrZ5UQwnanHsDWhUqpzXY8+6RqTvQczrdMtHr1pJP8nE6G6NvKzWJhcnc0BkCDITSWbSAqAaA6EEHDg==
go.sum database tree 23470203 fA/vQxRCMYYCzwGMoSaYipBC0tT0NEv4IpQgL8yCr8=
- sum.golang.org Az3grl+gARGNedljWIgZ62fx334EBbUFiPdkuoEZIVqgKKWccG0L9GXKzNBUNGK6MAUx4b5/f3ogAfftTCRV05W54Qa=
- tamago-example-ephemeral-witness 6SKM5Gn/6mUAAAAADpA/9FTe0cuJtPoztsjc+ksmieezmxhzF+y1+8TMgCbMjBEumBuraML92PLu10TNriB6ocf/pPkwyBzsIAA==

Lvfs 14770 TVcAhxR0NKIKoKwGAsVSouDSiQ1o21A9LT1ZPZYMnSY=
- lvfs eQjRQuZh1vVt9r1JR1xYMHtbBz9xtL/tiRdrOnkakyycwJkKLvoAA2PGUsWi0uwxFdxTgb1l5VijHQX3ddTos204=
- tamago-example-ephemeral-witness 6SKM5Gn/6mUAAAАЗ/uaA9ptWcQKdrt3esieSL9XjAQQmKFVolEomVTKD+gUYXxVQY6GHK0KbuQdeIAGEyBiGGuQF2xJyJih7NvoBA==
```



```
$ sudo ./provision --template=ci
...
Fetching TRUSTED_OS bin from "f2a54c9ff38f27b92afe9f0db6794d528e34cf508e60f90d7399f87f6f8143b1"
Fetching TRUSTED_APPLET bin from "a2012b90c44e8e4e48aa04f41f005813342e9f461cdf9f705e72fa1f4dd0f870"
Fetching BOOTLOADER bin from "1285e82d785723a054e2f43d19af3edf8d7f1092ef5dee58c5010753ec6f039b"
Fetching RECOVERY bin from "8271e2a8ccefb6c4df48889fcbb35343511501e3bcd527317d9e63e2ac7349e3"
⚠ OPERATOR: please ensure boot switch is set to USB, and then connect unprovisioned device
Recovery firmware is 1924096 bytes + 16384 bytes HAB signature
Waiting for device to be detected...
found device 15a2:007d Freescale Semiconductor Inc SE Blank 6UL
Attempting to SDP boot device /dev/hidraw0
Loading DCD at 0x00910000 (976 bytes)
Loading imx to 0x8000f400 (1940480 bytes)
Serial download on /dev/hidraw0 complete
Witness device booting recovery image
Waiting for block device to appear
Waiting for block device to settle...
✓ Detected device "/dev/hidraw0"
✓ Detected blockdevice /dev/disk/by-id/usb-F-Secure_USB_armory_Mk_II_720A9DEAD439211E-0:0
Bootloader firmware is 2976768 bytes + 16384 bytes HAB signature
Flashing images...
✓ os @ 0x5000
✓ bootloader @ 0x2
✓ boot config @ 0x4fb0
✓ applet @ 0x200000
✓ Flashed images
⚠ OPERATOR: please change boot switch to MMC, and then reboot device ⚡
Waiting for device to boot...
Waiting for armored witness device to be detected...
✓ Detected device "/dev/hidraw0"
✓ Witness serial number 720A9DEAD439211E found
✓ Witness serial number 720A9DEAD439211E is not HAB fused
⚠ OPERATOR: please reboot device ⚡
Waiting for device to boot...
✓ Witness ID DEV:ArmoredWitness-nameless-rain+192be1c1+AY5ob1kU0v3w4obdEBXVC0ygvNhco8wDMk0MIk1YGZdv provisioned
✓ Device provisioned!
```

```
$ docker run armored-witness-build-verifier continuous \
--log_origin=transparency.dev/armored-witness/firmware_transparency/ci/3 \
--log_url=https://api.transparency.dev/armored-witness-firmware/ci/log/3/ \
--log_pubkey=transparency.dev-aw-ftlog-ci-3+3f689522+Aa1Eifq6rRC8qiK+bya07yV1fXyP156pEMsX7CFBC6gg

No previous checkpoint, starting at 0
Running Monitor.From (0, 5]
Downloading and installing tamago 1.22.0
Installed tamago 1.22.0 at /usr/local/tamago-go/1.22.0
Leaf index 0: ✓ reproduced build TRUSTED_APPLET@0.3.1710338359-incompatible (fc52bc11b0d543de847eed44b285acfe7eabed03) => 4f36a18f64014ee9f7c56568c8aec3bc07d9b05849d3b96c9ff3ad3e8aa721f
Leaf index 1: ✓ reproduced build TRUSTED_OS@0.3.1710338980-incompatible (90eb1cb61f0981fe1bdcf5b23b08ae8bf44bbbe) => 7f562e45ac78487679d422ec6a7adffe9e0bbbc8d4d44d3622a4978bf4c68075
Leaf index 2: ✓ reproduced build TRUSTED_APPLET@0.3.1710339913-incompatible (7a5de51228e9299b7185440c73b54c86baefb117) => f226de72b0c56c7f8443fb90112c9d5169165e038f7eb9e7bbe2a21951ce3097
Leaf index 3: ✓ reproduced build RECOVERY@0.3.1710340256-incompatible (850baf54809bd29548d6f817933240043400a4e1) => 8271e2a8ccefb6c4df48889fcbb35343511501e3bcd527317d9e63e2ac7349e3
Leaf index 4: ✓ reproduced build BOOTLOADER@0.0.1710340266-incompatible (86aef7e97bc3a96814e52b8c01bdd67e26cc837) => 1285e82d785723a054e2f43d19af3edf8d7f1092ef5dee58c5010753ec6f039b
No known backlog, switching mode to poll log for new checkpoints. Current size: 5
```

```
$ sudo ./verify --template=ci
...
Fetching RECOVERY bin from "8271e2a8ccefb6c4df48889fcbb35343511501e3bcd527317d9e63e2ac7349e3"
-----
⚠ Operator, please ensure boot switch is set to USB, and then connect device ⚡
-----
Recovery firmware is 1924096 bytes + 16384 bytes HAB signature
Waiting for device to be detected...
found device 15a2:007d Freescale Semiconductor Inc SE Blank 6UL
Attempting to SDP boot device /dev/hidraw0
Loading DCD at 0x00910000 (976 bytes)
Loading imx to 0x8000f400 (1940480 bytes)
Sending jump address to 0x8000f400
Serial download on /dev/hidraw0 complete
Witness device booting recovery image
Waiting for block device to appear
Waiting for block device to settle...
✓ Detected device "/dev/hidraw0"
✓ Detected blockdevice /dev/disk/by-id/usb-F-Secure_USB_armory_Mk_II_720A9DEAD4390E1F-0:0
Found config at block 0x4fb0
Reading 0x2d6c00 bytes of firmware from MMC byte offset 0x400
Found config at block 0x5000
Reading 0xdbd965 bytes of firmware from MMC byte offset 0xa0a000
Found config at block 0x200000
Reading 0x102a51a bytes of firmware from MMC byte offset 0x4000a000
✓ Bootloader: proof bundle is self-consistent
✓ Bootloader: proof bundle checkpoint(@10) is consistent with current view of log(@10)
✓ TrustedOS: proof bundle is self-consistent
✓ TrustedOS: proof bundle checkpoint(@10) is consistent with current view of log(@10)
✓ TrustedApplet: proof bundle is self-consistent
✓ TrustedApplet: proof bundle checkpoint(@10) is consistent with current view of log(@10)
✓ Device verified OK!
-----
⚠ Operator, please ensure boot switch is set to MMC, and then reboot device ⚡
-----
```

Performance

Go code runs (expectedly) with identical, or improved, speed compared to the same code executed under a full blown OS.

TamaGo drivers operates comparably to their Linux counterparts, no serious overhead is present and anyway absolute performance is not a main focus of the effort, which remains security oriented.

Go ECDSA testsuite ¹	TamaGo	Linux
ECDSA sign+verify p224	115 ms	116 ms
ECDSA sign+verify p256	48 ms	46 ms
ECDSA sign+verify p384	1.85 s	1.89 s
ECDSA sign+verify p521	3.48 s	3.60 s

AES-128-CBC encryption w/ DCP	TamaGo	OpenSSL (afalg)
65536 blocks for 10s	6208	4528
4096 blocks for 10s	70326	60204

Go standard libraries run with comparable performance, while TamaGo hardware drivers highlight increased performance.

¹ https://github.com/golang/go/blob/go1.22.0/src/crypto/ecdsa/ecdsa_test.go#L76



Current limitations

The TamaGo runtime is single threaded therefore:

- avoid¹ tight loops without function calls
- avoid deadlocks (e.g. do not sleep in `main()` if nothing else is happening)

Packages/applications which rely on unsupported system calls do not compile (e.g. terminal prompt packages that require `syscall.SYS_IOCTL`), though usually such packages do not make sense in the context of OS-less unikernel operations.

Importing libraries that require `cgo` can only be done with internal linking, integrating C code with `cgo` is possible as long as such code is free standing.

There is no OS, there are no users, there are no signals, there are no environment variables. **This is a feature, not a bug.**

With the exception of few limitations² Go is surprisingly adept to run on bare metal.

¹ or just force `runtime.Gosched`

² <https://github.com/usbarmory/tamago/wiki/Internals#go-application-limitations>



Applications and future

TamaGo imx6ul package supports a wide variety of i.MX6 SoC drivers,
Raspberry Pi and RISC-V support is also available.

**TamaGo lays out the foundation for development of pure Golang
HSMs,
cryptocurrency wallets,
authentication tokens,
TrustZone secure monitors,
and much more...**

It is our policy to keep comments and references (document title and page number) for all low level interactions within drivers.

TamaGo source code is a great tool to learn low level SoC development!



What have we¹ learned?

Bare metal applications can play a big role in the future of secure embedded systems and can be built by **reducing complexity**.

We feel the need for a paradigm shift and think there is no place for C code in complex drivers or applications anymore.

Go is a language that, among others, can definitely play a role in this.

To achieve trust we proved that Go distribution modifications can be minimal to achieve bare metal execution.

We completely **killed C²**.

It's all about enabling choice and building trust.

¹ "We" as in the authors, but maybe the audience as well.

² The SoC boot ROM jumps directly to Go runtime.



USB armory

Repository: <https://github.com/usbarmory/usbarmory>

Documentation: <https://github.com/usbarmory/usbarmory/wiki>

HAB/OTP tool: <https://github.com/usbarmory/crucible>

TamaGo

Repository: <https://github.com/usbarmory/tamago>

Documentation: <https://github.com/usbarmory/tamago/wiki>

API: <https://pkg.go.dev/github.com/usbarmory/tamago>

Example:

GoTEE

Repository: <https://github.com/usbarmory/GoTEE>

Documentation: <https://github.com/usbarmory/GoTEE/wiki>

Example: <https://github.com/usbarmory/GoTEE-example>

Armored Witness

Repository: <https://github.com/transparency-dev/armored-witness>

Bootloader: <https://github.com/transparency-dev/armored-witness-boot>

OS: <https://github.com/transparency-dev/armored-witness-os>

Applet: <https://github.com/transparency-dev/armored-witness-applet>

TamaGo

Bare metal Go for ARM/RISC-V SoCs

Secure embedded unikernels
with drastically reduced attack surface

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