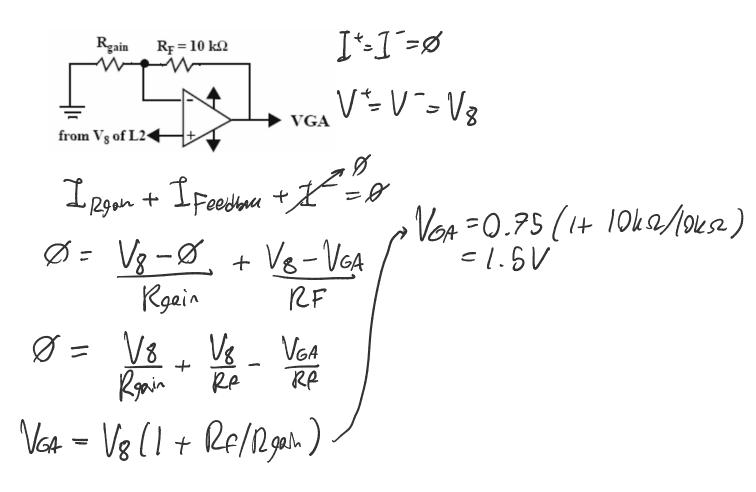
PREPARATION:

Assume device parameters: $K_n = 0.6 \text{ mA/V2} = K_p$, $V_{TN} = 1.5 \text{ V}$, $V_{TP} = -1.5 \text{ V}$. **P1**. For the circuit of **L2**, calculate the numeric value of V_8 .

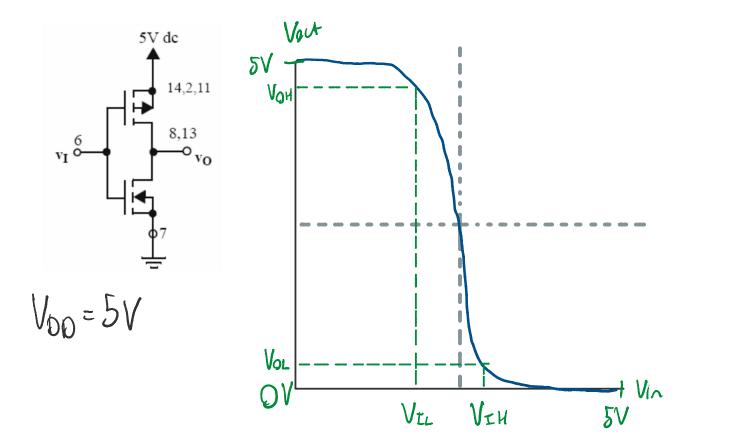
$$\begin{array}{ll}
V_{GS} &= V_{OO} - I_{O} P_{O} \\
V_{GS} &= 9 V - J_{O} SOOKR \\
I_{D} &= \frac{V_{D}}{2} (V_{GS} - V_{TN})^{2} \\
\frac{2I_{D}}{V_{D}} &= (V_{GS} - V_{TN})^{2} \\
\frac{f_{O}}{O\cdot 3} &= (9 - I_{O} 500K - 1.5)^{2} \\
I_{D} &= (7.5 - I_{O} 500K)^{2} \cdot 0.3 \\
I_{D} &= 1.5 \mu A \\
V_{S} &= I_{O} R_{D} &= 0.75 V
\end{array}$$

P2. For the circuit of L4, calculate the numeric value of VGA for case (ii).

$$R_{gain} \quad R_{F} = 10 \text{ k}\Omega$$



P3. For the circuit of **L8**, *neatly* plot the voltage transfer characteristic.



VIL VIH 5V