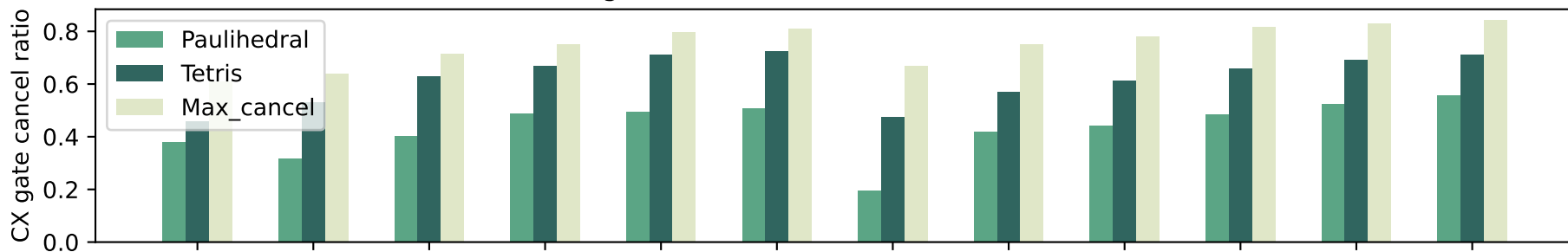
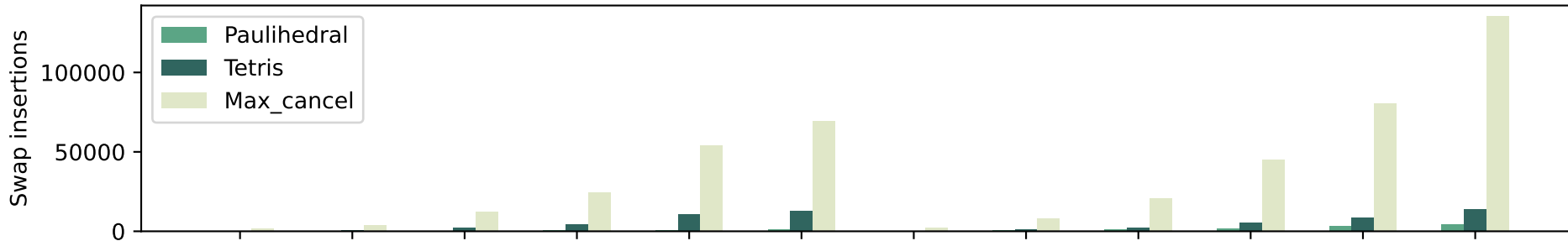


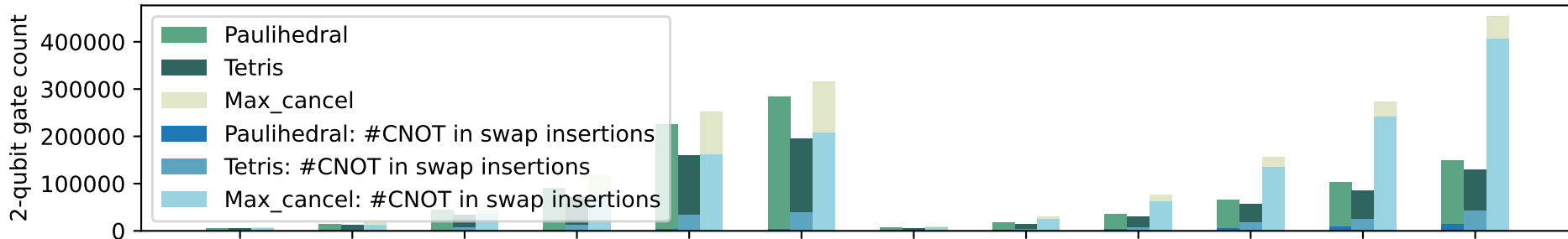
CX gate cancel ratio across Benchmarks



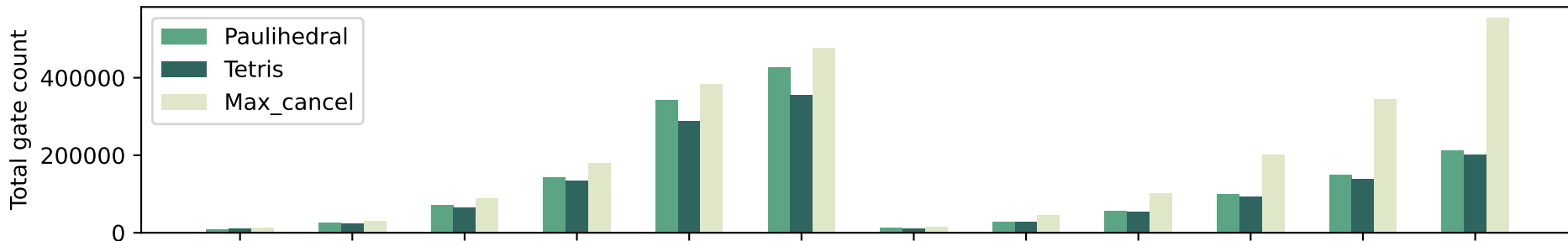
Swap insertions across Benchmarks



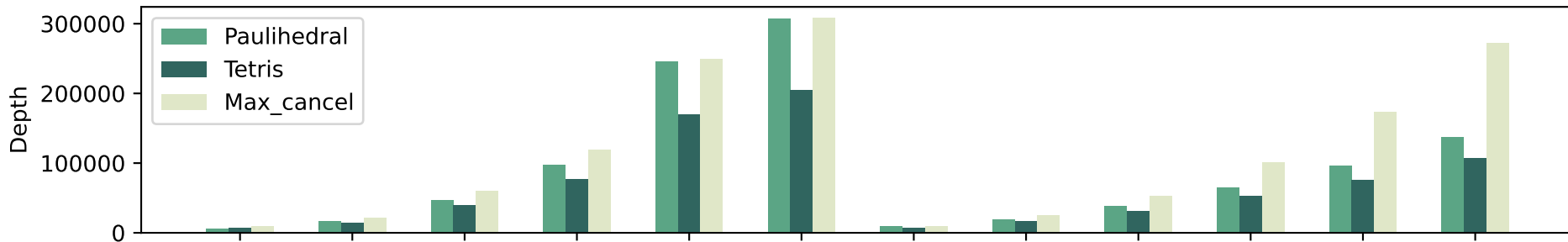
2-qubit gate count across Benchmarks



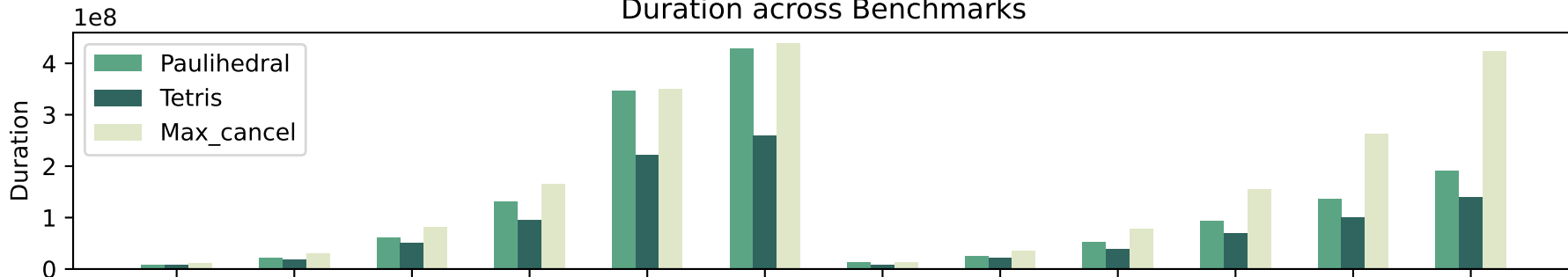
Total gate count across Benchmarks



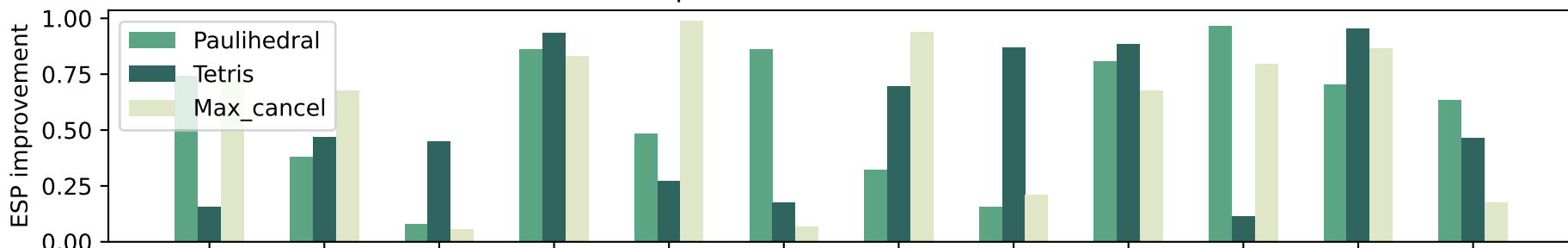
Depth across Benchmarks



Duration across Benchmarks



ESP improvement across Benchmarks



Benchmarks