

VARIABLE GAIN AMPLIFIER

Design of a stable VGA with high GBW

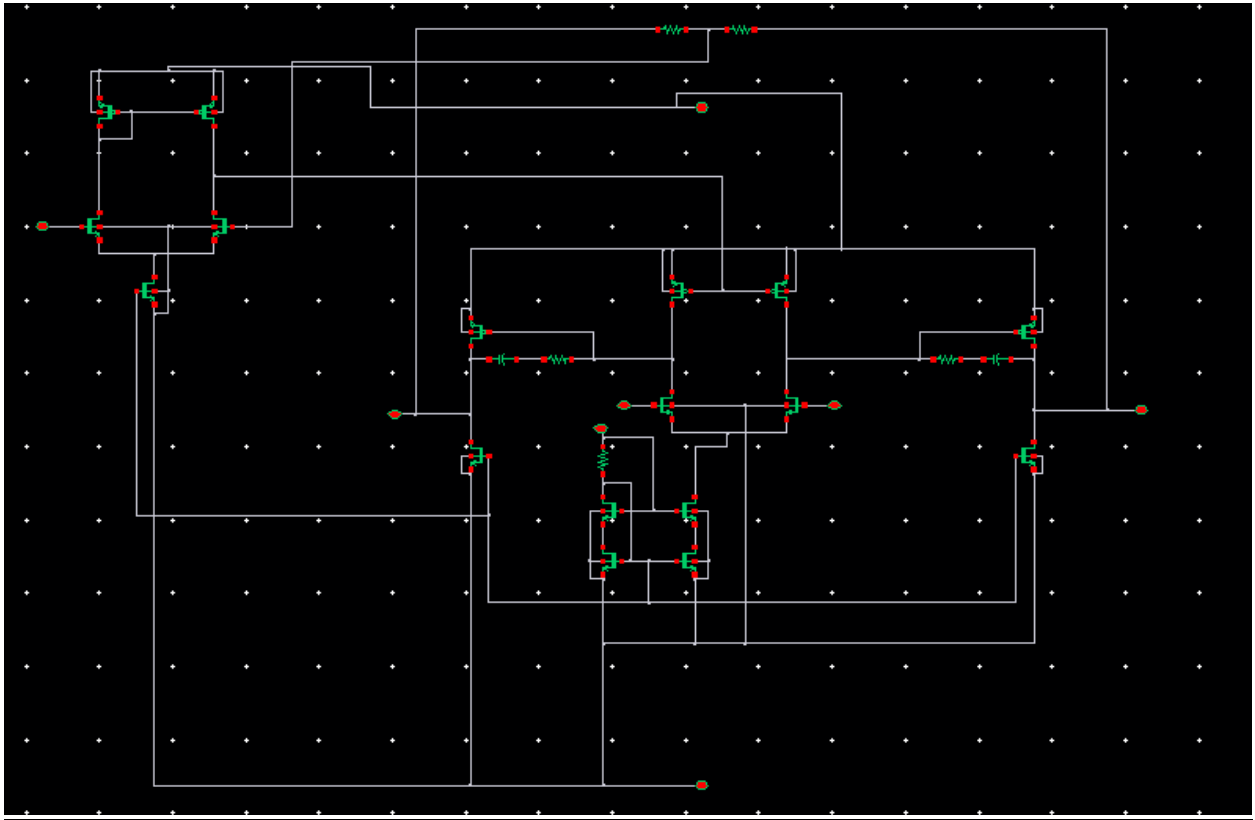


Group 12

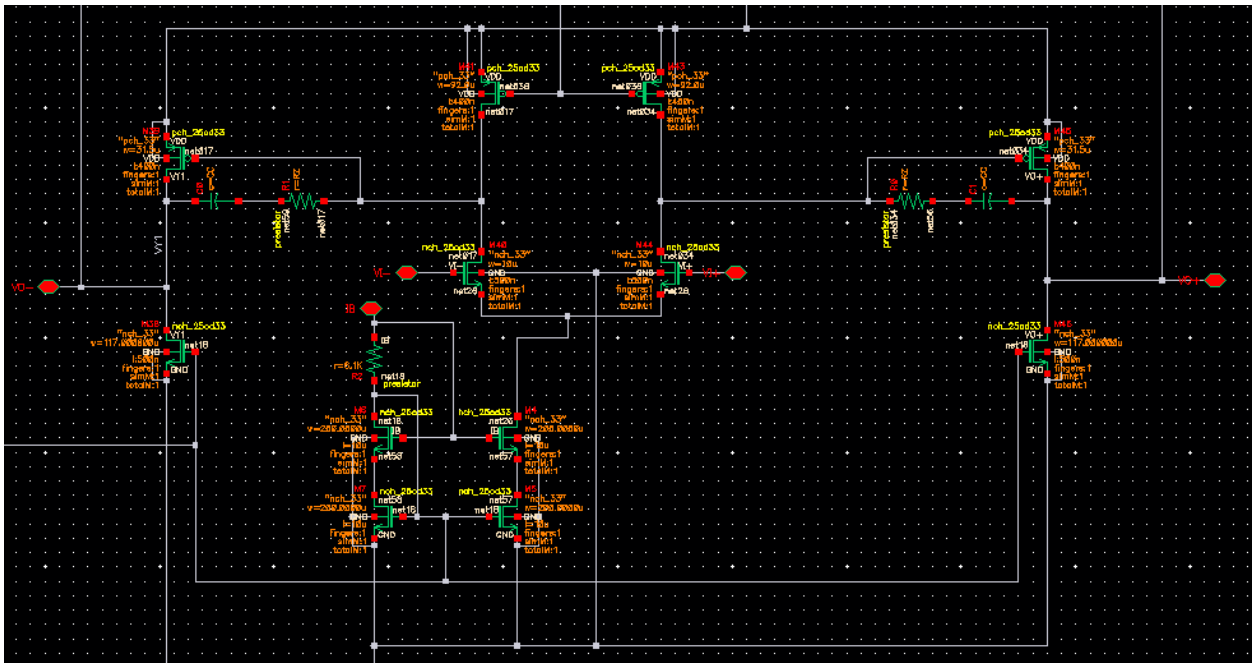
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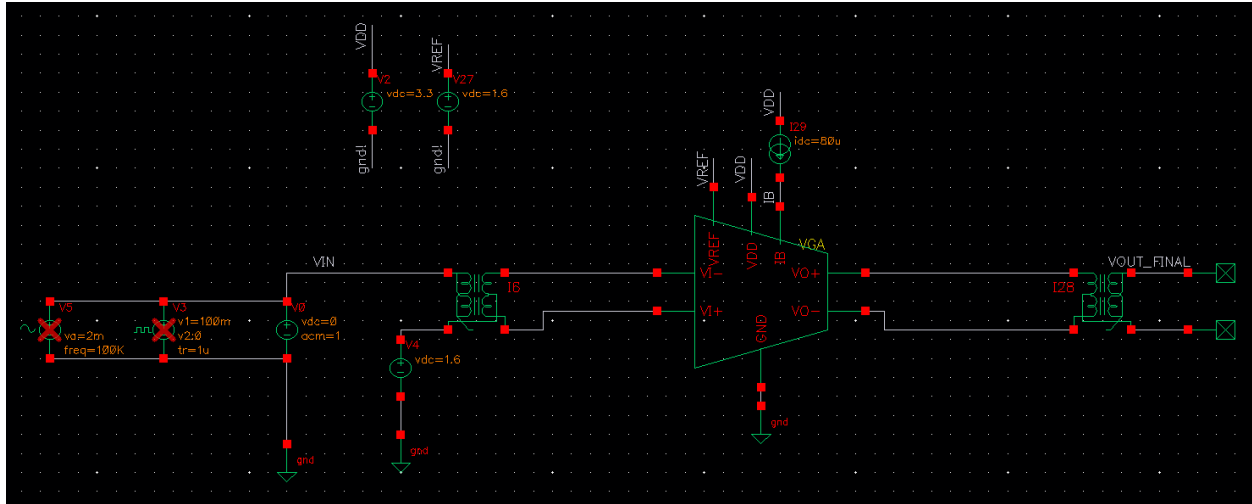
Complete Design :



The Diff OTA:



Open Loop Simulation



Test	Output	Nominal
AIC_Tasks2:OPENLOOP:1	VOUT	
AIC_Tasks2:OPENLOOP:1	AOL	1.058k
AIC_Tasks2:OPENLOOP:1	AOL_dB	60.49
AIC_Tasks2:OPENLOOP:1	BW_OL	661.2k
AIC_Tasks2:OPENLOOP:1	GBW_OL	701.4M
AIC_Tasks2:OPENLOOP:1	UGF	1.651G
AIC_Tasks2:OPENLOOP:1	PM	-111.5

Parameters	Value	Parameters	Value
gm	687.9u	gm	2.596m
gds	3.656u	gds	229.1u
id	80u	id	320u
cgg	8.728p	cgg	37.11p
cdd	225.2f	cdd	8.06p
css	9.187p	css	37.28p

Parameters	Value	Parameters	Value	Parameters	Value	Parameters	Value
gm	2.165m	gm	1.027m	gm	6.893m	gm	2.339m
gds	24.94u	gds	5.737u	gds	29.23u	gds	42.14u
id	-160u	id	160u	id	684.6u	id	-684.6u
cgg	191.4f	cgg	27.88f	cgg	315.1f	cgg	76.07f
cdd	27.25f	cdd	2.437f	cdd	27f	cdd	8.35f
css	155.5f	css	25.96f	css	295.8f	css	70.99f

Hand analysis:

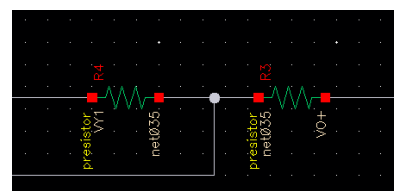
$$A_{OL} = g_{m1}(r_{o1} \parallel r_{o2}) * g_{m3}(r_{o3} \parallel r_{o4}) = 1066 = 60.55 \text{ dB}$$

$$BW_{OL} = \frac{1}{2\pi R_{out1}(g_{m3}R_{out2}C_c + C_2)} = 699 \text{ KHz}$$

Numbers agree with simulationsX

The schematic illustrates a 2.5GHz CMOS PLL. Key components and their parameters are as follows:

- Transistors:**
 - M15, M16:** $id = -1.711u$, $vgs = -1.424$, $vds = -1.424$, $yth = -616.3m$, $vdsat = -674.2m$, $gm = 3.926u$, $ROUT = 113.9M$, $cgg = 7.511f$.
 - M1, M2:** $id = 1.711u$, $vgs = 821.5m$, $vds = -1.008$, $yth = 735.6m$, $vdsat = 140.5m$, $gm = 21u$, $ROUT = 8.43M$, $cgg = 1.29f$.
 - M3, M4:** $id = 1.711u$, $vgs = 821.5m$, $vds = -1.008$, $yth = 735.6m$, $vdsat = 140.5m$, $gm = 21u$, $ROUT = 8.43M$, $cgg = 1.29f$.
 - M5, M6:** $id = 1.711u$, $vgs = 821.5m$, $vds = -1.008$, $yth = 735.6m$, $vdsat = 140.5m$, $gm = 21u$, $ROUT = 8.43M$, $cgg = 1.29f$.
 - M7, M8:** $id = 1.711u$, $vgs = -1.424$, $vds = -1.424$, $yth = -616.3m$, $vdsat = -674.2m$, $gm = 3.926u$, $ROUT = 113.9M$, $cgg = 7.511f$.
 - M9:** $id = 3.421u$, $vgs = 759.3m$, $vds = 778.5m$, $yth = 567.1m$, $vdsat = 197.2m$, $gm = 28.22u$, $ROUT = 4.704M$, $cgg = 3.948f$.
- Capacitors:**
 - C22:** $cap = 4.82f$.
- Other components:**
 - V30, V29:** Voltage sources.
 - I37, I38:** Current sources.
 - VB:** Bias voltage source.

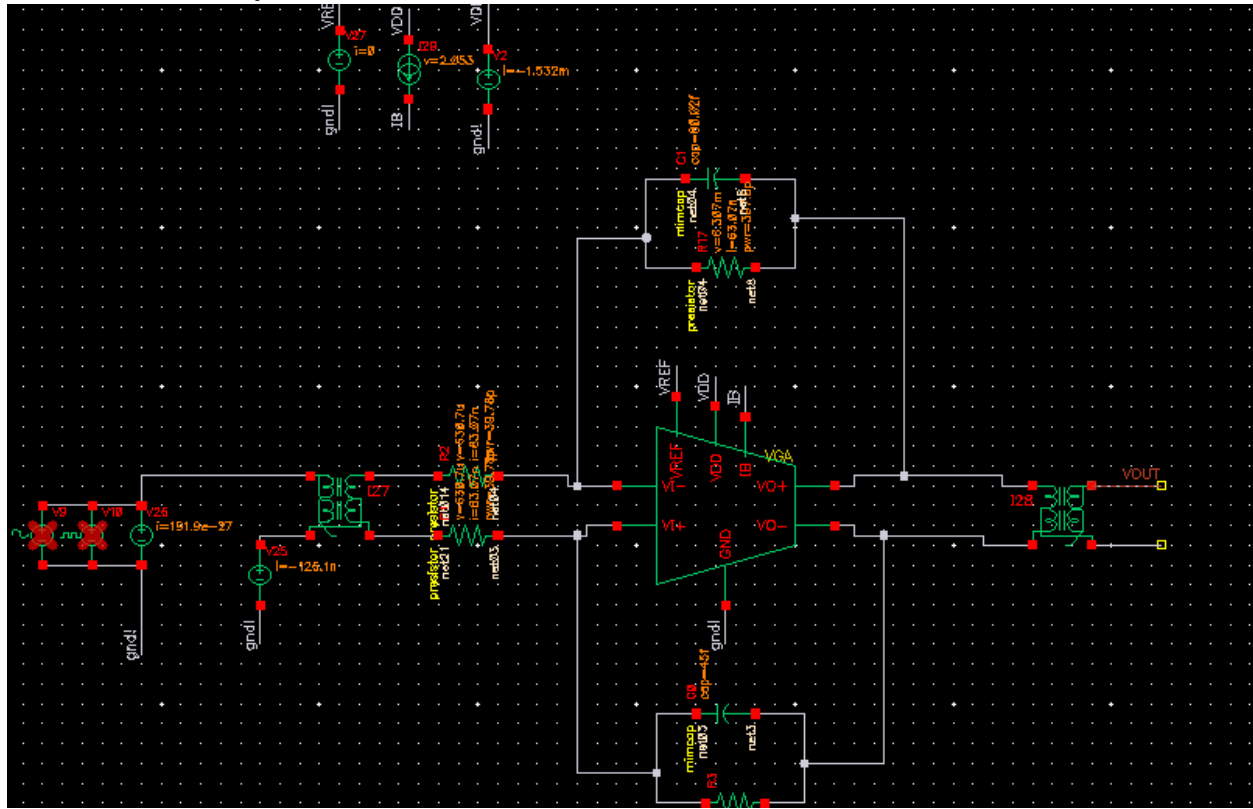


nch_250d33
V0+
net18
GND

M46
Id=894.5uA
W=753.5um
SND
vds=1.593V
vb=-616.8mV
Vgsat=151.0mV
gm=0.892mA/V
rout=24.17kΩ
cgg=315.fF

Vref =1.6DC level of output : VDS = 1.593V

Feedback Impedance Values



C2 for cutoff at 20Mhz : $C2=46f$ cutoff freq = 22Mhz

$R2=100k$ ohm

Discussion about values of R1:

We need the gain to change between these values (20dB 30dB 40dB 50dB 60dB)

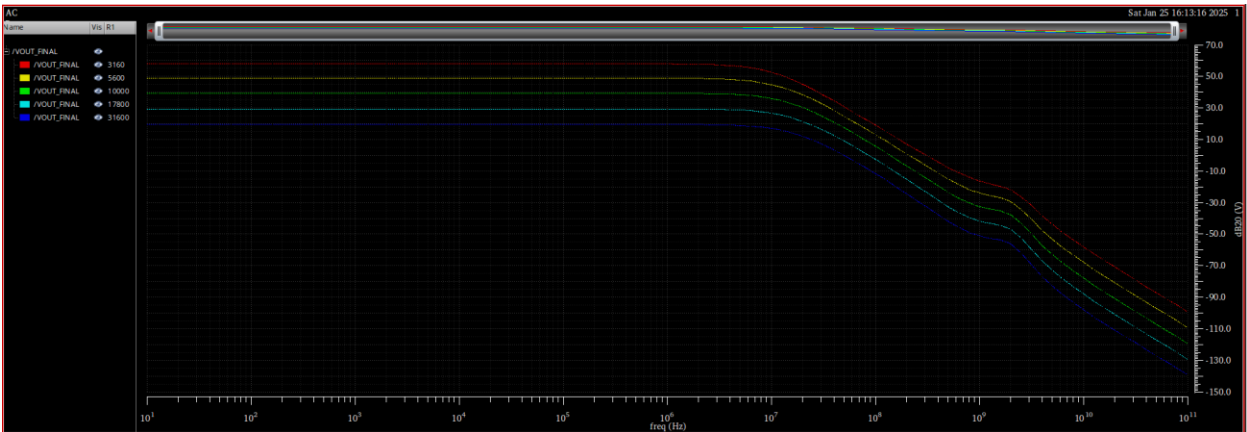
And they correspond to (10 31.6 100 316 1000)

By dividing each gain on our two amplifiers we get (3.16 5.6 10 17.77 31.6)

As we chose $R2=100k$ ohm this would mean that the values of R1 are (31.6k 17.8k 10k 5.6k 3.16k)

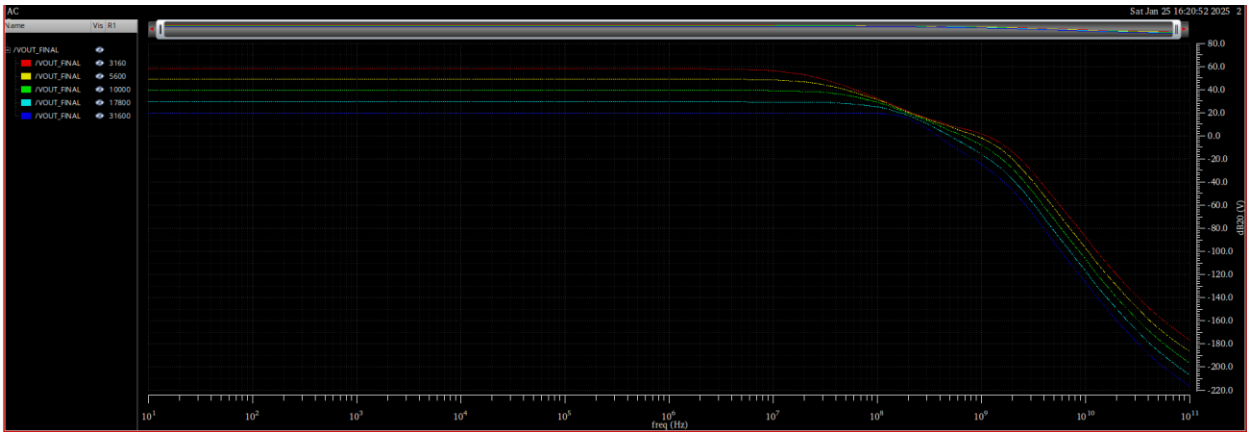
Frequency Response with and without C2

With C2



Point	Test	Output	Nominal
Parameters: R1 =3.16k			
1	AIC_Tasks2:playground:1	VOUT	
1	AIC_Tasks2:playground:1	ACL	58.4
1	AIC_Tasks2:playground:1	BWCL	6.918M
Parameters: R1 =5.6k			
2	AIC_Tasks2:playground:1	VOUT	
2	AIC_Tasks2:playground:1	ACL	49.37
2	AIC_Tasks2:playground:1	BWCL	8.273M
Parameters: R1 =10k			
3	AIC_Tasks2:playground:1	VOUT	
3	AIC_Tasks2:playground:1	ACL	39.68
3	AIC_Tasks2:playground:1	BWCL	9.535M
Parameters: R1 =17.8k			
4	AIC_Tasks2:playground:1	VOUT	
4	AIC_Tasks2:playground:1	ACL	29.82
4	AIC_Tasks2:playground:1	BWCL	10.5M
Parameters: R1 =31.6k			
5	AIC_Tasks2:playground:1	VOUT	
5	AIC_Tasks2:playground:1	ACL	19.92
5	AIC_Tasks2:playground:1	BWCL	11.13M

Without C2

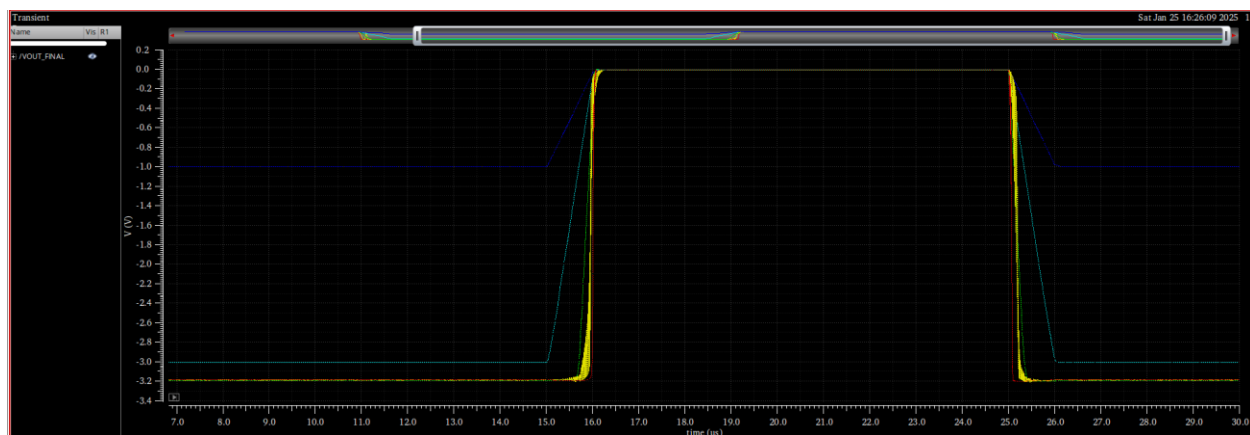
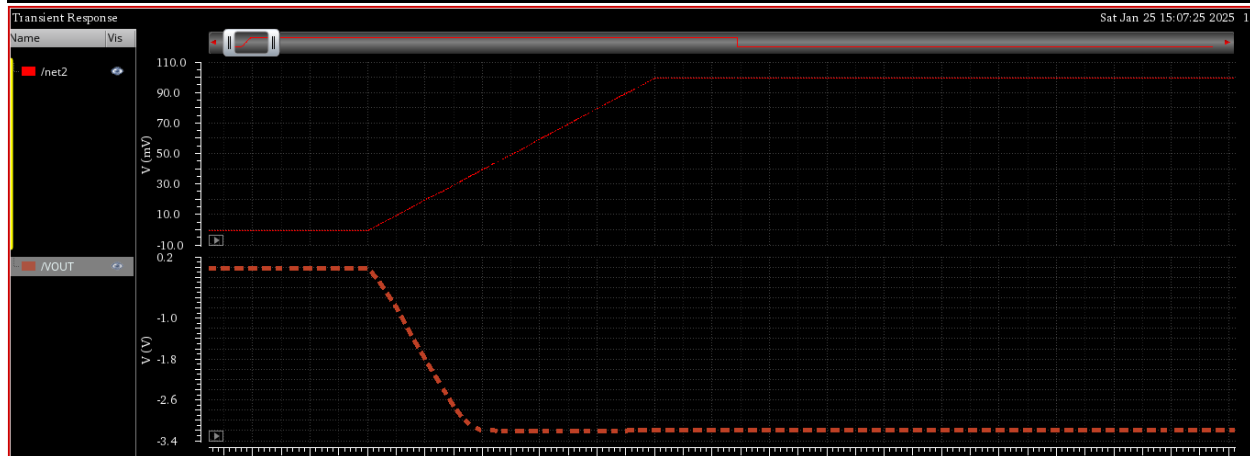
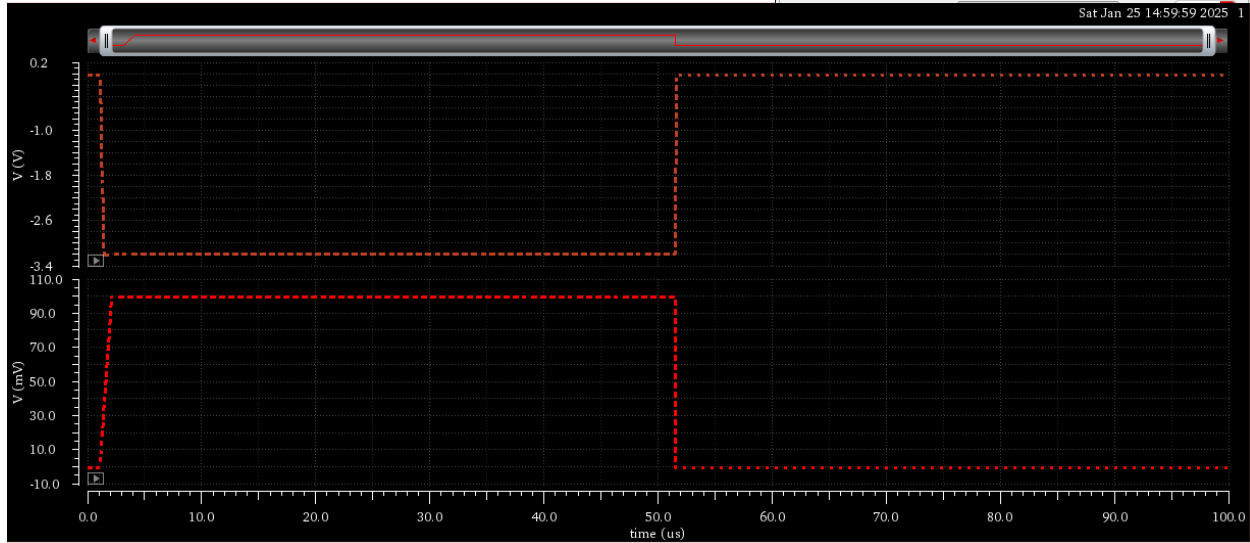


Point	Test	Output	Nominal
Parameters: R1 =3.16k			
1	AIC_Tasks2:playground:1	VOUT	
1	AIC_Tasks2:playground:1	ACL	58.4
1	AIC_Tasks2:playground:1	BWCL	14.08M
Parameters: R1 =5.6k			
2	AIC_Tasks2:playground:1	VOUT	
2	AIC_Tasks2:playground:1	ACL	49.37
2	AIC_Tasks2:playground:1	BWCL	23.39M
Parameters: R1 =10k			
3	AIC_Tasks2:playground:1	VOUT	
3	AIC_Tasks2:playground:1	ACL	39.68
3	AIC_Tasks2:playground:1	BWCL	41.6M
Parameters: R1 =17.8k			
4	AIC_Tasks2:playground:1	VOUT	
4	AIC_Tasks2:playground:1	ACL	29.82
4	AIC_Tasks2:playground:1	BWCL	81.44M
Parameters: R1 =31.6k			
5	AIC_Tasks2:playground:1	VOUT	
5	AIC_Tasks2:playground:1	ACL	19.96
5	AIC_Tasks2:playground:1	BWCL	174.7M

Transient Simulation

At gain = 100

Voltage 1	0 V	off
Voltage 2	100.0m V	off
Period	100u s	off
Delay time	1u s	off
Rise time	1u s	off
Fall time	1n s	off



X