

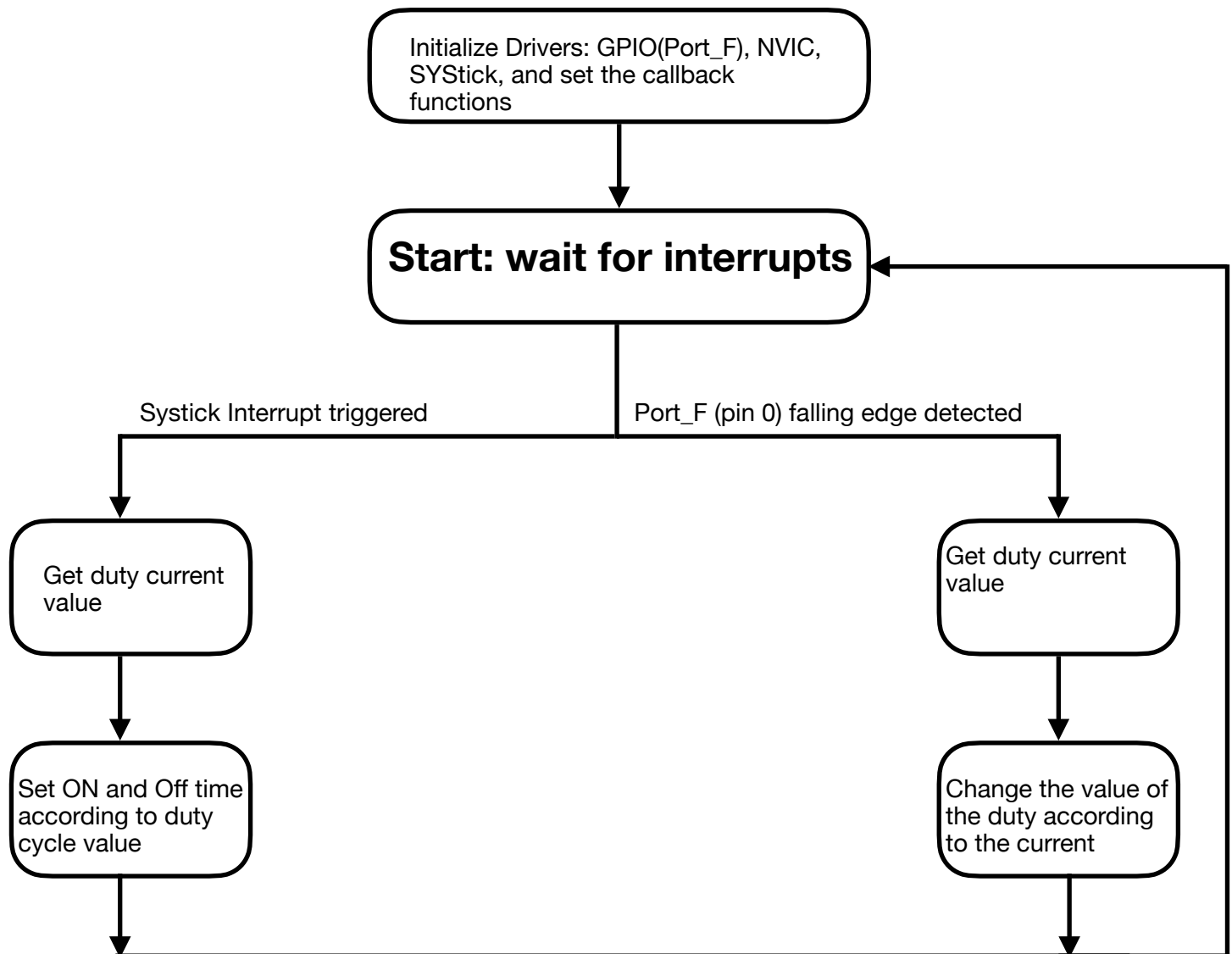
FWD Embedded Advanced Nano-degree Project 1

Led Control using TM4C123GH6PM

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Block diagram



Note: the GPIOF and Systick interrupts are in the same priority group therefore no preemption will occur; ie, if falling edge is detected while ISR of Systick is active the duty cycle will not change until Systick ISR finishes execution.