Cairo University

Faculty of Engineering
Computer Departement
3rd year

Architecture Project - Tomasulo Processor

WorkLoad

Component	Load
Instruction Queue	Ahmed Ibrahim - Yasmeen Ahmed
Decoding Circuit	Ahmed - Abdelrahman-Yasmeen - Reem
Register File	Ahmed Ibrahim - Yasmeen Ahmed
Reorder Buffer	Ahmed Ibrahim - Yasmeen Ahmed
Program Counter Controller	Ahmed Ibrahim - Yasmeen Ahmed
ALU Reservation Stations	Abdelrahman Mohammed - Reem Ashraf
Load Buffers	Abdelrahman Mohammed - Reem Ashraf
Reservation Station	Abdelrahman Mohammed - Reem Ashraf
Memory Unit	Abdelrahman Mohammed - Reem Ashraf
Assembler	Reem Ashraf

Team 12

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