

**ABDUL MANAF F**Course : **M.Tech.**, Electronics Engineering, 2021

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CGPA : 8.19

**ACADEMIC DETAILS**

| COURSE    | SPECIALIZATION                                     | INSTITUTE/COLLEGE                                    | BOARD/UNIVERSITY                         | SCORE     | YEAR |
|-----------|--|--|--|-----------|------|
| UG        | Electronics & Electrical Communication Engineering | Rajiv Gandhi Institute Of technology Kottayam Kerala | APJ Abdul kalam Technological University | 7.82 CGPA | 2019 |
| CLASS XII |  | Kendriya Vidyalaya Delhi Cantt                       | CBSE                                     | 83.4 %    | 2014 |
| CLASS X   |  | Kendriya Vidyalaya Delhi Cantt                       | CBSE                                     | 9.4 CGPA  | 2012 |

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|------------------------------|---|
| <b>Subjects / Electives</b>  |   |
| <b>Technical Proficiency</b> | Java, C++ Language, Python3, Verilog, Machine Learning, Deep Learning, Digital Electronics, MATLAB, Analog Circuit Design |

**SUMMER INTERNSHIP / WORK EXPERIENCE**

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| <b>Industrial training in basic telecom technology, Regional telecom Training Centre</b> | <b>Jun 2016 - Jun 2016</b> |
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**PROJECTS**

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| <b>Devanagiri Hand Written Recognition - Deep Learning</b><br>Devanagiri scripts were recognized using CNN. The CNN was built using KERAS.  | <b>Nov 2019 - Dec 2019</b> |
| <b>OBJECT DETECTION USING DEEP LEARNING - ARTIFICIAL INTELLIGENCE</b><br>Detect specific Object using the technique of Deep learning where a CNN is used to perform the task,                           | <b>Jan 2019 - May 2019</b> |
| <b>MINIGEN - Analog Integrated Circuits</b><br>Miniature version of conventional function generator. It uses DC base signal for signal generation. It is implemented using LM324 Operational Amplifier. | <b>Nov 2017 - Dec 2017</b> |

**AWARDS AND RECOGNITIONS**

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|--|-----------------|
| <b>Secured Third Place in the ANALOG- an online contest   TEXAS INSTRUMENT</b>                     | <b>Jun 2020</b> |
| <b>Secured ELITE grade in "Introduction to modern application development"   NPTEL, IIT MADRAS</b> | <b>Jul 2017</b> |
| <b>Secured Second Position in circuit debugging competition   CET Trivandrum</b>                   | <b>Mar 2017</b> |

**CONFERENCES AND WORKSHOPS**

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| <b>ARDUINO</b><br>Organized by: <b>Technospecies Global Solution in Association with E-Cell, IIT KAHARGPUR.</b>   Date: <b>Mar 2017</b><br><b>"SPHERE DRONE DESIGN"</b><br>Organized by: <b>AEROTRIX</b>   Date: <b>Feb 2017</b><br><b>INTELLIGENT ROBOTICS</b><br>Organized by: <b>IEEE</b>   Date: <b>Mar 2016</b> |
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**LANGUAGES KNOWN**

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| Hindi, English, Malayalam |
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