

# MOHAMMAD ABDUL AFROZ

9652015372 ◇ Hyderabad, T.S

[asi.afroz2005@gmail.com](mailto:asi.afroz2005@gmail.com) ◇ [linkedin.com/in/md-abdul-afroz](https://www.linkedin.com/in/md-abdul-afroz) ◇ [github.com/abdulafroz04](https://github.com/abdulafroz04)

## OBJECTIVE

---

Undergraduate in Electronics and Communication Engineering. Highly motivated and adaptable individual with a passion for continuous learning and growth. With excellent problem-solving abilities and a keen attention to detail, I thrive in challenging environments and excel at tackling complex tasks.

## EDUCATION

---

**B.Tech in Electronics and Commuication Engineering**, JNTUH-UCESTH, Hyderabad Expected 2024  
**CGPA: 8** Aggregate

**MPC - Intermediate**, Sri Nalanda Junior College, kothagudem 2018 - 2020  
10th CGPA: 9.7/10  
Aggregate IPE percentage: 96.60Secured **1252** in TS-Eamcet out of 1.4lakh+ candidates.  
Secured **96.98** percentile in JEE Mains-2020

## SKILLS

---

<b>Technical Skills</b>	<b>Digital Logic design, Microcontroller and Microprocessor Programming Embedded Systems, Digital Signal Processing (DSP), VLSI Design Network Protocols,FPGA Programming</b>
<b>Languages</b>	<b>C, C++, Python (Programming essentials), Embedded C, Data Structures and Algorithms</b>

## EXPERIENCE

---

**Design Internship** Feb 2023 - Mar 2023  
Maven Silicon Hyderabad, Telangana

- Managed a process re-engineering project to improve and consolidate end-to-end service processes; restructured communication flow among 10 departments and cut down paperwork by 75
- During the Internship I have encountered following tasks:
  1. Learning about various devices.
  2. Functions of the devices as Master and Slave.
  3. Bridge connection for communication between devices.
  4. Various Testing conditions.
- Achieved more knowledge on the interfacing of devices using VERILOG.

**FPGA Architecture Design Internship** June 2023 - Ongoing  
Pan Tech Solutions Hyderabad, Telangana

- I have been learning FPGA design using VHDL.
- Various tasks are been assigned in the intern that provide experience in various test conditions and logic design-ing.
- Futher FPGA design using python and many more tasks will be assigned

**Embedded System and IoT Intern** August 2023 - October 2023  
EmertXe Hyderabad, Telangana

- Coding of various devices using Embedded C.
- Interfacing of devices with Automation using C Language.

## PROJECTS

---

**Multi Channel MAC Protocol:** A Multichannel Medium Access Control (MAC) Protocol enhances WSN performance by efficiently allocating and utilizing multiple communication channels. The simulation-based approach allows for testing and optimization of the protocol's parameters, leading to a deeper understanding of its behavior in different scenarios using **MATLAB** ([Try it here](#))

**AHB to APB Bridge Design:** The AHB to APB bridge is an AHB slave, providing an interface between the high speed AHB and the low-power APB. ([Try it here](#))

#### **Mini Audio Amplifier:**

1. Built a Mini Audio Amplifier using simple elements.
2. A variable resistor attached act as a amplification factor determinant.
3. It works on basis of Kirchoff's Law.

#### **Pattern Pursuit:**

1. Learnt Arduino coding for joystick, LED, pushbutton.
2. Learnt of different inbuilt functions of Arduino.
3. Usage of a joystick with the directions and angles providing sensitivity to movement.

#### **LEADERSHIP**

---

- A student and member of **IEEE Student Chapter**.
- Crew foreman for **JHUB**.
- A scholar for **FOUNDATION FOR EXCELLENCE**.
- Team Leader for **AASYA FOUNDATION**. I have been a part of the organization for more than 2 years and I have been involved in various activities. These activities have helped me in honing my communicational skills, management skills and presentation skills.

#### **CERTIFICATIONS**

---

##### **Flipkart Grid 5.0 -Software Development Track**

Certificate of Participant for entering into **Round 1.2**.

##### **Coco-Cola Disrupt Season 3**

Certificate of Participation by Coco-Cola.

##### **Introduction to IoT**

By CISCO NETWORKING ACADEMY.

02/2023

##### **Digital Signal Processing**

by COURSERA.

Jan 2023 - June 2023

##### **Programming Essentials in Python**

By CISCO NETWORKING ACADEMY.

Oct 2022 - Feb 2023