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He is an appointed member of the Administrative Committee of the IEEE Solid-State Circuits Society, a member and past chair of the Analog Signal Processing Technical Committee for the IEEE Circuits and Systems Society, and a past member and chair of the Wireline Communications subcommittee of the Custom Integrated Circuits Conference.

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Errata | Analog Integrated Circuit Design

The purpose of this Chapter is to discuss fundamental circuit building blocks. A good knowledge of these basic building blocks is critical to understanding many subjects to be discussed later. ...

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I've used "Design of Analog CMOS Integrated Circuits, 1st Ed." as the textbook for my classes since it was released. In my opinion it is the most intuitive and easy to understand of available references. Some few but important improvements were included into this second edition, mainly the form of appendices and "nanometer design notes", which ...

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