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EECE488: Analog CMOS Integrated Circuit Design Introduction and Background Shahriar Mirabbasi Department of Electrical and Computer Engineering University of British Columbia shahriar@ece.ubc.ca Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged. SM 2 EECE 488 – Set 1: Introduction and Background Marking

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Design of Analog CMOS Integrated Circuits

Analog Cc Integrated Circuit Design W/L ratios Topology DC Currents L W Circuit or systems specifications Fig. 1.1-3 The electrical design requires active and passive device electrical models for - Creating the design - Verifying the design - Determining the robustness of the design The Practice of Analog IC Design (5/13/04) Page 18

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