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Analog Electronic Circuits Laboratory Viva Questions 1 Theory_Questions: 1. The slope of VDS-IDS characteristics of a FET is controlled by a (A) VGS (B) IDS (C) VDS ... Problematic_Questions: 9. The drain resistance of a FET (Common source configuration) is, $R_D = 1\text{K}\Omega$, $g_m = 40$ milli Siemens then what is the gain of this amplifier?

Analog Electronic Circuits Laboratory Viva Questions 1 ...

Home/ lab viva questions/LICA part 1 Viva Questions on Op Amps: 1. What are the advantages of integrated circuits? · increased package density. · Cost reduction . · Increased system reliability. · Improved functional performance. · Increased operating speeds. · Reduction in power consumption.

Linear Integrated circuits lab Viva Questions Part 1 ~ ECE ...

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VIVA QUESTION FOR ANALOG COMMUNICATION

Analog Electronics Laboratory Manual - 10ESL37 Dept of ECE- GCEM Page iv LAB INSTRUCTIONS Do's Ensure your presence five minutes before the commencement of the lab. Attend all the lab sessions without fail. Come well prepared for every lab session. Complete and Bring the Lab records regularly.

Analog Electronics Laboratory Manual - 10ESL37

Dept of E&C, CEC Analog Electronics Lab Manual 10ESL37 23 Procedure: Make the Connections as shown in the circuit diagram Apply 230V AC supply from the power mains to the primary of the transformer Observe the voltage across secondary to get V_m , the peak value in CRO Use relevant formula to find V_{dc} and V_{rms}

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