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cadence . 59. What are the other alternative software apart from cadence used for VLSI design? Microwind, Tanner, Hspice, Pspice, Mentor graphics, Xilinx etc... Sushanth KJ, Asst. Pr of. Dept. of ECE, BIT, Mangaluru Page 7 VLSI Lab Viva questions and answers 60.

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VLSI Lab viva question with answers1 0 X11 1 X0D flip-flopQ(t) Q(t+1) D0 0 00 1 11 0 01 1 1T flip-flopQ(t) Q(t+1) T0 0 00 1 11 0 11 1 059. Design a BCD counter with JK flip-flopsAnswer60. Design a counter with the following binary sequence 0, 1, 9, 3, 2, 8, 4 and repeat.

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VLSI Lab viva question with answers Synchronous reset logic will synthesize to smaller flip-flops, particularly if the reset is gated with the logic generating the d-input. But in such a case, the combinational logic gate count grows, so the overall gate count savings may not be that significant.

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Electrical Circuits Lab Viva Questions and Answers Electrical Lab Edit Question No. 01: What is difference between power and energy? Answer: Energy is defined as the capacity of a physical system to perform work. In the context of electric circuits, energy (w) is related to power by the following relationship  $p = \frac{dw}{dt}$

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