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Chapter 1: Introduction Instructor: Dr. Hyunyoung Lee
Foreword (by Frank Vahid) > HDL (Hardware Description Language) based design has established itself as the modern approach to design of digital systems, with VHDL (VHSIC Hardware Description Language) and Verilog HDL being the two dominant HDLs. Numerous universities thus introduce their students to VHDL (or Verilog).

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