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EEC281 – VLSI Digital Signal Processing 4 units – Winter Quarter. Lecture: 3 hours. ... Keshab K. Parhi, VLSI Digital Signal Processing Systems: Design and Implementation. Rudra Pratap, Getting Started with Matlab. Behrooz Parhami, Computer Arithmetic, Algorithms and Hardware Design.

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The Journal of Signal Processing Systems publishes research papers on the design and implementation of signal processing systems, with or without VLSI circuits. Readers will find a wide range of topics covered within the subject areas of system design and implementation, algorithms, architectures, and applications.

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He has served as technical program cochair of the 1995 IEEE VLSI Signal Processing workshop and the 1996 ASAP conference, and as the general chair of the 2002 IEEE Workshop on Signal Processing Systems. He was a distinguished lecturer for the IEEE Circuits and Systems society during 1996-1998, and is serving a second term currently (2019-20).

Keshab K. Parhi - Electrical and Computer Engineering

Pipelining is an important technique used in several applications such as digital signal processing (DSP) systems, microprocessors, etc.It originates from the idea of a water pipe with continuous water sent in without waiting for the water in the pipe to come out. Accordingly, it results in speed enhancement for the critical path in most DSP systems.

Pipelining (DSP implementation) - Wikipedia

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