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UNIT V: SPECIFICATION USING VERILOG HDL PART -A (2 Marks ...
EC2354 –VLSI DESIGN III /VI ECE – PREPARED BY L.M.I.LEO JOSEPH Asst.Prof /ECE Page 1 UNIT III COMBINATIONAL AND SEQUENTIAL CIRCUIT DESIGN Part –A (2 Marks) 1. What is a BiCMOS? BiCMOS is a type of integrated circuit that uses both bipolar and CMOS technologies. 2.

UNIT III COMBINATIONAL AND SEQUENTIAL CIRCUIT DESIGN Part ...
2.What is the full custom ASIC design? In a full custom ASIC, an engineer designs some or all of the logic cells, circuits or layout specifically for one ASIC. It makes sense to take this approach only if there are no suitable existing cell libraries available that can be used for the entire design. 3.What is the standard cell-based ASIC design?

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