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1) Explain how logical gates are controlled by Boolean logic? In Boolean algebra, the true state is denoted by the number one, referred as logic one or logic high. While, the false state is represented by the number zero, called logic zero or logic low. And in the digital electronic, the logic high is denoted by the presence of a voltage potential.

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After having done the de-facto preparation of VLSI interview questions, you can focus more on the specific niche or the focus area that you are interviewing for, which could be verification, analog design or something else.

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Designer must how data flows between various registers of the design.Gate level: The module is implemented in terms of logic gates and interconnections between these gates. Designer should know the gate-level diagram of the design.Switch level: This is the lowest level of abstraction. The design is implemented using switches/transistors.

VLSI Interview Questions with Answers - 1

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Floorplan Interview Question and Answer. The input to a floorplanning tool is a hierarchical netlist that describes the interconnection of the blocks (RAM, ROM, ALU, cache controller, and so on); the logic cells (NAND, NOR, D flip-flop, and so on) within the blocks; and the logic cell connectors (the terms terminals, pins , or ports mean the same thing as connectors).

FLOORPLANING INTERVIEW QUESTIONS AND ANSWERS

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