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Z. Wang, Z. Chi and K. K. Parhi, "Area-Efficient High Speed Decoding Schemes for Turbo/MAP Decoders", submitted to IEEE trans. on VLSI Systems, Nov. 2000. Z. Wang and K. K. Parhi, "On-line extraction of soft decoding information and its applications in VLSI turbo codes", submitted to IEEE trans. on CAS-II, Oct. 2000.

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Brief Biography. Keshab K. Parhi received the B.Tech. (Honors) degree from the Indian Institute of Technology, Kharagpur (India) in 1982 in electrical engineering, the M.S. degree from the University of Pennsylvania in 1984 in electrical engineering, and the Ph.D. degree from the University of California, Berkeley in 1988 in electrical engineering and computer sciences.

**Keshab K. Parhi - Electrical and Computer Engineering**

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