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You get very carefully chosen 95 of the most important, most likely to be asked questions with illustrated answers when it comes to interviewing in the field of digital ASIC design, static timing analysis, and CMOS circuit design. Knowing answers to these questions will ensure that you get the job offer from your next interview. You see, it is very important to have enough confidence in ...

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1) Mention what is Payroll? Payroll is the term used for the compensation a business must pay to its employees for a set period of time or on a given date. 2)

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3. Did you have any goals when you joined the company? I wanted to offer my immense analytic experience for the benefit of the team. During the first 3 months, I put all I had into the work until I was taken to the procurement department.

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Question 1. What Is Safety ? Answer : It is a condition which gives you freedom from hazard, risk, accident which may cause injury, damage and loss to material or property damage and even death.

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sir, What is the meaning of 16 in 16 ffc technology? Is the following explanation correct? In advanced CMOS technologies, e.g. 32nm, 28 nm, these numbers usually refer to 1/2 the contacted pitch of the DRAM ,(as they are the most compact circuit which can be fabricated out) which is also 1/2 the pitch of the first level of metalization (the closest to the silicon substrate).

VLSI Concepts: VLSI Basic

As we seen in earlier blog the average power consumed by the CMOS circuit can be divided into three different components[1]. They are: 1)Dynamic (switching)power consumption

ASIC-System on Chip-VLSI Design: Dynamic (switching) power

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Specification: This is the first stage in the design process where we define the important parameters of the system that has to be designed into a specification. High level Design: In this stage, various details of the design architecture are defined.In this stage, details about the different functional blocks and the interface communication protocols between them etc. are defined.

VLSI Design - Front end vs back end - Differences and ...

My Mentor told me when ever you come to me with a question, "I also want you bring along all the possible answers, on the basis of your experience and knowledge".

Basic of Timing Analysis in Physical Design |VLSI Concepts

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