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Chapter 4 Low-Power VLSI DesignPower VLSI Design Jin-Fu Li Advanced Reliable Syy()stems (ARES) Lab. Department of Electrical Engineering National Central UniversityNational Central University

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Advanced VLSI Design Jason Stinson Intel Corporation jstinson@stanford.edu J. Stinson EE 371 Lecture 1 2 Class Overview This class builds on EE313 and EE271 to look at the circuit design issues in large digital VLSI chips. At the core of this class is the job of 'circuit design' and the tasks that a circuit designer does in the industry.

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