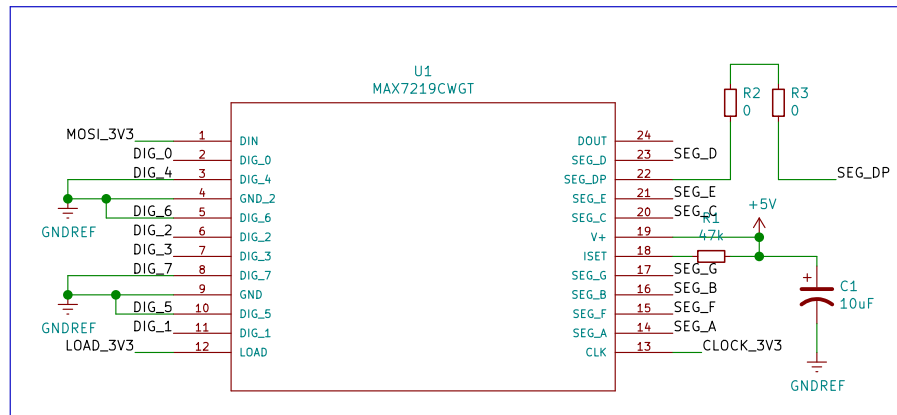
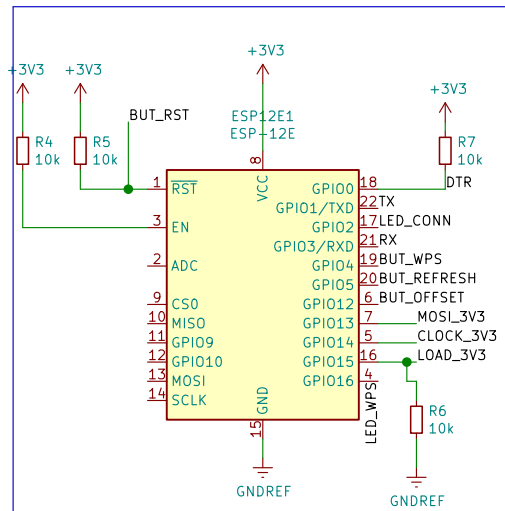


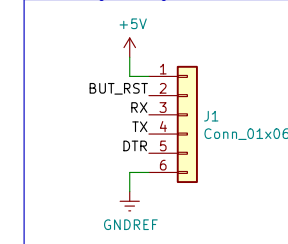
Display Driver



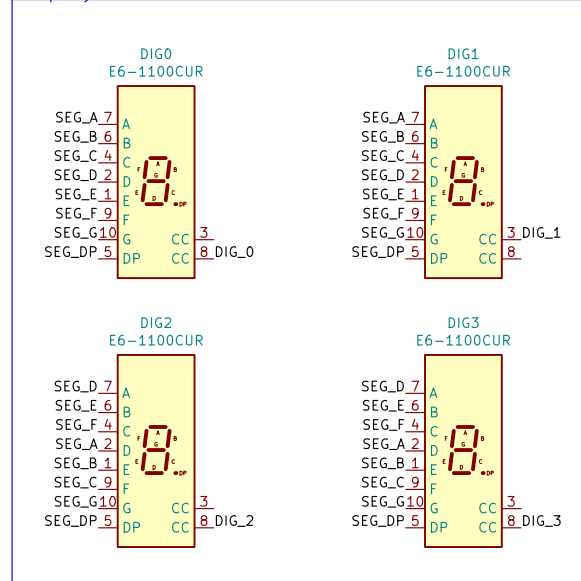
ESP12-E



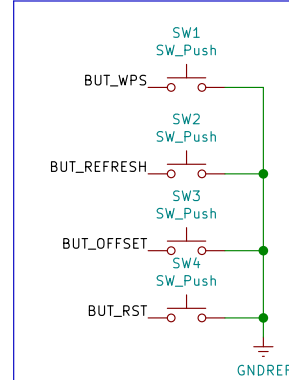
Serial Programming Interface



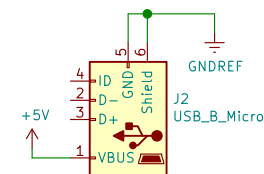
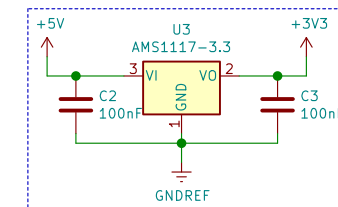
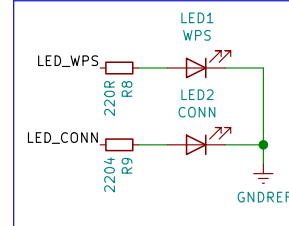
Displays



Buttons



Leds



Digital Clock with network integration using ESP12E/F module and MAX7219 display driver.
https://github.com/abdullahcanakci/network_clock

Sheet: / 1/1

File: kicad_project.sch

Title: Network Clock

Size: A4 Date: JULY 5, 2019

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