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DEPARTMENT OF ELECTRICAL AND ELECTRONIC
ENGINEERING
ELECTRICAL CIRCUIT LAB - 6

SUMMER 2022-2023

Section: W, Group: 2

LAB REPORT ON

Transient Analysis of RC Series and RL Series using PSPICE

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Submitted By

Name	ID	Contribution
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Date of Submission: August 4, 2022

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Introduction: In this experiment we apply a pulse waveform to the RC and RL series circuit to analyze the transient response of the circuit by using PSPICE simulating tool. The pulse width relative to a circuit's time constant determines how it is affected by an RC and RL circuits.

The purpose of this experiment is to

1. simulate the circuits by using components from the PSPICE library and,
2. analyze obtained graphs and results

Theory and Methodology:

Time Constant (τ): A measure of time required for certain changes in voltages and currents in RC and RL circuits. Generally, when the elapsed time exceeds five-time constants (5τ) after switching has occurred, the currents and voltages have reached their final value, which is also called steady-state response.

The time constant of an RC circuit is the product of equivalent capacitance and the Thevenin resistance,

$$\tau = R \times C \quad (1)$$

The time constant of an RL circuit is the equivalent inductance divided by the Thevenin resistance,

$$\tau = L/R \quad (2)$$

Time Period (T): Time required to complete one cycle is called Time Period or the length of each cycle of a pulse train is termed its time period (T).

Pulse width (t_p): The pulse width of an ideal square wave is equal to half of the time period.

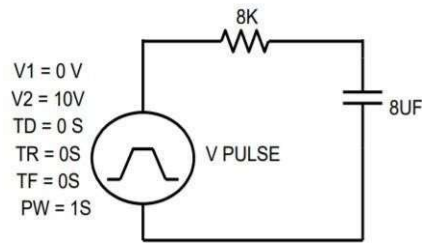


Figure-1: RC circuit

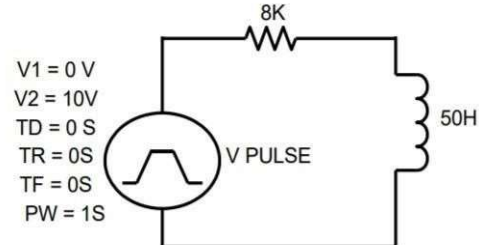


Figure2: RL circuit

Apparatus:

1. PC
2. PSPICE Simulating tools

Precautions:

Connecting of circuit should be done properly and PSPICE simulating software should be properly installed using the information provided at the manual before starting the experimental work.

Experimental Procedure:

1. Open the PSPICE Design Manager window: Start Program MicroSim Eval 8.
2. Open schematic editor: Press Run Schematics icon from the bar on the left side of the screen.
3. Select: Draw Get New Part, then select and place each of the circuit elements one by one (VPULSE for pulse type voltage source, R for resistor, C for capacitor and EGND for ground). Join the elements by using the wire as necessary.
4. Change the label and magnitude of each element by double clicking on them and editing as necessary.
5. Then go to Analysis Setup Select Transient and provide the necessary Values. And select automatically Run Probe after Simulation.
6. To execute the analysis, select: Analysis Simulate or press F11. To view the analysis result select: Analysis examine output.
7. Select Analysis Simulate.
8. Select Trace Add and select the desired traces.
9. Perform the text analysis as instructed

Data Table:

For RC series circuit,

Time (t)	Percentage change	V _c (theoretical) (V)	V _c (readings) (V)
τ	63.2%	6.32	6.32
2τ	86.5%	8.65	8.65
3τ	95%	9.50	9.50
4τ	98.2%	9.82	9.82
5τ	99.3%	9.93	9.93

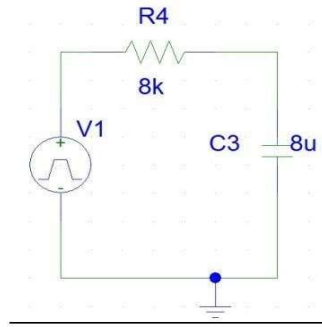
For RL series circuit,

Time (t)	Percentage change	I _L (theoretical) (μ A)	I _L (readings) (μ A)
τ	63.2%	126.4	126.4
2τ	86.5%	173	172.98
3τ	95%	190	190.06
4τ	98.2%	196.4	196.4
5τ	99.3%	198.6	198.7

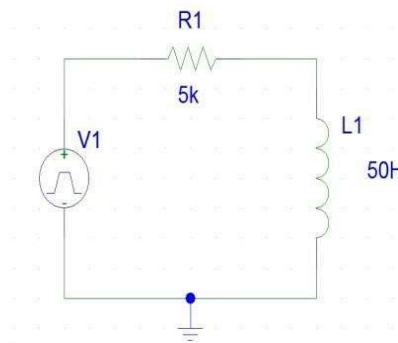
Simulation and Measurement:

In PSpice window, construct the virtual RC and RL circuits as shown below. Simulate the parametric wave shape for both the circuits. Compare the simulation results with your theoretical data and comment on the differences (if any).

RC Circuit:

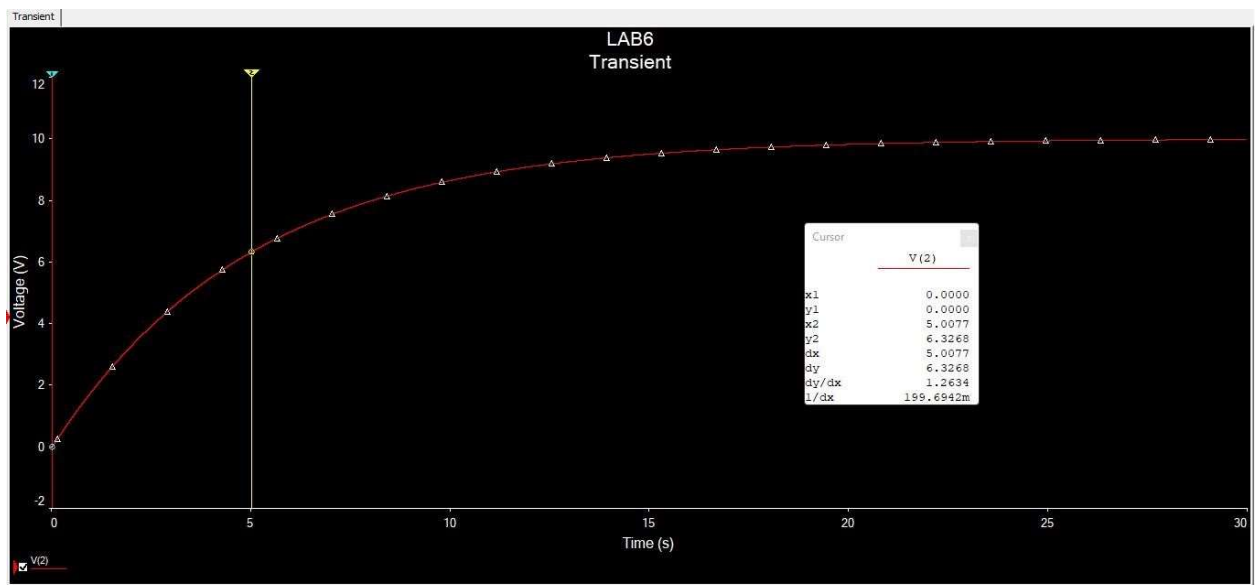


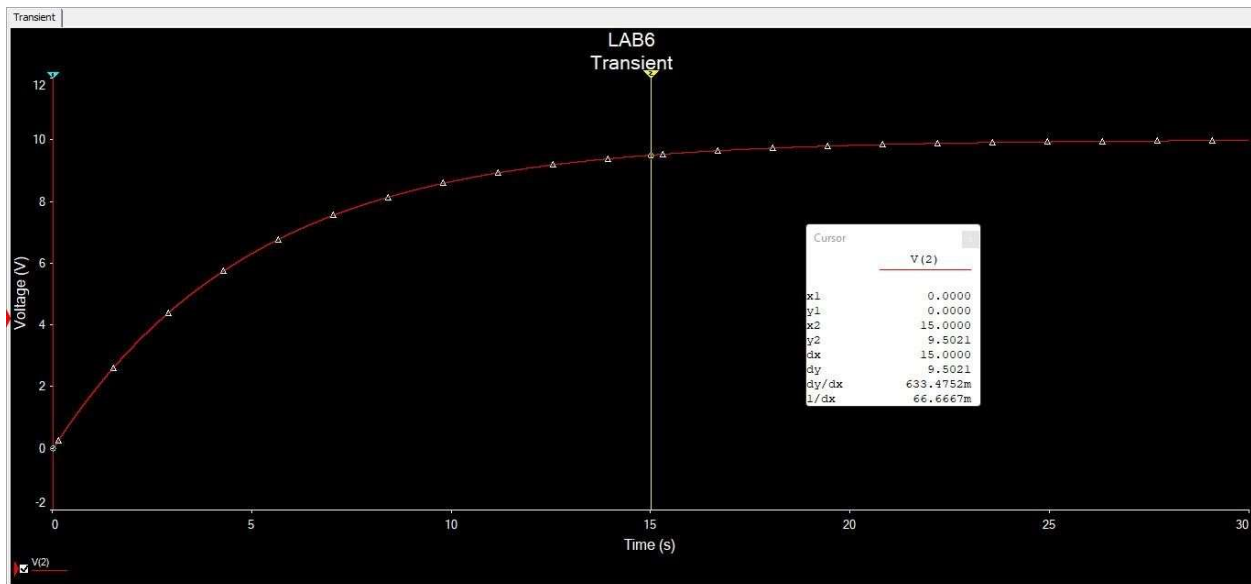
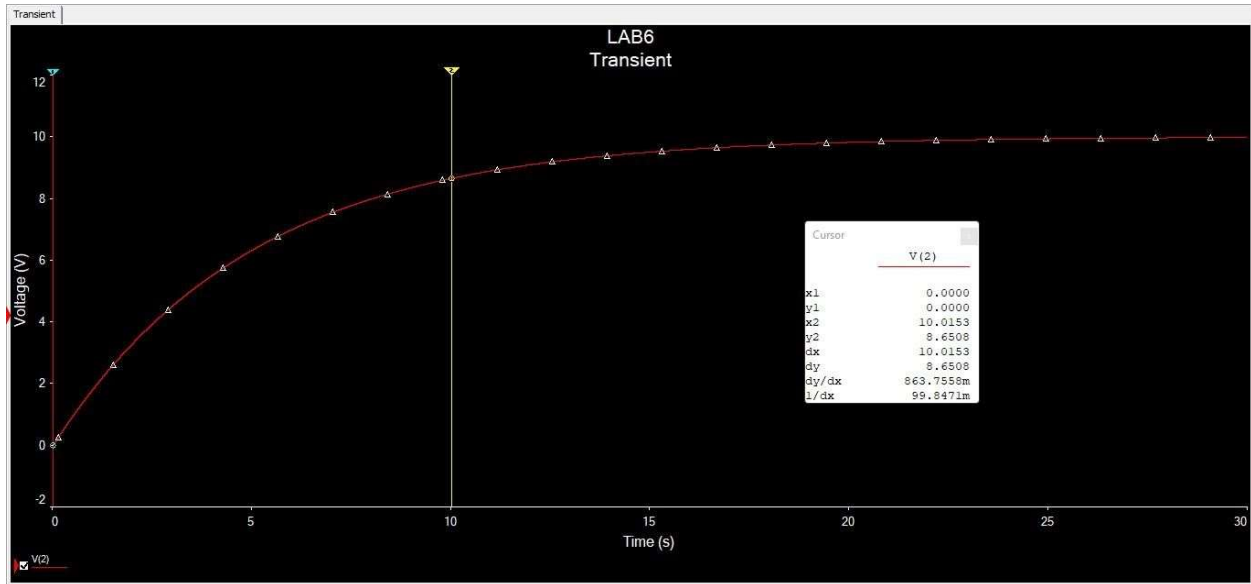
RL Circuit:

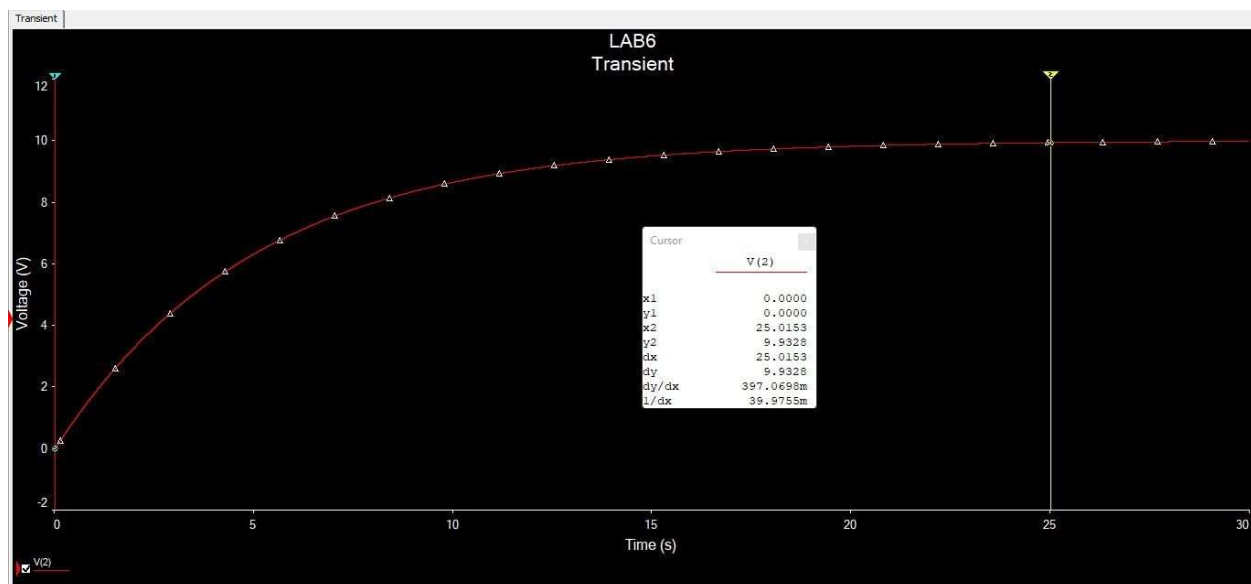
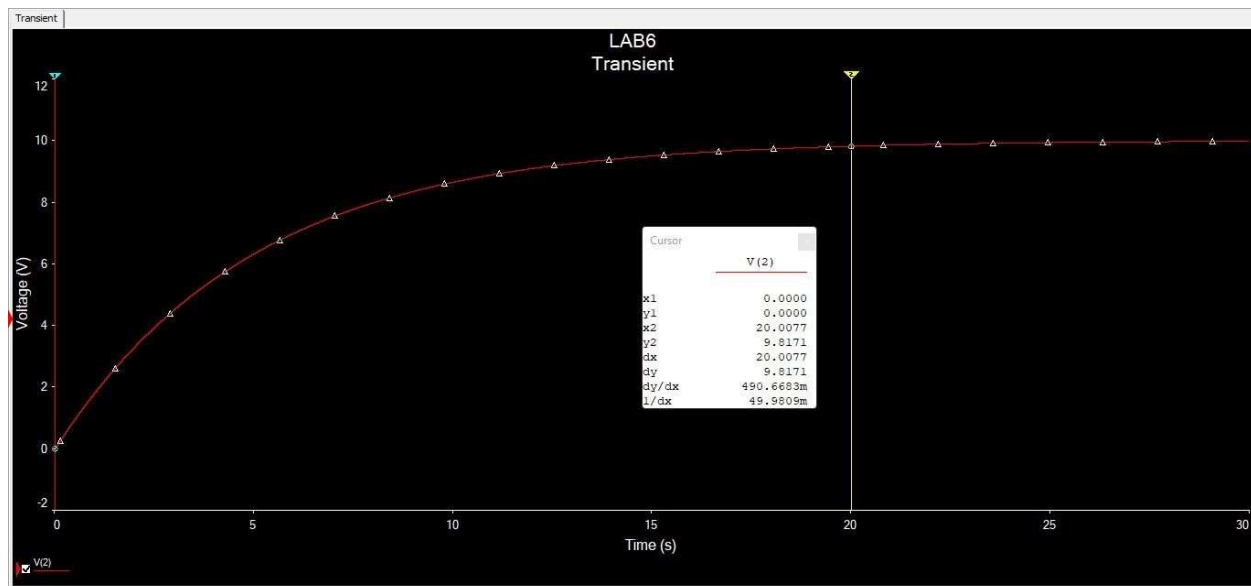


Sumilation:

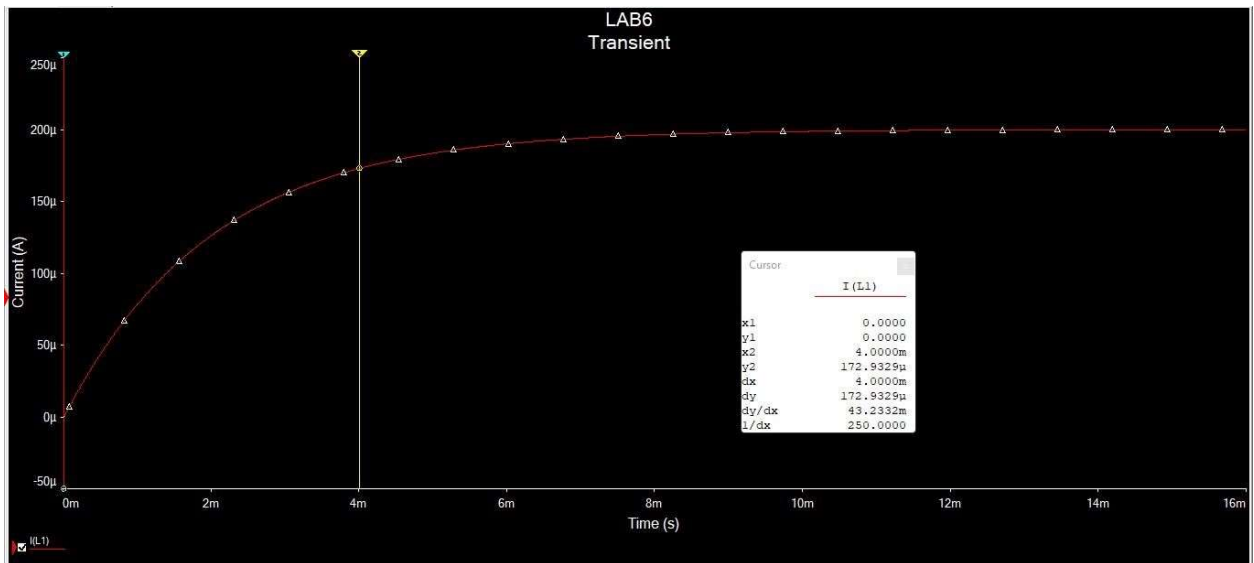
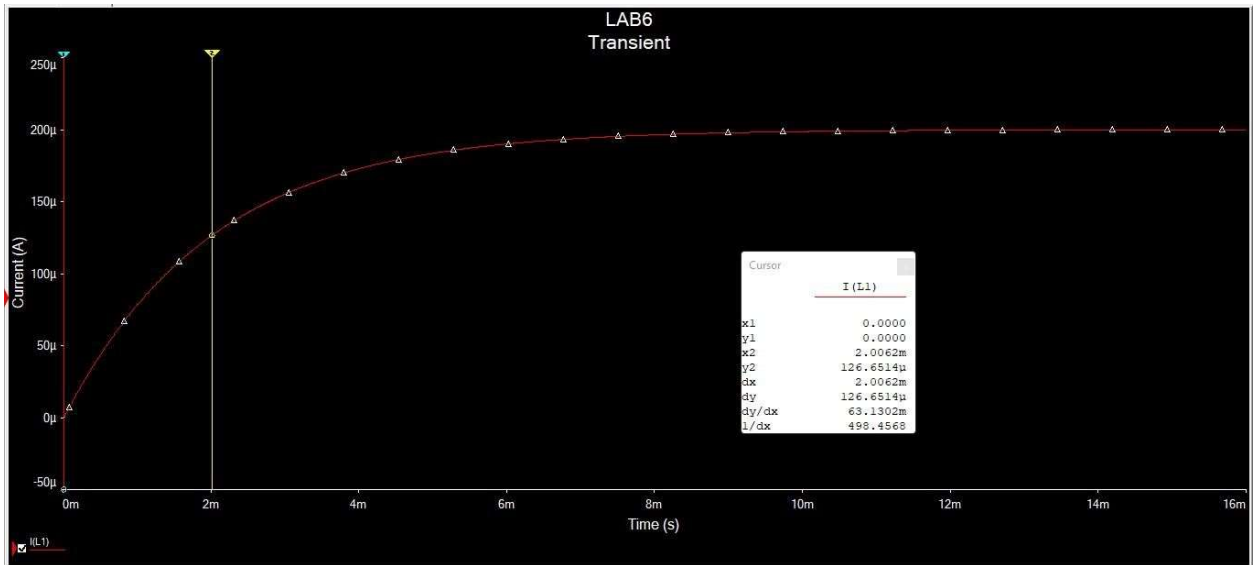
RC Circuit -

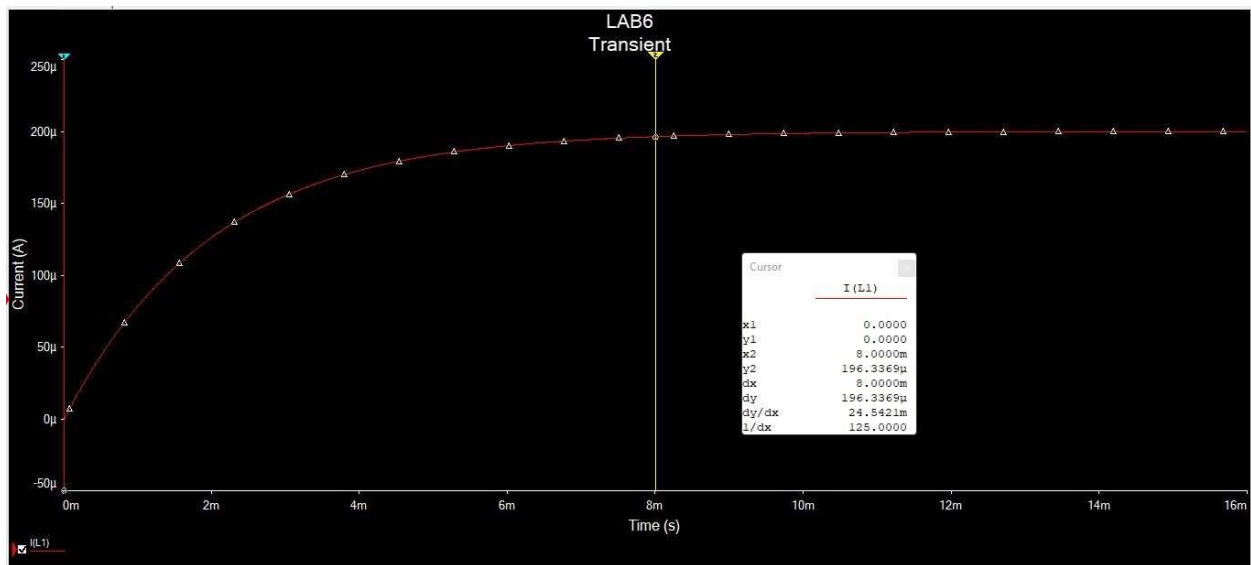
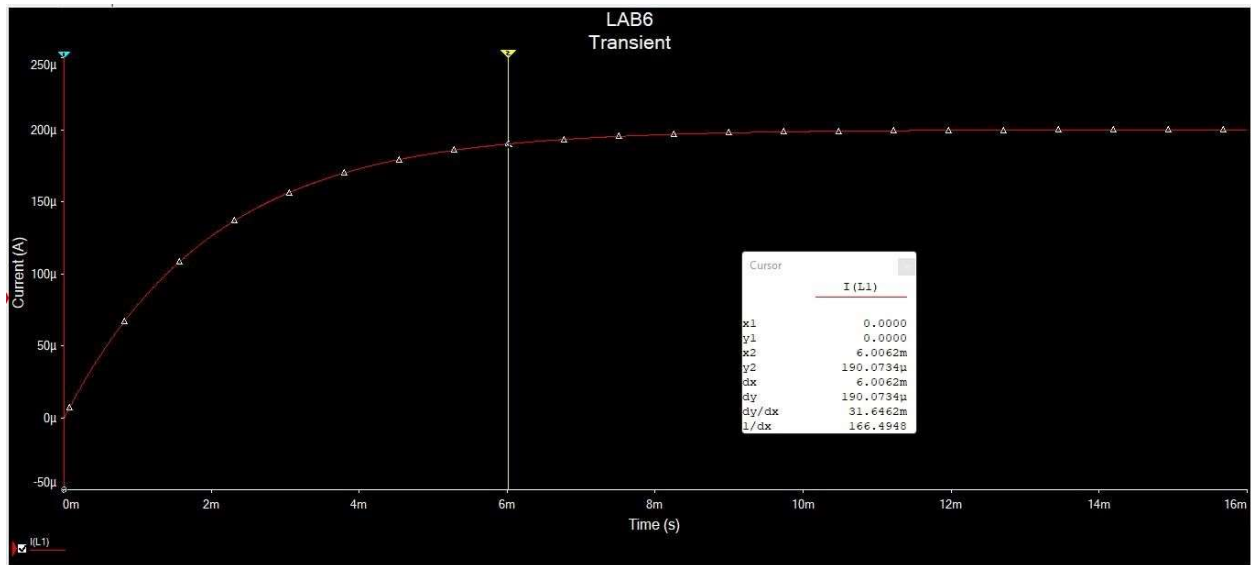


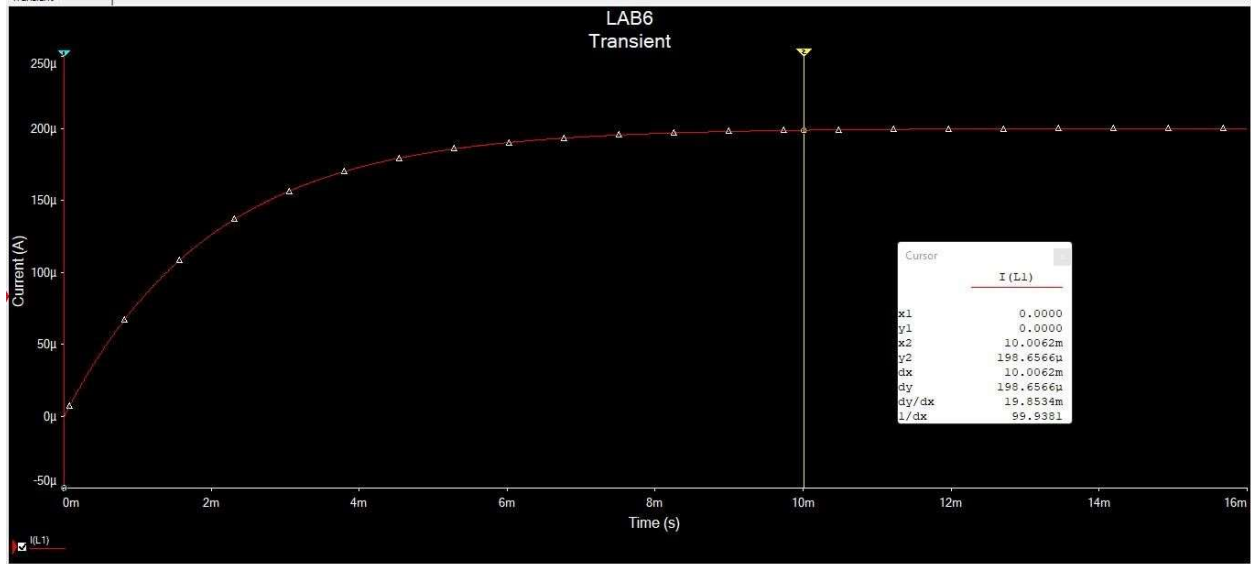




RL Circuit-







Theoretical Calculation:

Transient analysis for RC series circuit:

Given $\mathcal{E} = 10\text{V}$

$R = 50\text{ k}\Omega$

$C = 100\text{ }\mu\text{F}$

we know, time constant, $\tau = RC$

$$= 50 \times 10^3 \times 100 \times 10^{-6}$$

$$= 5\text{ sec}$$

voltage across capacitor equation:

$$V_C(t) = \mathcal{E} (1 - e^{-\frac{t}{RC}})$$

$$V_C(t) = \mathcal{E} (1 - e^{-\frac{t}{\tau}})$$

If $t = \tau$

$$V_C = 10 (1 - e^{-1})$$

$$= 6.32\text{V}$$

If $t = 2\tau$

$$V_C = 10 (1 - e^{-2})$$

$$= 8.65$$

if $t = 3 \text{ s}$ if $t = 4 \text{ s}$

$$V_c = 10(1 - e^{-1})$$

$$V_c = 10(1 - e^{-4})$$

$$V_{OLDR} = 9.9 \text{ V}$$

=

$$\text{if } t = 5 \text{ s}$$

$$V_c = 10(1 - e^{-5})$$

$$V_{OLDR} = 9.9 \text{ V}$$

Transient analysis for RL series circuit:-

Given, $R = 50 \text{ k}\Omega$

$L = 100 \text{ mH}$

$$E = 10 \text{ V}$$

$$I = \frac{E}{R}$$

$$I = \frac{10}{50 \times 10^3}$$

$$I = \frac{1}{5 \times 10^3}$$

$$I = 200 \text{ }\mu\text{A}$$

$$V_{OLDR} = 200 \text{ }\mu\text{A}$$

time constant, $\tau = \frac{L}{R} = \frac{100}{50 \times 10^3}$

$= \frac{200 \mu A}{2 ms}$

current $I_L(t) = \frac{E}{R} (1 - e^{-\frac{t}{\tau}})$

if, $t = \tau$

$$I_L(t) = \frac{10}{50 \times 10^3} \times (1 - e^{-1})$$

$$= 126.4 \mu A.$$

$t = 2\tau, I_L(t) = 200 (1 - e^{-2}) = 173 \mu A$

$t = 3\tau, I_L(t) = 200 (1 - e^{-3}) = 190 \mu A.$

Discussion:

1. The trainer board and the mutism software were checked beforehand.
2. The capacitor and the resistor were placed properly according to the figure.
3. Voltage across the capacitor for RC circuit and current across the RL circuit were calculated theoretically properly.
4. In the similar for technical issues in some points value was not sharply accurate.
5. At last, all the simulated values were placed in the data table

Conclusion: In this experiment, the value of theoretical and simulated was closely same which determine that the experiment was successful. The goal of the experiment was set of from the beginning. The study was described by calculating the voltage across the capacitor of RC circuit and current across of the inductor of RL circuit.

Reference(s):

1. Robert L. Boylestad," Introductory Circuit Analysis", Prentice Hall, 12th Edition, New York, 2010, ISBN 9780137146666.
2. R.M. Kerchner and G.F. Corcoran, "Alternating Current Circuits", John Wiley & Sons, Third Ed., New York, 1956.
3. Lamar University website, [Cited: 12.01.2014] Available:
<http://ee.lamar.edu/eelabs/elen2107/lab5.pdf>
4. Lamar University website, [Cited: 12.01.2014] Available:
<http://ee.lamar.edu/eelabs/elen2107/lab6.pd>