Communication algorithms and principles for a prototype of a wireless mesh network

Zachodniopomorski Uniwersytet Technologiczny w Szczecinie Wydział Informatyki

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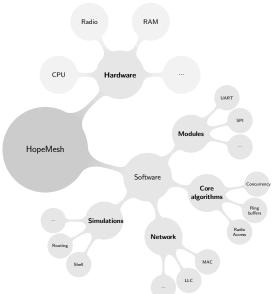
Outline

Introduction

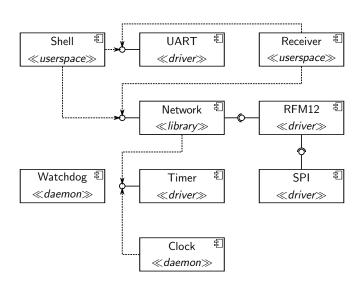
2 Research Overview

3 Architecture

Project mind map



Module Architecture



Concurrency Model

- Sequential Execution
- Concurrent Execution

Petri net driver

