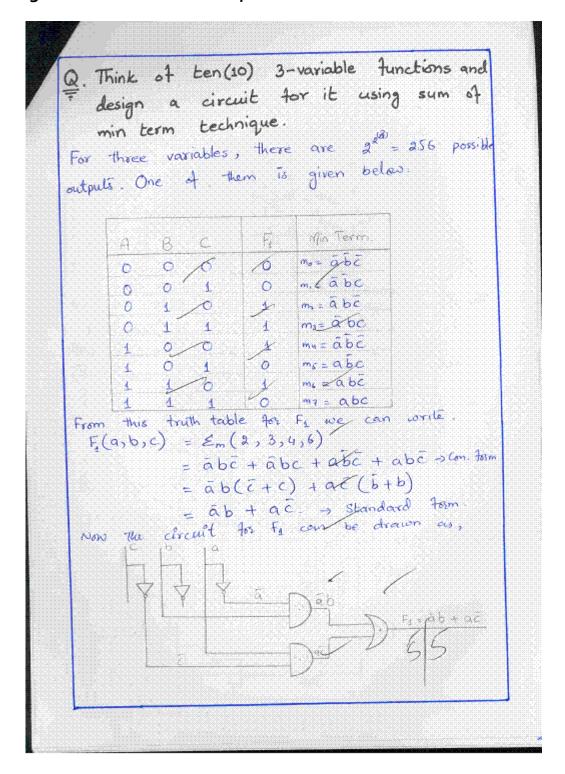
Q2: Think of ten (10) 3-variables functions and design circuits for them using sum of minterms technique.



For any function F2:

Α	8	٥ _	F ₂	rilin Texan
0	0	6	X	ajsc
0	. 0	4	4	a be
0	1	Ø	ار ه	ābē
0	4 '	1	-1	ābe
4	0	16	0	1 a5E
d	0	1	4	abc
1	4	ø	1.2	abe
1	4	1	4	abc

The function can be written as, $F_{2}(a,b,c) = \mathcal{L}_{n}(0,1,3,5,6,7)$

= ābc +ābc +ābc + abc +abc +abc

= ab(z+c) + c(ab fab) +ab(z+c)

= ab + abc + abc + ab

= ā(b+bc)+a(be+b)

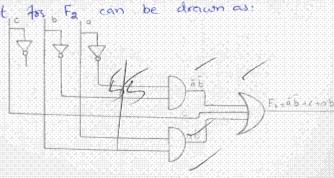
= ā (b+c) + a (8c+b)

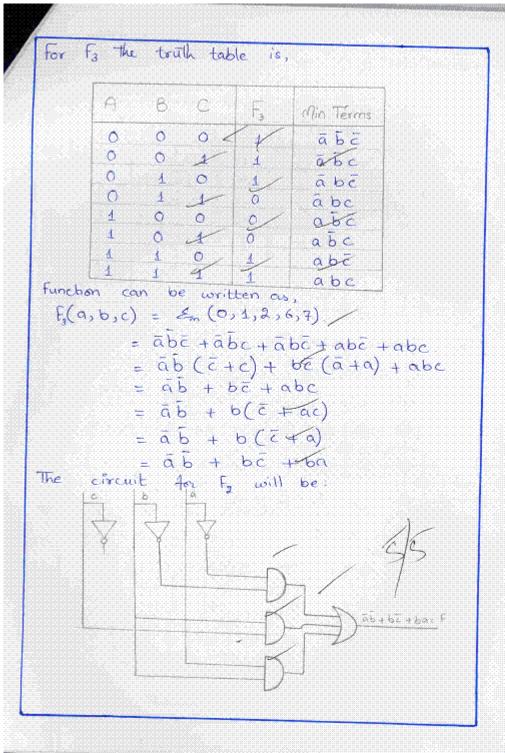
= ab+ac+ac+ab

= āb + c(ā+a)+ab/

= ab + c + ab - standard form

The circuit, to Fa, can be drawn as:





Now for Fy;

A	В	С	F4	Min Terms
0	0	0/	X	ājē
0	0	1	16 T	ábc
0	1	9	1	abc
0	4	1	_6	ābç
1	0	∕ 0∷	1	96E
4	0	1	Ø	αБς
4	1 ,	S.	1	abē
1	1	4	4	abo

The function can be written as,

circuit can be drawn as,



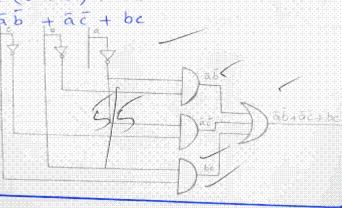
For any function is, let the truth table is, Min Terms The function can be written as, F5 (0,6,6) = 4, (0,1,2,3,7) Fr = abc + abc + abc + abc + abe + abc = ab(c+c) + bc(a+a)+ab(e+c) = ãb + bc +ab . Now looking at the standard term, the circuit be to drown

for any out put fo, truth table

	**********			12
Α	- 8	C	Fc	Mo Terms
0	0	0	1	astā
0	0	1	1	ábc
0	4.	0		ābā
0	4.	4	1	ع طو
4	. 0	0	0	αĒč
1	O	1 *	fo,	a bé
4	4	o		abá
4		4 -		abc

The function can be written as,

Circuit :



For any Fq, the truth table,

А	6	C.	Eg	Min Terris
0	0	8	6	व के ह
О	О	1	1	₹60
	1	و	1	ābč
0	4	4	1 ,	abe
1	0	ZÓ	6	abā
1	0	1	1	abc
1	1	40/	6	9 6 ē
<u>4</u>	1	Í	O	abe

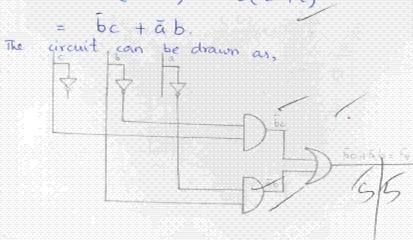
function can be written as,

Circit for Fy can be drawn as

For any Aunction Fo,

		00000000000000		
А	ß	C	Fg	An Terms
0	0	Ó	18	abi
0	0	1	4	abc
0	1	0	/1	ābč
0	1	1	1	4 бс
1	0	0	- O	abā
1	0	1	4	abc
1	1	0	0	abē
1	1	4	100	abe

function can be written as,

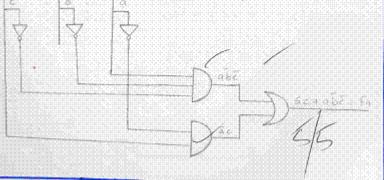


Let for be any output for which the truth table is,

Α	હ	С	Fa offin Terms
O	0	a C	0 ā5ā
0	₫.	ØΤ	1 ábc
0	1	0/	o á bã
0	1	1	1 abe
1	0	o ,	I abc
1	0	ا ⁄د	o abc
4	4/	0 /	o abc
4	1	Ĭ/	6 abc

Function can be written as, for(0,000)= 2 (1,3,4)

The circuit can be drawn as,



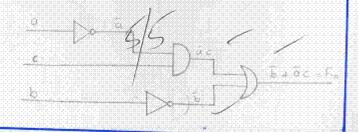
Let Fo be any output for which the fan-

A	. в	C	F.	Min Terms
0	0	o	1/1	ã b €
0	0	1	1	abc
0	4	O	0	ābī
O	4.	4.	1_1	ã bc
1		O	1	a-6c
4	0	4	L	a bc
1	3.	0	0	46E
1	1	1	46	sobe

The function can be written as,

=
$$\bar{b}(\bar{a}+a) + \bar{a}c = \bar{b} + \bar{a}c$$

Circuiti



Q3: Think of five (05) 4-variables functions and design circuits for them using product of maxterms technique.

7	J2	500L 6	EAK	Az	GEBR	A a	nd L	LOGI	c Gales*
:0W									ctions and roduct of
Ins No	: 1. 21 v Let 1 w	s the ta	ce otal i is F3 : tru	have comb Fig c uth	4. ination and Fo table	vaku ms c are will	ables, ne 2 1 the 1 be	50 4 7 = 5 des as;	by gamula 6. ired gurction
2204	£				1956 799	12 Se (1)	th0	N 0221	riend inhits
w A			, B, C	Fa					med inputs Man Terms
	B			Fa	F2	F3	F4		Max Terms
Α	/3	ç	B	Fa	F2	F3		Fş	Man Terms AtBIC+D
A o	/3	9	<u>م</u>	Fa	F2	F3	F4 }	& 0 0 1	Man Terms AtB+C+D AtB+C+B
A	(3	0 0 1	Δ 0 1	Fa	F2 0	Fg 0	F4 1	Fs 0 0 1	Man Terms AtBIC+D
A	(3	000	Δ 0 1	Fa	F2 0	Fg 0	F4 1	Fs 0 0 1	Max Terms AtB+C+D AtB+C+D AtB+C+D
A 0 0 0 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 1 1 0	6 4 0 4	Fa 0 0 1 1 1 1	F2 0	F3 0 1 1 1 1 1 1	F4 }	F 0 0 1 1 1	Man Teurns AtB+C+D A+B+C+D A+B+C+D A+B+C+D A+B+C+D A+B+C+D A+B+C+D
A 0 0 0 0 0 0 0 0 0	(A)	0 0 1	0 1 0 1	Fa 0 0 0 1 1 1 1 0	F2 0 0 0 1 1	F3 0 1 1 1 1 1 1	F4 1	F 0 0 1 1 1 1 L	Man Terms AtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D
A 0000000	(A)	0 0 1 1 0	0 1 0 1	Fa 0 0 0 1 1 1 1 0	F2 0 0 0 1 1 0	F3 0 1 1 1 1 1	F4 1 1 1 1 0	F 0 0 1 1 1 1 L	Man Teurns AtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D
A 000000000	(A)	0 0 1 1 0 1 1	6 4 0 1 0 4	Fa 0 0 0 1 1 1 0 0 1 1	F2 0 0 0 1 1 0	F3 0 1 1 1 1 0	F4 1 1 1 1 0	F 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Man Teurns AtB+C+D AtB+C+D AtB+C+D BtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D
A 000000000	(A) 0 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0 0 1 1 0 1 1	0 1 0 1 0 1	Fa 0 0 0 1 1 1 0 0 1 1	F2 0 0 0 1 0 1 0 1	F3 0 1 1 1 1 0 0	F4 1 1 1 0 0	F 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Man Teurns AtB+C+D AtB+C+D AtB+C+D BtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D AtB+C+D
A 000000000	/3 0 0 0 1 1 1 1	0 0 1 1 0 0 1 1 0	0 1 0 1 0 1	Fa 0 0 0 1 1 1 0 0 0 1 1 1 1 1 1 1 1 1 1	F2 0 0 0 1 1 1 1	F3 0 1 1 1 0 0 1	F4 1 1 1 0 0 1	F 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Man Teurns AtB+C+D
A 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	/3 0 0 0 1 1 1 1 0	0 0 1 1 0 0 1 1 0		Fa 0 0 1 1 1 0 0 1 1 0 0 1 1 0 0 0 1 1 0 0 0 1 1 0 0 0 0 1 1 0	F2 0 0 0 1 1 0 1 1 0 1 1	F3 0 1 1 1 1 0 0	F4 1 1 1 0 0 1 1 0 0 0 0 0 0 0 0 0 0 0 0	F 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 0	Man Teurns AtB+C+D A+B+C+D A+B+C+D
A 0 0 0 0 0 0 0 0 0 0 0 1 1	(A)	0 0 1 1 0 0 1 1 0 0 1 1 0 0		Fa 0 0 0 1 1 1 0 0 1 1 1 1 1 1 1 1 1 1 1	F2 0 0 0 1 1 1 1	F3 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	F4 1 1 1 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1	F 0 0 1 1 1 1 1 1 1 1 1 0 0 0 0 0 0 0	Man Teurns AtB+C+D AtB+C+D
A 0 0 0 0 0 0 0 0 0 0 0 1 1	/3 0 0 0 1 1 1 1 0	0 0 1 1 0 0 1 1 1		Fa 0 0 1 1 0 0 1 1 1 1	F2 0 0 0 1 1 0 1 1 0 1 1	F3 0 1 1 1 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1	F4 1 1 1 0 0 1 1 1 1	5 0 0 1 1 1 1 1 1 1 1 0 0 0 0	Man Teurns AtB+C+D AtB+C+D AtB+C+D BtB+C+D AtB+C+D

