

I hereby pledge that I will strictly adhere to academic integrity codes and the work done on this examination is solely my own and I will not receive/give my help from/to anybody or source during this examination.

Q2)

index = 7 bits, 2^7 block

32 bytes block size $\rightarrow 2^5 \rightarrow 5$ offset

block size 32 bytes

16 bytes

Index - 7 - tag - memory

a) $\text{tag} = \text{total size} - \text{index} - \text{offset} = 32 - 7 - 5 = 20 \text{ bit}$

1 1110 0101 0110 1110 1011 0001 0011 1000
 Index v tag block offset

b) $\text{block address} = \text{Index} + \text{tag}$

Block Address		Block
Index	Tag	Offset

- 1111 0011 0110 1110 1011 0001 001

c) Set address:

0110 1110 1011 0001 0011 1000

d) $2^7 \rightarrow \text{index}$ $2^7 \times 2 \rightarrow \text{two assoc} \rightarrow 2^8 \times \underbrace{2^5}_{\text{data block}} = 2^{13} = 8 \text{ Kilobytes}$

e) $\text{AMBT} = \text{hit time}_{L1} + \text{Miss rate}_{L1} \times (\text{hit time}_{L2} + \text{Miss rate}_{L2} \times \text{Miss penalty}_{L2})$

$4 \text{ ns} + 0,1 \times (10 \text{ ns} + 0,4 \times 600 \text{ ns})$

$= 4 \text{ ns} + 0,1 (10 + 24)$

$= 4 + 0,1 (34)$

$= 4 + 3,4$

$= 7,4$