

Abenezer Wudenhe

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EDUCATION

University of California, Riverside (UCR) <ul style="list-style-type: none">• SMART Fellow• Chancellor's Distinguished Fellow• GAANN Fellow	PhD (Computer Science) Expected: May 2024
University of Maryland, Baltimore County (UMBC) <ul style="list-style-type: none">• Meyerhoff Scholar• NSA Scholar	BS (Computer Engineering) May 2018 (<u>Cum Laude</u>)

PROFESSIONAL EXPERIENCE

Extreme Storage and Computer Architecture Lab (ESCAL) <i>Graduate research assistant to Dr. Hung-Wei Tseng.</i> <ul style="list-style-type: none">• <u>Optimizing memory hierarchy for mixed precision computing</u><ul style="list-style-type: none">○ Developed an GPGPU-sim extension to enable more accurate simulation of NVIDIA's half-precision computation and evaluation of the overhead.○ Developed a set of Rodinia benchmarks to utilize the half-precision support.○ Accelerate the performance of GPU kernels with reasonable accuracy using CUDA• <u>TPUPoint: Profiler and optimizer for TPU cloud</u><ul style="list-style-type: none">○ Designed and developed an automatic profiling and optimization tool for Google's TPU-based ML Cloud Platform.○ Achieved up to 1.12x speedup for programmer's optimizations using TensorFlow.○ Ported a set of MLPerf applications to Google's TPU Cloud Platform.	2018 Aug – Present
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ARMY CYBER DWD Internship <i>Software Engineering Intern.</i> <ul style="list-style-type: none">• Assessed new technologies for ARMY Big Data Platform.• Explored Amazon Kinesis tool for data stream processing for reduction of database overhead.	2019 May – Aug 2019
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University of Michigan Lab 4PROGRESS REU <i>Undergraduate research assistant to Dr. Chad Jenkins</i> <ul style="list-style-type: none">• Applied cluster computing methods to robotic visualization techniques and object recognition.• Developed GPU accelerated image rendering using Nvidia drivers and CUDA programming.	2017 May - Aug 2017
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PUBLICATION

A. Wudenhe, Hung-Wei Tseng. "TPUPoint: Automatically Characterizing Hardware Accelerated Data Center Machine Learning Program Behavior". In IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS 2021), 2021.

Q. Meng, D. Gupta, **A. Wudenhe**, X. Du, L. Hong, F. Choa. "Three-Dimensional EEG Signal Tracking for Reproducible Monitoring of Self-Contemplating Imagination". In Advances in Science, Technology and Engineering Systems Journal (ASTESJ), 2017.

TECHNICAL SKILLS

- Experience programming in **C**, **C++**, **python**, **CUDA**, html, MPI, php, Arduino, OpenMP, Open MPI, TensorFlow, Sklearn, Javascript, NodeJS
- Experience writing technical documents using LaTeX, BibTex, Word
- Experience with Xilinx Design Tool, MATLAB, Cadence's Allegro Design Entry CIS, Atmel Studio