






Austin Ebel

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EDUCATION	Columbia University <i>Bachelors of Science, Electrical Engineering</i> GPA: 3.85/4.00	2015-2020 New York, NY
	College of William and Mary <i>Bachelors of Science, Computer Science</i> GPA: 3.89/4.00	2015-2020 Williamsburg, VA
PUBLIC- ATIONS	<i>Gardner, J., Hunt, K., Ebel, A., Rose, E., Zylich, S., Jensen, B., Wise, K., Siochi, E., Sauti, G. Machines as Craftsmen: Localized Parameter Setting Optimization for Fused Filament Fabrication 3D Printing. <i>Advanced Materials Technologies</i>, 2019</i>	
RESEARCH EXPERIENCE	VLSI Lab, Columbia University Supervisor: Mingoo Seok	2021
	<ul style="list-style-type: none">• This work will explore hardware architectures for machine learning related to either <i>TinyML</i> or <i>hardware security</i>.	
	Research Assistant, Columbia University Supervisor: Debasis Mitra	2020-2021
	<ul style="list-style-type: none">• Used deep reinforcement learning to more accurately model optimal investments in information security.<ul style="list-style-type: none">– Publication in progress.	
RELEVANT PROJECTS	NASA Langley Research Center Supervisors: John Gardner, Godfrey Sauti	2018
	<ul style="list-style-type: none">• Created an end-to-end tool for integrating machine learning into the 3D printing process. Resulting prints show a 14% improvement in quality and a 28% decrease in runtime.<ul style="list-style-type: none">– Published paper.	
	<i>Full-Custom 8-Bit Microprocessor Design</i> 	
	<ul style="list-style-type: none">• Designed a fully custom 8-bit microprocessor core in Cadence Virtuoso using IBM's 90nm technology.	
	<i>Parallelization of Particle Swarm Optimization</i> 	
	<ul style="list-style-type: none">• Reduced the runtime complexity of Particle Swarm Optimization from $O(n^2)$ to $O(n)$ by making use of parallel computing techniques on GPUs. Optimal use of shared memory, block size, and data transfer techniques were investigated.	
	<i>Pipelined RISC-V CPU (in progress)</i> 	
	<ul style="list-style-type: none">• Working through Berkeley's <i>EECS151 Introduction to Digital Design and Integrated Circuits</i> FPGA labs and final project.	

ADDITIONAL EXPERIENCE	<i>NASA Jet Propulsion Laboratory</i> Supervisor: Stirling Algermissen <ul style="list-style-type: none"> Expanded the scope of automated testing procedures for use in NASA's upcoming <i>SWOT</i> satellite. 	2020
	<i>NASA Jet Propulsion Laboratory</i> Supervisor: Mike Gangl <ul style="list-style-type: none"> Developed a cloud-based service to help hydrologists query existing and future NASA datasets. 	2019
PRESENT- ATIONS	<i>Columbia University Data Science Institute</i> Poster Session, <i>Data Science Day</i> Attacker-Defender Investment Strategies in Cybersecurity	2021
	<i>Columbia University Data Science Institute</i> Cybersecurity Center Poster Session Attacker-Defender Investment Strategies in Cybersecurity	2021
AWARDS	3 rd Place (\$150), <i>Columbia Masters Design Expo</i> Parallelization of Particle Swarm Optimization	2019
OTHER	An assortment of other, non-hardware related projects can be found on my website: austinebel.net	
TECHNOLOGY SUMMARY	<i>Programming Languages:</i> Python, MATLAB, C++, Verilog <i>Hardware Tools:</i> Cadence Virtuoso, Calibre, Ultrasim, Xilinx Vivado <i>Others:</i> Unix, Git, L ^A T _E X	