

PROJECT TIMELINE

PROJECT TITLE	RC5 Computer Architecture Analysis
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PROJECT MANAGER Dr. Mohamed El-Hadedy

COMPANY NAME

DATE _____

PHASE			DETAILS																			
PROJECT WEEK:			Enter the date of the first Sunday of each month -->				FEB				MAR				APR				MAY			
							6	13	20	27	6	13	20	27	3	10	17	24	1	8	15	22
1	Project Conception and Research	1.1 Brainstorming	1.1																			
		1.2 Research Codes (RC5) [Verilog, C, etc.]		1.2																		
		1.3 Finalize Concepts			1.3																	
2	Project Definition and Planning	2.1 Run Codes Researched on appropriate software				2.1																
		2.2 Change algorithmis appropriately					2.1															
3	Final Phase	3.1 Performance Metric Gathering							3.1													
		3.2 Other Implementation Options									3.2											
		3.3 Other stuff?											3.3									