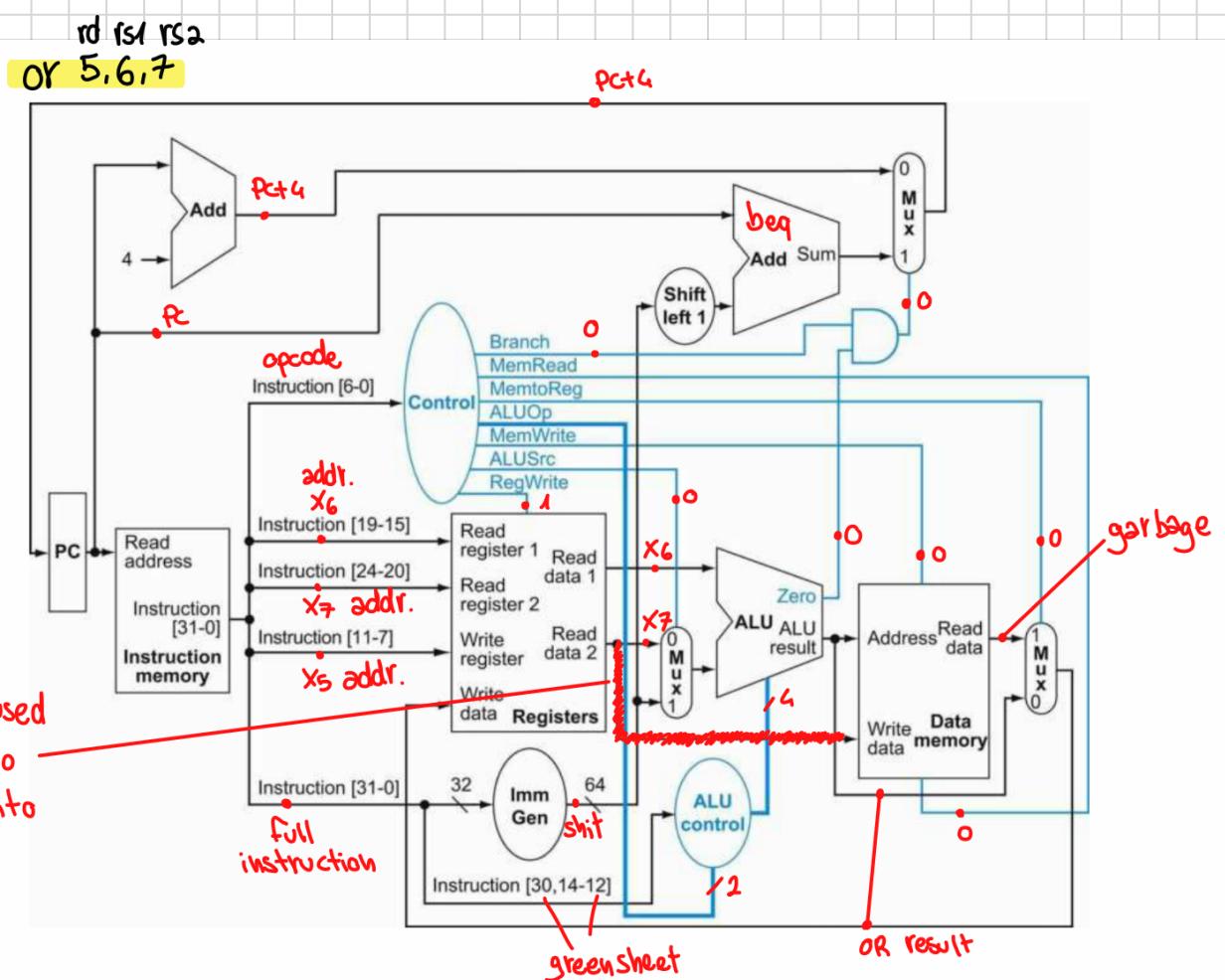


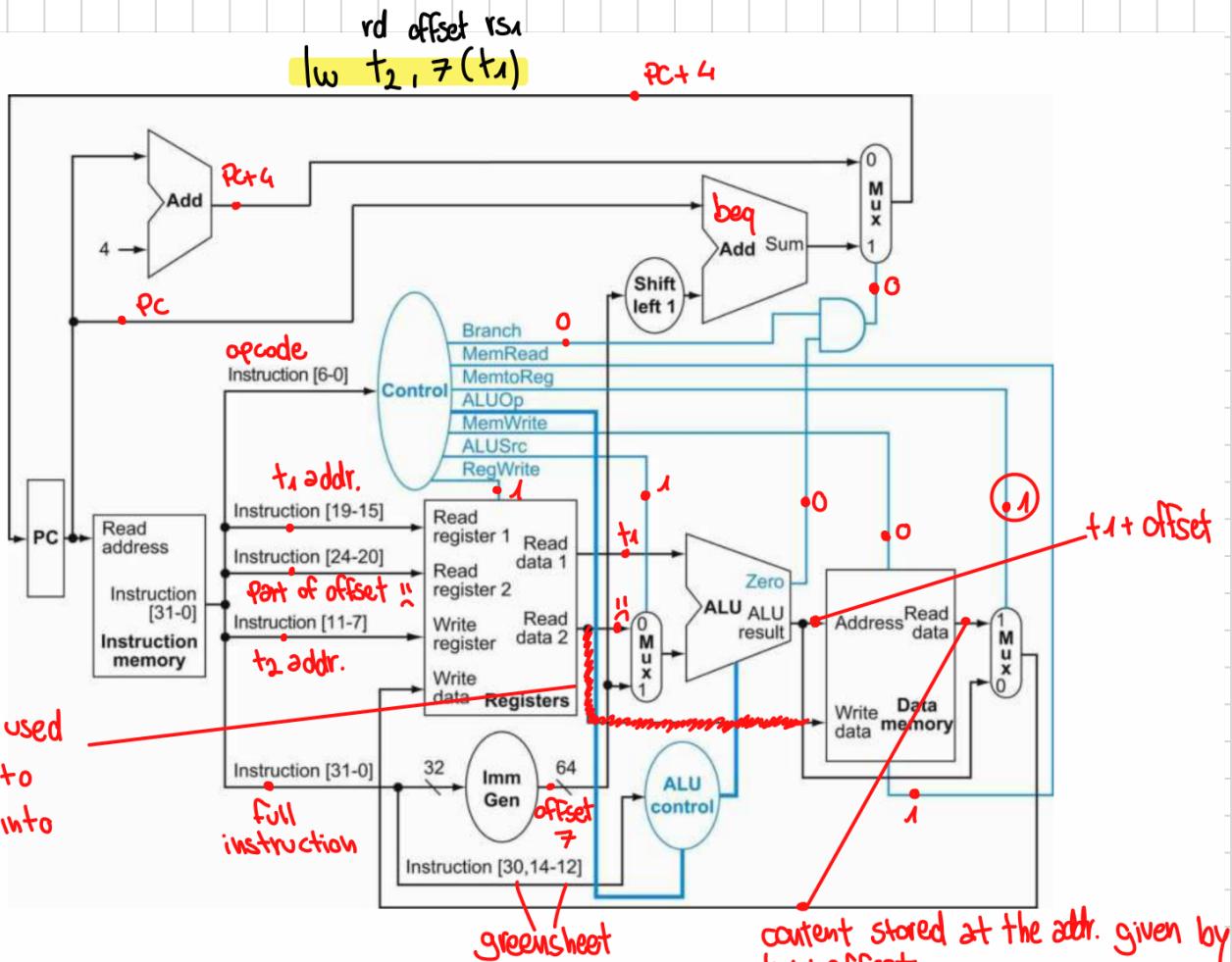
This line is used only in SW, to write data into the memory.



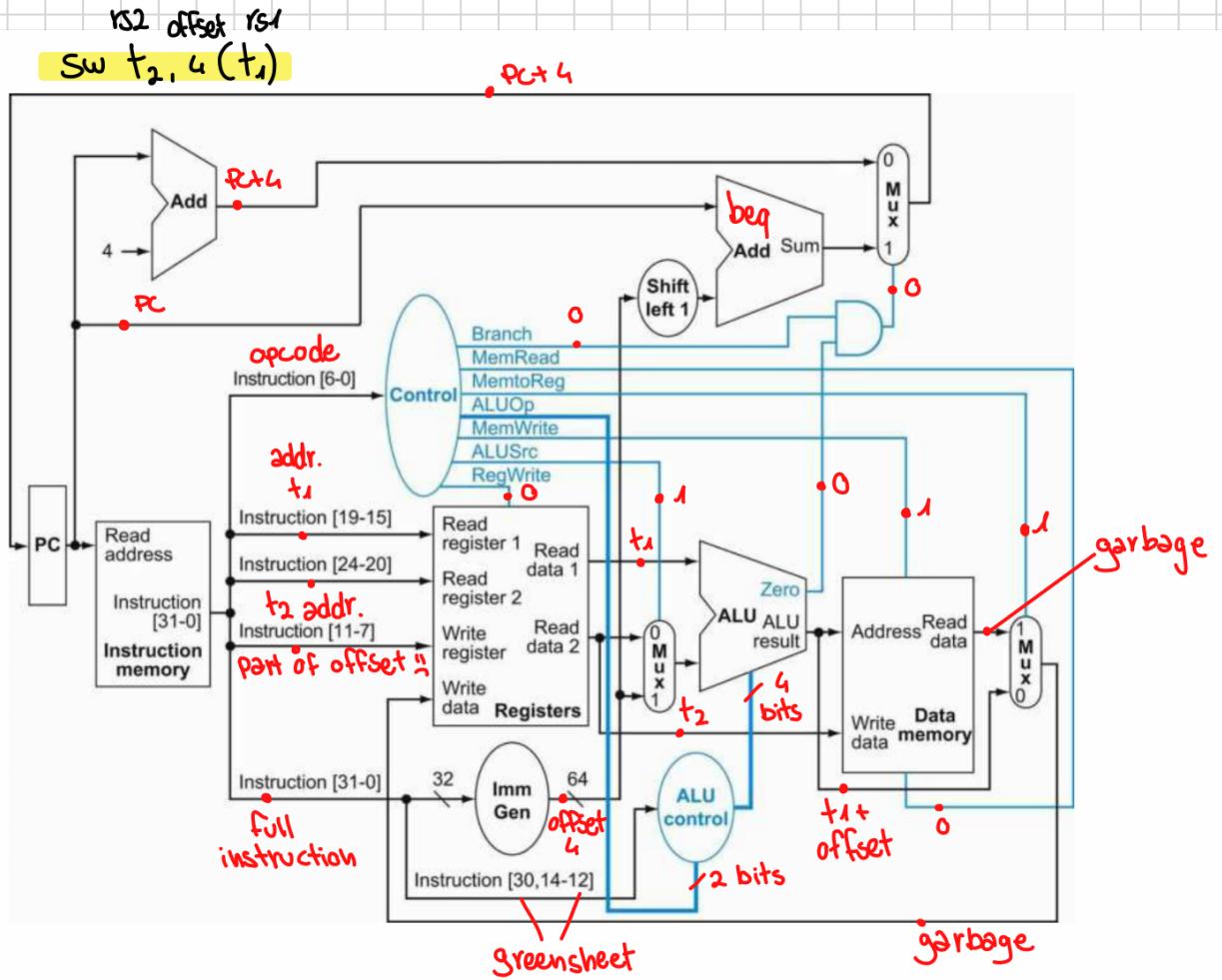
This line is used only in SW, to write data into the memory.

31	27	26	25	24	20	19	15	14	12	11	7	6	0	
funct7		rs2		rs1		funct3		rd		opcode				R-type
	imm[11:0]			rs1		funct3		rd		opcode				I-type
imm[11:5]		rs2		rs1		funct3		imm[4:0]		opcode				S-type
imm[12:10:5]		rs2		rs1		funct3		imm[4:1 11]		opcode				B-type
						imm[31:12]				rd				U-type
						imm[20:10:1 11 19:12]				rd				J-type

lw

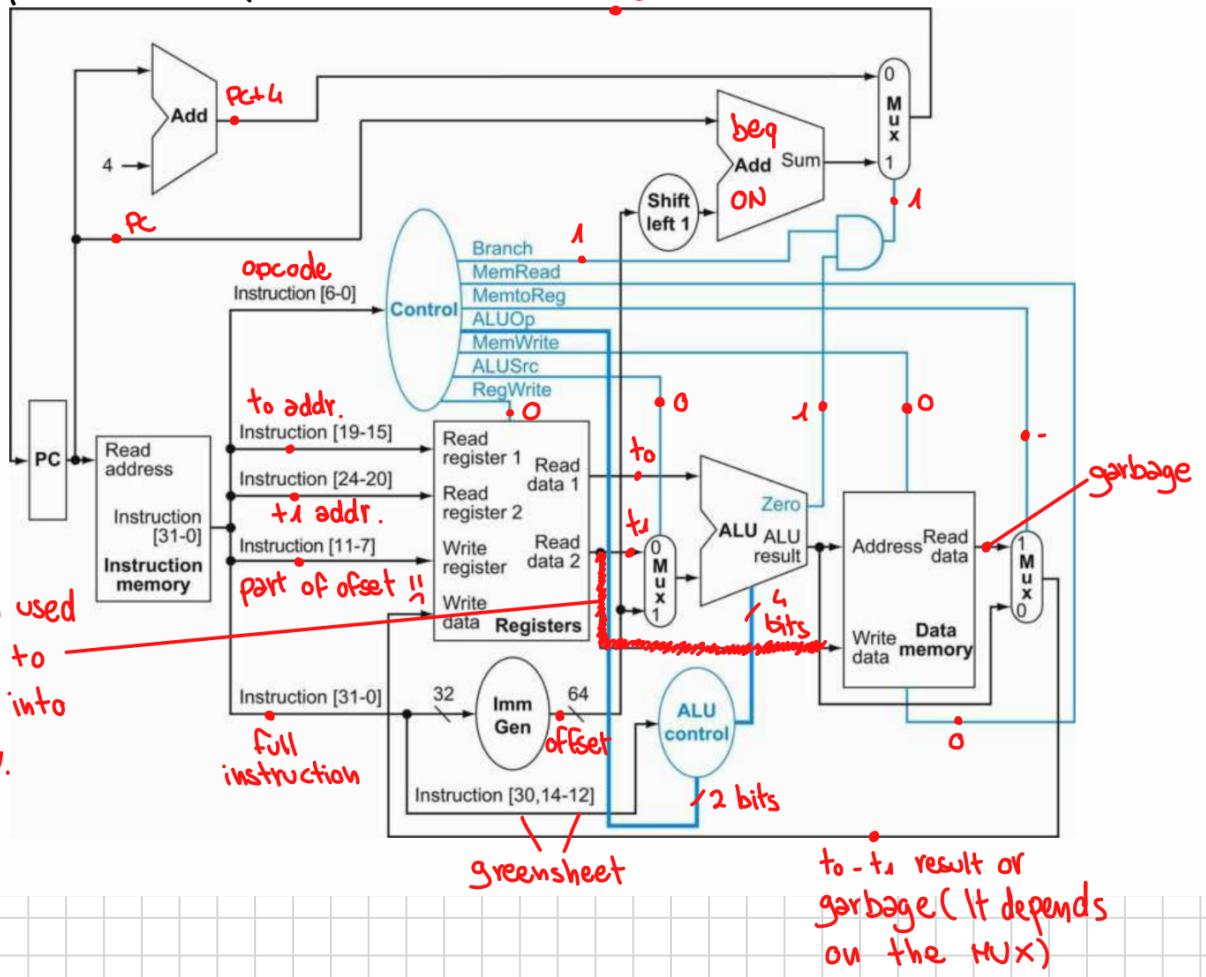


31	27	26	25	24	20	19	15	14	12	11	7	6	0	
funct7		rs2		rs1		funct3		rd		opcode				R-type
imm[11:0]				rs1		funct3		rd		opcode				I-type
imm[11:5]		rs2		rs1		funct3	imm[4:0]			opcode				S-type
imm[12:10:5]		rs2		rs1		funct3	imm[4:1 11]			opcode				B-type
			imm[31:12]					rd		opcode				U-type
			imm[20:10:1 11 19:12]						rd		opcode			J-type



beq to,  $t_1$ , offset (label)

branch



This line is used  
only in SW, to  
write data into  
the memory.