Vth Comparison: Traditional vs √ld Method y=xNMOS PMOS 0.6 0.4 0.2 Vth √ld Method (V) 0.0 -0.2 · -0.4-0.6 0.6 -0.6 -0.4-0.2 0.0 0.2 0.4 Vth Traditional Method (V)