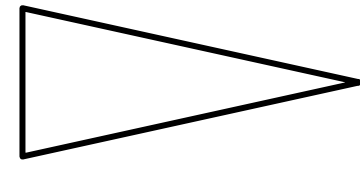


Legend:

$w_{...}$ Workload vector



SAF microarchitecture component



Transitive workload equality via wire

$s_{...}$

Component scale parameters



Transfer relation (component-specific)

$s'_{...}$

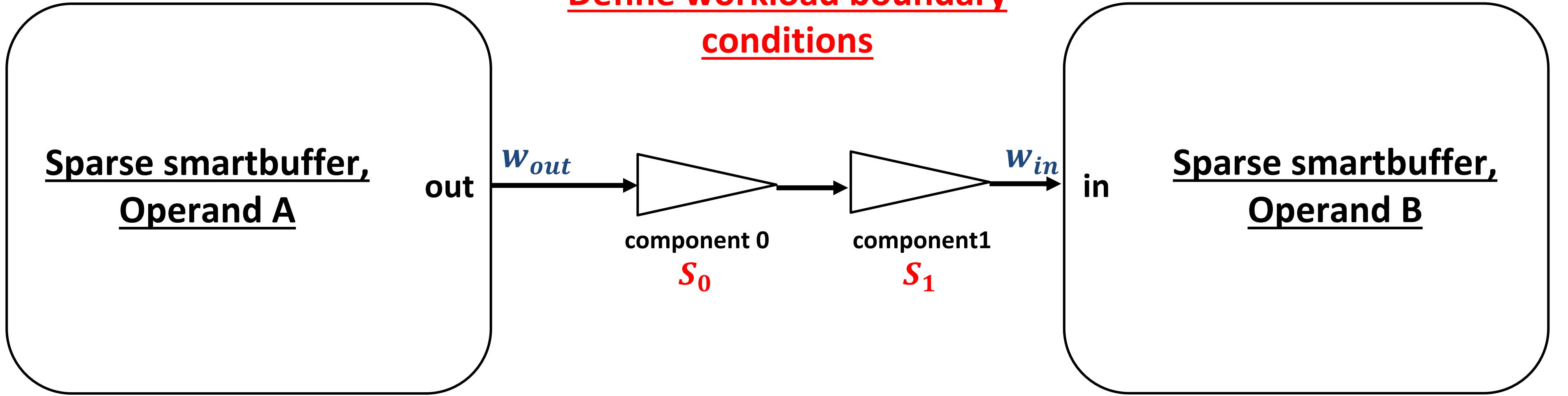
Updated component scale parameters



Constraint violation

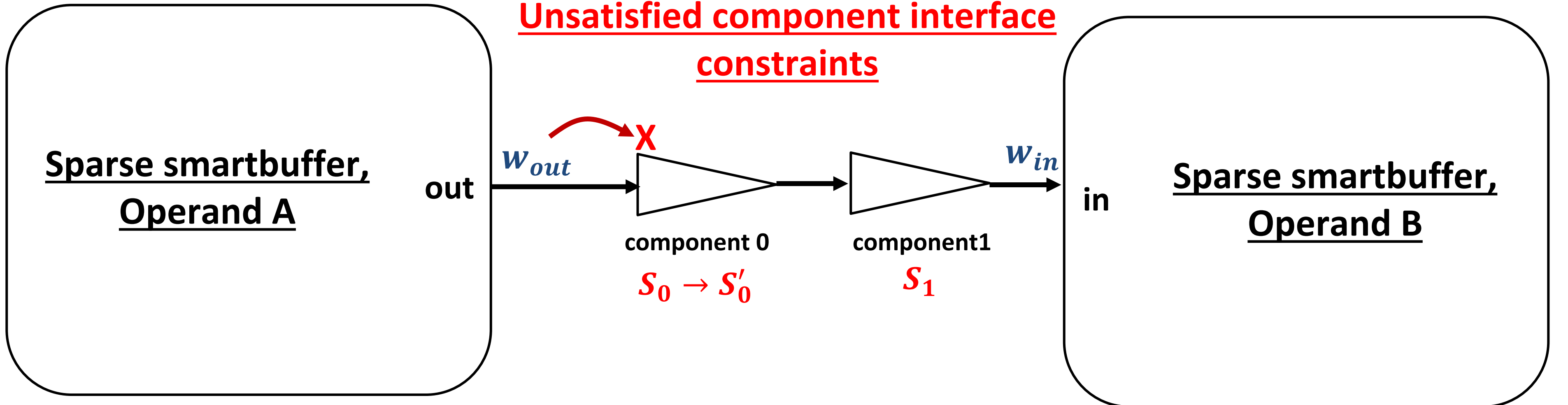
(i)

Define workload boundary conditions



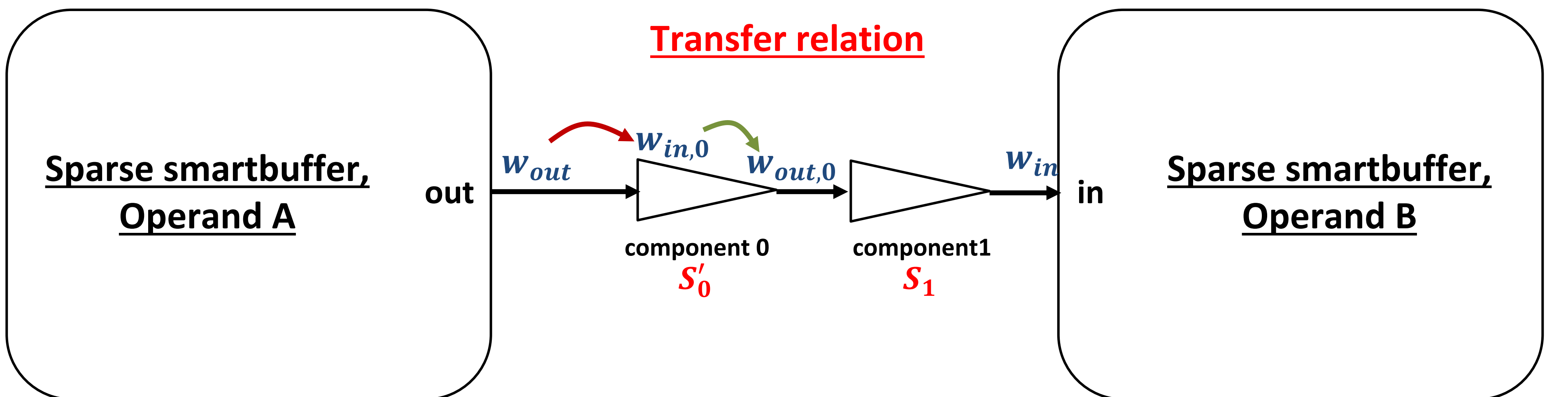
(ii)

Unsatisfied component interface constraints



(iii)

Transfer relation



(iv)

Daisy-chained solution process; unsatisfied boundary condition

