Test Cases				
Instruction	**Assembly Code**	**Expected Result**	**Test Condition**	**Success?**
MOV AL r0,#1	4001			
ADD AL r1,r0	0110			
SYS AL	9800	r1 = 1	Reg dependence	yes
MOV AL r0,#4	4004			
ADD AL r0,r1	0101			
SYS AL	9800	r0 = 4	Reg dependence	yes
MUL S r0,#0	4a00			
MOV EQ r1,#1	4411			
ADD AL r1,r0	0110		Conditional and	
SYS AL	9800	r1 = 1	reg dependences	yes
MOV S r0,#0	4200			
MOV NE r1,#2	4612			
MOV AL r1,#4	4014			
MOV AL r0,r1	4101		Conditional and	
SYS AL	9800	r0 = 4	reg dependences	yes
MOV AL r0,#2	4002			
MOV AL r1,#5	4015			
MOV S r2,#0	4220		Conditional	
MUL EQ r0,r1	4d01		dependences	
MOV AL r2,r0	4120		with	
SYS AL	9800	r2 = 10	multiplication	yes
MOV AL r0,#5	4005			
MOV AL r1,#1	4011			
STR AL r0,[r1]	7901			
LDR AL r2,[r1]	3921		Memory	
SYS AL	9800	r2 = 5	dependence	yes
MOV AL r0,#6	4006			
MOV AL r1,#1	4011			
MOV S r2,#0	4220			
STR EQ r0,[r1]	7d01		Conditional and	
LDR AL r3,[r1]	3931		memory	
SYS AL	9800	r3 = 6	dependence	yes
ADD AL r0, #6	0006			
ADD AL r1, #1	0011			
MOV AL r15, #6	40f6			
SYS AL	9800			
SYS AL	9800			
SYS AL	9800			
ADD AL r1, r0	0110			
ADD AL r0, #5	0005			
ADD AL r0, #6	0006			
ADD AL r0, #5	0005			
ADD AL r1, r0	0110	r0=22; r1=29	Jumps	yes

	4037			
MOV AL r3, #7	4001			
MOV AL r0, #1	8b30			
SUB S r3, r0	44f6			
MOV EQ r15, #6	0611			
ADD NE r1, #1	40f1	reg0 = 1		
MOV AL r15, #1	c002	reg1 = 6		
MOV AL r2, #42	402a	reg2 = 42		
SYS AL	9800	reg3 = 0	Jump with Loop	yes
PRE AL #1	c001			
PRE AL #2	c002			
ADD AL r0, #1	0001		Two PRE's in a	
SYS AL	9800	r0=33	row	yes
ITOF AL r0,#6	3006			
ITOF AL r1,#0	3010	reg0 = 4'x40c0		
ITOF AL r2,#-3	302d	reg1 = 4'x0		
ITOF AL r3,#-7	3039	reg2 = 4'xc040		
SYS AL	9800	reg3 = 4'c0e0	ITOF	yes
	c002			
	300a			
ITOF AL r0,#42	cff8			
ITOF AL r1,#-117	301b	reg0 = 0x4228		
SYS AL	9800	reg1 = 0xc2ea	Large # ITOF	yes
ITOF AL r0,#6	3006			
MOV AL r1, #0	4010			
FTOI AL r2, r0	2920	reg0 = 4'x40c0		
SYS AL	9800	reg2 = 4'x0006	Basic FTOI	yes
	cff8			
ITOF AL r0,#-117	300b			
MOV AL r1, #0	4010			
FTOI AL r2, r0	2920	reg0 = 4'xc2ea	Negative and	
SYS AL	9800	reg2 = 4'xff8b	Large FTOI	yes
ITOF AL r0,#5	3005			
ITOF AL r1, #6	3016			
MULF AL r0, r1	5101	reg0 = 16'b0100000111110000		
FTOI AL r2, r0	2920	reg1 = 16'b0100000011000000		
SYS AL	9800	reg2 = 16'b0000000000011110	Basic MULF	yes

	cfff			
ITOF AL r0,#-10	3006			
ITOF AL r1,#7	3017			
MULF AL r0, r1	5101	reg0 = 16’b1100001010001100	MULF on a	
FTOI AL r2, r0	2920	reg1 = 16’b0100000011100000	Positive and	
SYS AL	9800	reg2 = 16’b1111111110111010	Negative Float	yes
	cffe			
	300b			
ITOF AL r0,#-21	cffe			
ITOF AL r1,#-20	301c			
MULF AL r0, r1	5101	reg0 = 16’b0100001111010010	MULF on Two	
FTOI AL r2, r0	2920	reg1 = 16’b1100000110100000	Large Negative	
SYS AL	9800	reg2 = 16’b0000000110100100	Values	yes
				does not match up with
ITOF AL r0,#5	3005			precise
RECF AL r1,r0	6910	reg0 = 16’b0100000010100000		reciprocal
SYS AL	9800	reg1 = 16’b0011101101001100	RECF	value
	cffc			
ITOF AL r1, #-50	301e			
FTOI AL r0, r1	2901	reg0 = 16'b1111111111001110	FTOI on negatives	
SYS AL	9800	reg1 = 16'b1100001001001000	with shift	yes
	c012			
	300c			
ITOF AL r0,#300	c01f			
ITOF AL r1,#500	3014			
ADDF AL r0,r1	0901		ADDF on large	
SYS AL	9800	reg0 = 4'x4448	positive #s	yes
	cfff			
	3003			
ITOF AL r0,#-13	c003			
ITOF AL r1,#55	3017			
ADDF AL r0,r1	0901			
SYS AL	9800	reg0 = 4'x4228	ADDF	yes

ITOF AL r0,#-1	300f			
ITOF AL r1,#2	3012			
ITOF AL r2,#-3	302d			
ITOF AL r3,#4	3034			
ADDF AL r0,r1	0901			
ADDF AL r0,r2	0902			
ADDF AL r0,r3	0903			
FTOI AL r3,r0	2930			
SYS AL	9800	reg3 = 2	ADDF	yes
ITOF AL r0,#1	3001			
ITOF AL r1,#-2	301e			
SUBF AL r0,r1	9101			
FTOI AL r3,r0	2930			
SYS AL	9800	reg3=3	SUBF	yes
	300b			
	3014			
ITOF AL r0,#-5	302d			
ITOF AL r1,#4	3032			
ITOF AL r2,#-3	0901			
ITOF AL r3,#2	5102			
ADDF AL r0,r1	9303			
MULF AL r0,r2	e001			
SUBF S r0,r3	4431			
MOV EQ r3, #17	3630			
ITOF NE r3, #0	5323			
MULF S r2, r3	2d30		Float instructions	
FTOI EQ r3, r0	e002	reg0 = 42	with conditional	
ADD EQ r0,#41	0409	reg2 = 0	dependenceies	
SYS AL	9800	reg3 = 1	and PRE's	yes