DELHI TECHNOLOGICAL UNIVERSITY

SUBJECT- DD-II ()

SUBMITTED To – Dr.POORNIMA MITTAL

SUBMITTED BY- Anshul (2K19/EC/022) and Abhay Lakhotra (2K19/EC/006)

Objective –

To write the VHDL code for the MIPS Processor and perform simulation.

Work Plan –

Firstly, we will use the instruction set and architecture design for the MIPS processor provided on the internet. Based on the provided instruction set, we will design and implement the data path and control unit. After completing the design for the MIPS processor, we will write the code for the whole design of the MIPS processor. After that we will verify the code by doing simulations in order to see how the MIPS processor works.

Software required-

ModelSim or Xilinx ISE

Applications -

The Microprocessor without Interlocked Pipeline Stage (MIPS) microprocessor is one of the world's most popular processors for embedded applications.

MIPS microprocessor can be found in applications ranging from hard disk controllers to laser-jet printers to gaming consoles and audio applications in consumer devices.