

Project 1 – Report requirements (Total pages – 10 to 12 except Appendix)

The report for project 1 (Cache Controller) shall include the following sections and material:

- 1) Abstract (no more than 400 words)
- 2) Introduction
- 3) System Specifications
 - a) Behavioral / Functional
- 4) Device description / design
 - (a) Symbols
 - (b) Block Diagrams
 - (c) State Diagrams
 - (d) Process Diagrams
- 5) Results
 - (a) Functional simulations
 - (b) Brief explanations of the results
- 6) Conclusions
- 7) References
- 8) Appendix with VHDL code (all blocks shall be included, with the exception of the CPU that is given)