



education

masters | computer science

iit delhi | 2005-10 | cgpa 8.9/10

- research: routing in geographically unaware wireless sensor networks
- advisor: Prof. S. N. Maheshwari

bachelors | computer science

itm, gurgaon | 2000-04 | honors

bachelors | law

faculty of law, university of delhi |
2018-21 | first division

- member of editorial board: Delhi Law Review (Student Edition) 🐼
- Para Legal Volunteer: DSLSA

awards/achievements

national talent search scholarship | ntse 1998

Awarded by NCERT, Govt. of India to only 750 students out of millions 🐼

corporate awards

- Best Poster Award: TECCI/Cadence
- Individual Excellence Award: Aricent
- Best Technical Paper: Aricent

misc test scores

• GATE-2005: All India Rank 10 • TOEFL iBT(2009): 112/120 • GMAT(2008): 770/800 • CAT-2004: 99.76 percentile

products/projects

getit/askme

- Coupon service • Fulfilment service
- Order engine • Omni-channel interface

cadence design systems

- Distributed scalable multi-threaded billion+ gate design netlist import 🐼🐼
- Fast scalable efficient future-ready FPGA board modelling framework 🐼🐼

skills

programming languages

experienced

• C • C++ • python • perl

adhoc usages

• shell • java • javascript • lisp • prolog • tcl • vba • node.js • react native

general

web/mobile development

• flask • html • js • mysql • redis • kafka

summary

- 15+ years of professional experience
- built high-performing cross-functional teams and diverse software products (data-com, electronic design automation, e-commerce) from the ground up
- experienced in product planning and risk management working remotely on multi-stakeholder project releases with geographically distributed cross-functional teams
- versatile engineer with masters in computer science coupled with a degree in law
- perpetual learner, innovator and engineer at heart
- natural problem solver with a Can-Do attitude
- leader who turns one-plus-one into eleven with collaboration, focus and direction

work experience

freelance consultant | october 2016 - present (5 years)

independent consultant at cadence design systems, noida | oct 2016 - feb 2021

- Defined product roadmap and built expert implementation teams
 - ✓ design, guide, and vet systems designs
 - ✓ review code and provide feedback
 - ✓ triage issues and setup mechanisms to prevent re-occurrence
- Re-Architected and developed distributed and scalable netlist import infrastructure ⇒ 20× improvement in runtime performance (30min turnaround time on Billion+ gates netlist, 100M+ lines of hdl/netlist over 100K+ files)
- Invented a proprietary graph cut algorithm for distributed parallel processing
- Continued track record of flawless execution of business critical projects

askme/getit infoservices pvt. ltd. | oct'2015 - sep'2016

director engineering

- Provided engineering leadership to a small team of senior managers, experienced developers working on the micro-services architecture based back-end development of various Askme products - AskmeBazaar, NDD, AskMeGrocery, AskmeDeals, AskmePay, AskMeFin, FlowerAura, AskmeFurniture
- Architected and implemented Unified Campaign Service - reduced campaign launch turnaround time from days to minutes
- Led product strategy and ownership for Unified Fulfilment Service and Order Engine
- Managed co-ordination with vendors
- Strengthened the team with technical hiring and performance management

cadence design systems | may'2013 - oct'2015

principal software engineer

- Led product innovation for Electronics Design Automation - Owned several key modules of Protium and Palladium flagship emulation platforms from Cadence
- Architected and implemented new 'board modeling' infrastructure for Protium
 - ✓ HDL (Verilog/VHDL) agnostic ✓ dynamic cable configurations ✓ modeling turnaround time ↓ weeks → minutes ✓ runtime latency ↓ by an order of magnitude ✓ Many features implemented ahead of time continue to find relevance at a scale of 100× seven years into the future
- Led R&D efforts on 'partitioning', 'memories', 'netlist-generation' and 'debug' projects
- **Best Poster Award** at TECCI event at Cadence Poster presentation describing product innovation



major courses

computer science & engineering

advanced algorithms
data structures
advanced computer architecture
compiler design
theory of computation
programming languages
operating systems
distributed systems
database management system
computer graphics
pattern recognition
discrete mathematics
computer networks
digital electronics

law

Administrative Law
Labor Law
Alternate Dispute Resolution
Contract Law
Intellectual Property Law
Competition Law
Company Law
Taxation Law
Jurisprudence
Constitutional Law
Public International Law
Interpretation of Statutes
Law of Crimes, White Collar Crimes

earlier experience

brocade | aug'2011 - may'2012

software engineer

- Led strategic planning and overall guidance for Layer 3 protocols to Data Center Ethernet L3 team

aricent | aug'2004 - aug'2011

software engineering manager

- Enabled customers to realize bold targets
- Worked on projects for clients like Alcatel-Lucent, Nokia-Siemens, Juniper and Verizon
- Built and Managed team of up to 33 engineers+managers on projects involving multiple teams, including multi-site and those from clients and vendors
- **"Individual Excellence Award"** for contributions to process improvement and employee engagement initiatives (improved Effort Estimation Model, a new Effort Tracking Tool, Code change estimation tool and a new Software Development approach based on my enhanced Requirement Traceability Matrix tool)
- Recognized as among the top innovators in the company in a poll
- Instrumental in realizing development and maintenance contract for a mobile backhaul multi-access gateway product with TDM/ATM/Ethernet interfaces. Manager and Technical Point of Contact for the customer for most modules, especially for L3/MPLS
- Cracked the mysteries in GMPLS stack with DiffServ-TE for development of TE-link management with OSPF-TE/LDP/Pseudowires
- Responsible for conception to realization of Chassis solution for Firewall product of Alcatel-Lucent
- On-site at Alcatel-Lucent, Calabasas, CA, USA. As Release Coordinator, negotiated requirements & design for all modules with all stakeholders including marketing, sales, architecture and verification teams. Managed all client-side release activities for three parallel releases
- Co-authored proprietary enhancement (later Patented) to ITU-G.8032 Standard
- **"Best Performer"** for 4 consecutive years at Aricent 2005-2008 and "Individual Excellence Award" in 2009. Highest performance rating among all peers in the Organizational unit
- **Best Technical Paper** award for White Paper on IPMVlans in Technovate'2007 event held at Aricent

internships

human rights law network | jun'2019 - jul'2019

legal intern

- led a team of lawyers and activists for bonded labor cases and rescue operations

rsb systems pvt. ltd. | jun'2002 - jul'2002

summer intern

- Contributed to data processing application development using Visual Foxpro
- Optimized scheduled tasks using Rushmore Query Optimization - 5X performance improvement
- Recognised by the client and the VP of the company for key contributions within a short time