

Sheet1

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
LD R1, [R7]	IF	ID	RD	ALU	MEM	WB										
LD R2, [R8]		IF	ID	RD	ALU	MEM	WB									
ADD R3, R1, R2			IF	ID	S	S	S	RD	ALU	MEM	WB					
JMP #6				IF	S	S	S	ID	FLUSH							
ADD R3, R3, R1								IF								
MUL R3, R3, R2																
SD [R8], R3										IF	ID	RD	ALU	MEM	WB	
HLT											IF	ID	RD	ALU	MEM	WB

Total clock cycles = 16

Clocks per Instruction = 16/6 [total instructions committed]

Number of stall cycles = 4