



CS230: Digital Logic Design and Computer Architecture

The Digital world: Logic gates

<https://www.cse.iitb.ac.in/~biswa/courses/CS230/main.html>

<https://www.cse.iitb.ac.in/~biswa/>



Phones (smart/non-smart)
on silence plz, Thanks

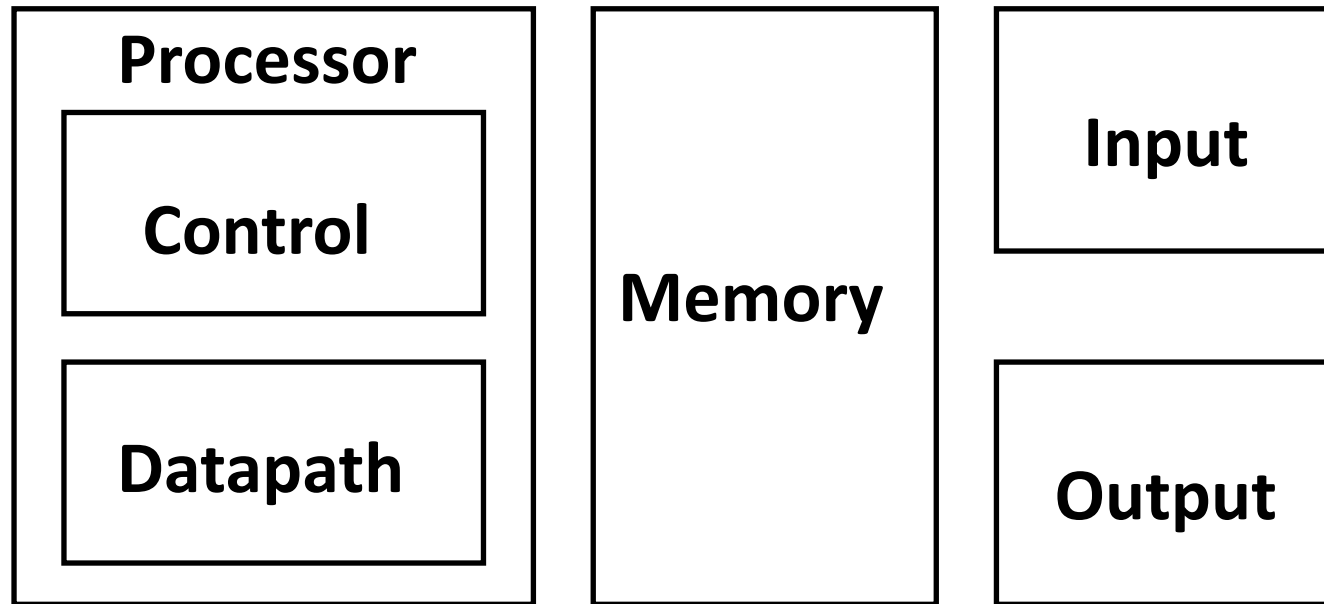


Logistics

- Join Piazza now
- Lab on Tuesday, 2 PM SL1 to SL3, attendance compulsory
- You can meet me and discuss if anything is not clear
- Problem set 1 in a week or two. Ungraded, for your practice only

Let's get started

Since 1946 all computers have had 5 components



In those days



Let's get
started: One
Step at a time



World of Digital computers

Not Analog

Digital: World of TRUE/FALSE or
1/0

World of binary variables

Logic circuits performing operations
on binary variables: Logic gates

Digits vs bits

■ Digits = powers of 10

... 100, 10, 1, $1/10$, $1/100$, $1/1000$...
... 10^2 , 10^1 , 10^0 , 10^{-1} , 10^{-2} , 10^{-3} ...

$$\text{Ex: } (36.25)_{10} = 3 \cdot 10 + 6 \cdot 1 + 2 \cdot 1/10 + 5 \cdot 1/100$$

■ Bits = powers of 2

... 8, 4, 2, 1, $1/2$, $1/4$, $1/8$...
... 2^3 , 2^2 , 2^1 , 2^0 , 2^{-1} , 2^{-2} , 2^{-3} ...

$$\text{Ex: } (100100.01)_2 = 1 \cdot 32 + 1 \cdot 4 + 1 \cdot 1/4$$

Decimal to binary

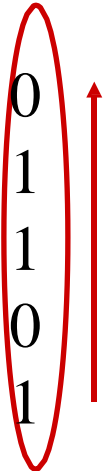
■ Left of decimal point

- Repeatedly divide integer part by 2 until you get 0
- Read remainders bottom to up

$$22 = (?)_2 \quad (10110)_2$$

22

11	R	0
5	R	1
2	R	1
1	R	0
0	R	1



Decimal to binary

- Right of decimal point
 - Repeatedly multiply fractional part by 2 until you get 1
 - Read integer portion top to bottom

$$0.8125 = (0.1101)_2$$

0.8125

1.6250

1.25

0.5

1.0

Both?

■ What if there are both left and right of the decimal point?

□ Do them separately and combine

• $22.8125 = (10110.1101)_2$

22

11 R 0

5 R 1

2 R 1

1 R 0

0 R 1



0.8125

1.6250

1.25

0.5

1.0



Binary Number System

1's column
10's column
100's column
1000's column

$$9742_{10} = 9 \times 10^3 + 7 \times 10^2 + 4 \times 10^1 + 2 \times 10^0$$

nine thousands
seven hundreds
four tens
two ones

1's column
2's column
4's column
8's column
16's column

$$10110_2 = 1 \times 2^4 + 0 \times 2^3 + 1 \times 2^2 + 1 \times 2^1 + 0 \times 2^0 = 22_{10}$$

msb
lsb
one sixteen
no eight
one four
one two
no one

Convert 84_{10} to binary

Convert 84_{10}
to binary

1010100_2

Similarly hexadecimal (base 16)

$2ED_{16}$

Convert this to decimal and binary

Binary addition

Simple

$$1 + 0 = 1$$

$$0 + 0 = 0$$

$$1 + 1 = 0 \text{ with carry } 1$$

So far
unsigned,
what about
signed

most significant bit denotes sign and
remaining N-1 bits denote value
(Sign/magnitude numbers)

$5_{10} : 0101_2$

$-5_{10} : 1101_2$

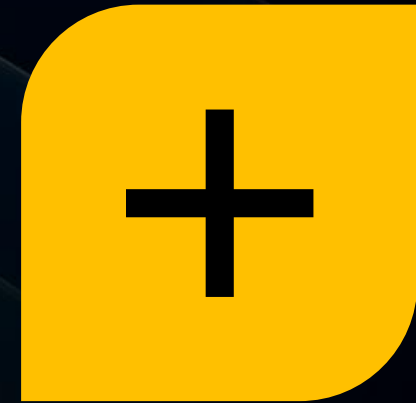
Binary addition does not make sense



$5 + (-5) = 0$ BUT NOT IN
SIGNED/MAGNITUDE



BTW, ZERO HAS TWO REPRESENTATIONS
IN SIGN MAGNITUDE +0 AND -0 , WHICH
IS SO CONFUSING



SOLUTION? 2'S COMPLEMENT

The 2's complement way for negative numbers

Take the complement of a binary number and add 1 to the lsb (least significant bit)

$-5_{10}: ?_2$

$5_{10}: 0101$, complement: 1010 , 2's complement: $101\mathbf{1}$

Range of Numbers

System	Range
Unsigned	$[0, 2^N - 1]$
Sign/Magnitude	$[-2^{N-1} + 1, 2^{N-1} - 1]$
Two's Complement	$[-2^{N-1}, 2^{N-1} - 1]$

Remember sign/magnitude has two zeros ☺

Sign Extension

To represent a signed number in 2's complement form using large number of bits

Repeat the sign bit at the msbs as needed

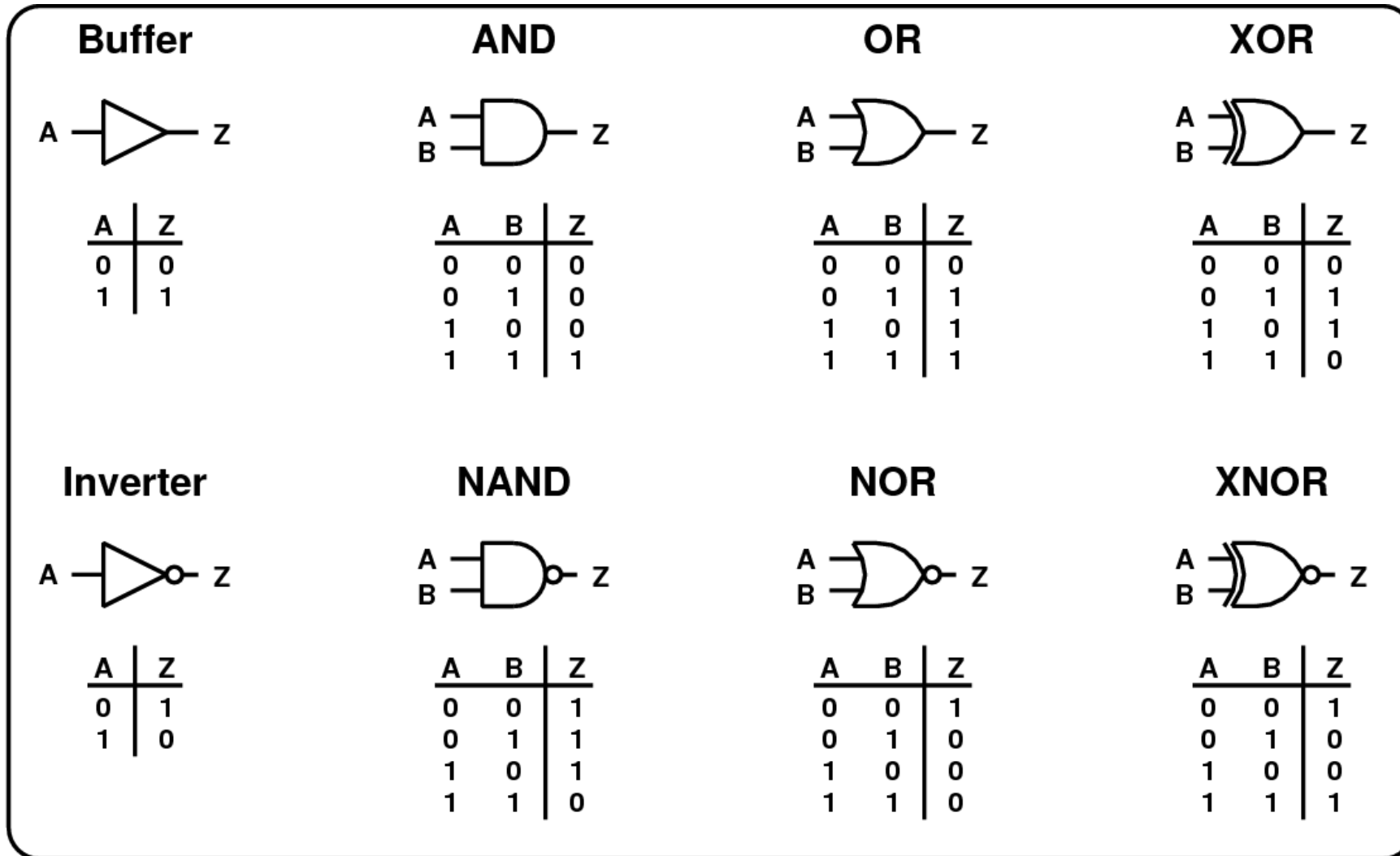
Overflow

1101 + 0101 ?

A photograph of a white ceramic cup filled with dark coffee, sitting on a matching saucer. The cup and saucer are placed on a light-colored, reflective surface. A soft shadow is cast to the right of the cup. In the background, a dark, out-of-focus area suggests a window or a wall. The word "PAUSE" is written in a large, white, sans-serif font across the middle of the cup.

PAUSE

Common Logic Gates





Universal Logic gates? Coffee points++

NAND and NOR

A bit of Boolean algebra

Operations with 0 and 1:

1. $X + 0 = X$
2. $X + 1 = 1$

Dual



- 1D. $X \cdot 1 = X$
- 2D. $X \cdot 0 = 0$

AND, OR with identities
gives you back the original
variable or the identity (dot: AND, plus: OR)

Idempotent Law:

3. $X + X = X$

- 3D. $X \cdot X = X$

AND, OR with self = self

Involution Law:

4. $\overline{\overline{X}} = X$

double complement =
no complement

Laws of Complementarity:

5. $X + \overline{X} = 1$

- 5D. $X \cdot \overline{X} = 0$

AND, OR with complement
gives you an identity

Commutative Law:

6. $X + Y = Y + X$

- 6D. $X \cdot Y = Y \cdot X$

Just an axiom...

Contd.

Associative Laws:

$$7. (X + Y) + Z = X + (Y + Z) \\ = X + Y + Z$$

$$7D. (X \cdot Y) \cdot Z = X \cdot (Y \cdot Z) \\ = X \cdot Y \cdot Z$$

Parenthesis order
does not matter

Distributive Laws:

$$8. X \cdot (Y + Z) = (X \cdot Y) + (X \cdot Z)$$

$$8D. X + (Y \cdot Z) = (X + Y) \cdot (X + Z) \quad \text{Axiom}$$

Simplification Theorems:

$$9. X \cdot Y + X \cdot \bar{Y} = X$$

$$9D. (X + Y) \cdot (X + \bar{Y}) = X$$

$$10. X + X \cdot Y = X, \text{ how?}$$

$$10D. X \cdot (X + Y) = X$$

$$11. (X + \bar{Y}) \cdot Y = X \cdot Y$$

$$11D. (X \cdot \bar{Y}) + Y = X + Y$$

Useful for
simplifying
expressions

Actually worth remembering — they show up a lot in real designs...

DeMorgan's Law (Can you prove it)?

$$12. \overline{(X + Y + Z + \dots)} = \bar{X} \cdot \bar{Y} \cdot \bar{Z} \dots$$

$$12D. \overline{(X \cdot Y \cdot Z \dots)} = \bar{X} + \bar{Y} + \bar{Z} + \dots$$

■ Think of this as a transformation

- Let's say we have:

$$F = A + B + C$$

- Applying DeMorgan's Law (12), gives us

$$F = \overline{(A + B + C)} = \overline{(\bar{A} \cdot \bar{B} \cdot \bar{C})}$$

At least one of A, B, C is TRUE --> It is **not** the case that A, B, C are **all** false

Contd. with a Truth Table

$$A = \overline{(X + Y)} = \bar{X}\bar{Y}$$

**NOR is equivalent to AND
with inputs complemented**



X	Y	$\overline{X + Y}$	\bar{X}	\bar{Y}	$\bar{X}\bar{Y}$
0	0	1	1	1	1
0	1	0	1	0	0
1	0	0	0	1	0
1	1	0	0	0	0

$$B = \overline{(XY)} = \bar{X} + \bar{Y}$$

**NAND is equivalent to OR
with inputs complemented**



X	Y	\overline{XY}	\bar{X}	\bar{Y}	$\bar{X} + \bar{Y}$
0	0	1	1	1	1
0	1	1	1	0	1
1	0	1	0	1	1
1	1	0	0	0	0

Remember: It is not

$$\overline{(X \cdot Y)} = \bar{X} \cdot \bar{Y}$$

$$\overline{(X + Y)} = \bar{X} + \bar{Y}$$

Definitions of interest

- A **normal term** is a product or sum term in which no variable appears more than once.
 - Examples: $a, \bar{a}, a+c, \bar{a}cd$ are normal terms; $\bar{a}+a, \bar{a}a$ are not normal terms.
- A **minterm** of n variables is a normal product term with n literals. There are 2^n such product terms.
 - Examples of 3-variable minterms: $\bar{a}bc, abc$
 - Example: $\bar{a}b$ is not a 3-variable minterm.
- A **maxterm** of n variables is a normal sum term with n literals. There are 2^n such sum terms.
 - Examples of 3-variable maxterms: $\bar{a}+b+c, a+b+c$

Definitions of interest

- A **sum of products (SOP)** expressions is a set of product (AND) terms connected with logical sum (OR) operators.
 - ▣ Examples: $a, \bar{a}, ab+c, \bar{a}c+bde, a+b$ are SOP expressions.
- A **product of sum (POS)** expressions is a set of sum (OR) terms connected with logical product (AND) operators.
 - ▣ Examples: $a, \bar{a}, a+b+c, (\bar{a}+c)(b+d)$ are POS expressions.

Definitions of interest

- The **canonical sum of products (CSOP)** form of an expression refers to rewriting the expression as a sum of minterms.
 - Examples for 3-variables: $\bar{a}bc + abc$ is a CSOP expression; $\bar{a}b + c$ is not.
- The **canonical product of sums (CPOS)** form of an expression refers to rewriting the expression as a product of maxterms.
 - Examples for 3-variables: $(\bar{a}+b+c)(a+b+c)$ is a CPOS expression; $(\bar{a}+b)c$ is not.
- There is a close correspondence between the truth table and minterms and maxterms.

SOP: Sum of Products

Also known as **disjunctive normal form** or **minterm expansion**

Find all the input combinations (minterms) for which the output of the function is TRUE.

A	B	C	F	
0	0	0	0	
0	0	1	0	
0	1	0	0	
0	1	1	1	← 0 1 1
1	0	0	1	← 1 0 0
1	0	1	1	← 1 0 1
1	1	0	1	← 1 1 0
1	1	1	1	

- Each row in a truth table has a minterm
- A minterm is a product (AND) of literals
- Each minterm is TRUE for that row (and only that row)

All Boolean equations can be written in SOP form

Contd.

A	B	C	F		0 1 1	1 0 0	1 0 1	1 1 0	1 1 1
0	0	0	0		$\bar{A}BC$	$A\bar{B}\bar{C}$	$A\bar{B}C$	$AB\bar{C}$	ABC
0	0	1	0						
0	1	0	0						
0	1	1	1	←					
1	0	0	1	←					
1	0	1	1	←					
1	1	0	1	←					
1	1	1	1	←					

$F = \bar{A}BC + A\bar{B}\bar{C} + \boxed{A\bar{B}C} + AB\bar{C} + ABC$

Activates this term

- Only the shaded product term — $A\bar{B}C = 1 \cdot \bar{0} \cdot 1$ — will be 1

Contd.

- Standard “shorthand” notation
 - If we agree on the **order** of the variables in the rows of truth table...
 - then we can enumerate each row with the decimal number that corresponds to the binary number created by the input pattern

A	B	C	F
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

100 = decimal 4 so this is minterm #4, or m4

111 = decimal 7 so this is minterm #7, or m7

f =

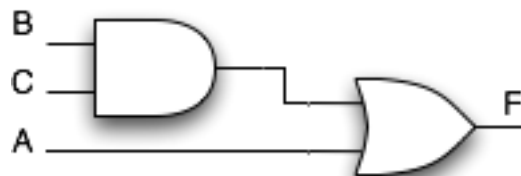
We can write this as a sum of products

Or, we can use a summation notation

Contd.

A	B	C	minterms
0	0	0	$\overline{A}\overline{B}\overline{C}$ = m0
0	0	1	$\overline{A}\overline{B}C$ = m1
0	1	0	$\overline{A}B\overline{C}$ = m2
0	1	1	$\overline{A}BC$ = m3
1	0	0	$A\overline{B}\overline{C}$ = m4
1	0	1	$A\overline{B}C$ = m5
1	1	0	$AB\overline{C}$ = m6
1	1	1	ABC = m7

Shorthand Notation for
Minterms of 3 Variables



2-Level AND/OR
Realization

F in canonical form:

$$F(A,B,C) = \sum m(3,4,5,6,7) \\ = m3 + m4 + m5 + m6 + m7$$

$F =$

canonical form \neq minimal form

F

We are on the same page?

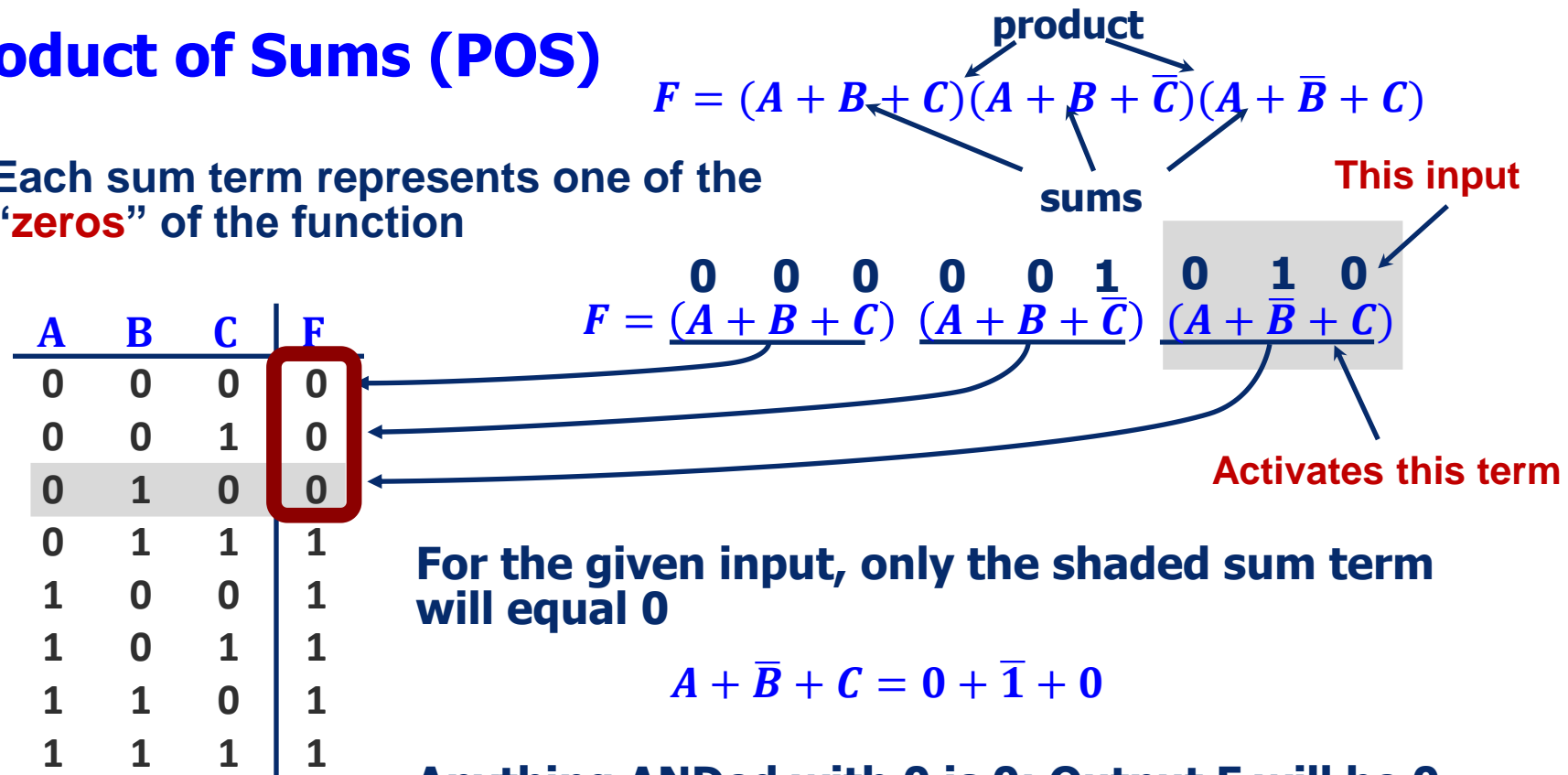
Row number	x_1	x_2	x_3	Minterm	Maxterm
0	0	0	0	$m_0 = \overline{x}_1\overline{x}_2\overline{x}_3$	$M_0 = x_1 + x_2 + x_3$
1	0	0	1	$m_1 = \overline{x}_1\overline{x}_2x_3$	$M_1 = x_1 + x_2 + \overline{x}_3$
2	0	1	0	$m_2 = \overline{x}_1x_2\overline{x}_3$	$M_2 = x_1 + \overline{x}_2 + x_3$
3	0	1	1	$m_3 = \overline{x}_1x_2x_3$	$M_3 = x_1 + \overline{x}_2 + \overline{x}_3$
4	1	0	0	$m_4 = x_1\overline{x}_2\overline{x}_3$	$M_4 = \overline{x}_1 + x_2 + x_3$
5	1	0	1	$m_5 = x_1\overline{x}_2x_3$	$M_5 = \overline{x}_1 + x_2 + \overline{x}_3$
6	1	1	0	$m_6 = x_1x_2\overline{x}_3$	$M_6 = \overline{x}_1 + \overline{x}_2 + x_3$
7	1	1	1	$m_7 = x_1x_2x_3$	$M_7 = \overline{x}_1 + \overline{x}_2 + \overline{x}_3$

POS: Product of Sum

Find all the input combinations (maxterms) for which the output of the function is FALSE.

Product of Sums (POS)

Each sum term represents one of the “**zeros**” of the function



For the given input, only the shaded sum term will equal 0

Anything ANDed with 0 is 0; Output F will be 0

The function evaluates to FALSE (i.e., output is 0) if **any** of the Sums (maxterms) causes the output to be 0

Contd.

1. **Minterm to Maxterm conversion:**
rewrite minterm shorthand using maxterm shorthand
replace minterm indices with the indices not already used
E.g., $F(A, B, C) = \sum m(3, 4, 5, 6, 7) = \prod M(0, 1, 2)$
2. **Maxterm to Minterm conversion:**
rewrite maxterm shorthand using minterm shorthand
replace maxterm indices with the indices not already used
E.g., $F(A, B, C) = \prod M(0, 1, 2) = \sum m(3, 4, 5, 6, 7)$
3. **Expansion of F to expansion of \bar{F} :**

$$\begin{array}{ll} \text{E. g., } F(A, B, C) = \sum m(3, 4, 5, 6, 7) & \longrightarrow \bar{F}(A, B, C) = \sum m(0, 1, 2) \\ = \prod M(0, 1, 2) & \longrightarrow = \prod M(3, 4, 5, 6, 7) \end{array}$$

4. **Minterm expansion of F to Maxterm expansion of \bar{F} :**
rewrite in Maxterm form, using the same indices as F

$$\begin{array}{ll} \text{E. g., } F(A, B, C) = \sum m(3, 4, 5, 6, 7) & \longrightarrow \bar{F}(A, B, C) = \prod M(3, 4, 5, 6, 7) \\ = \prod M(0, 1, 2) & \longrightarrow = \sum m(0, 1, 2) \end{array}$$

Digital Logic ☹️

Boring lectures will go away
soon 😊

Once we jump into architecture

Till then “Hang in there”

