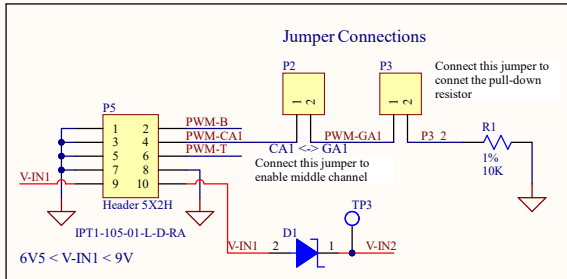
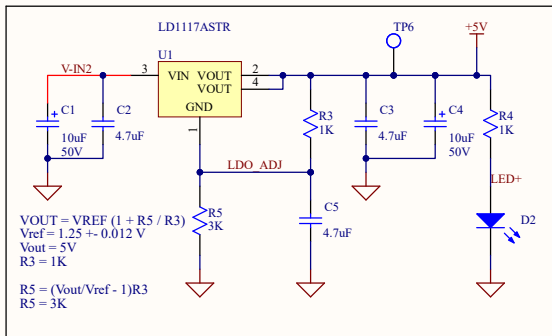


Haddock Power Switching Module with Sensing

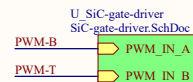
Control signals and Logic Voltage Input



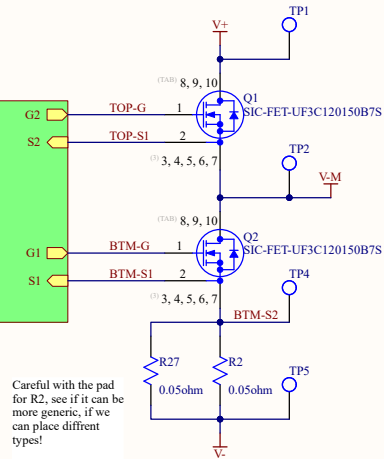
LDO 5V Adjustable Linear Regulator



Isolated Control Side

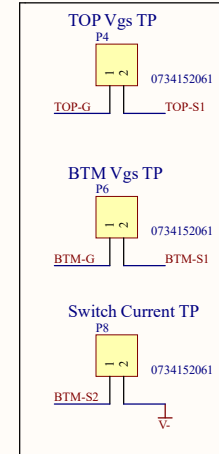


Isolated Switching Side

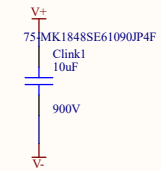


Careful with the pad for R2, see if it can be more generic, if we can place different types!

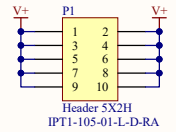
MMCX Sockets



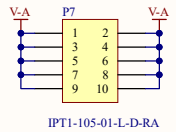
C-link Capacitors



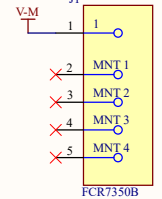
Top Terminal Connector



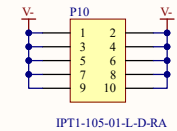
Mid Terminal Connector



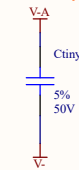
Pole Connector



Bottom Terminal Connector



C-tiny Capacitor

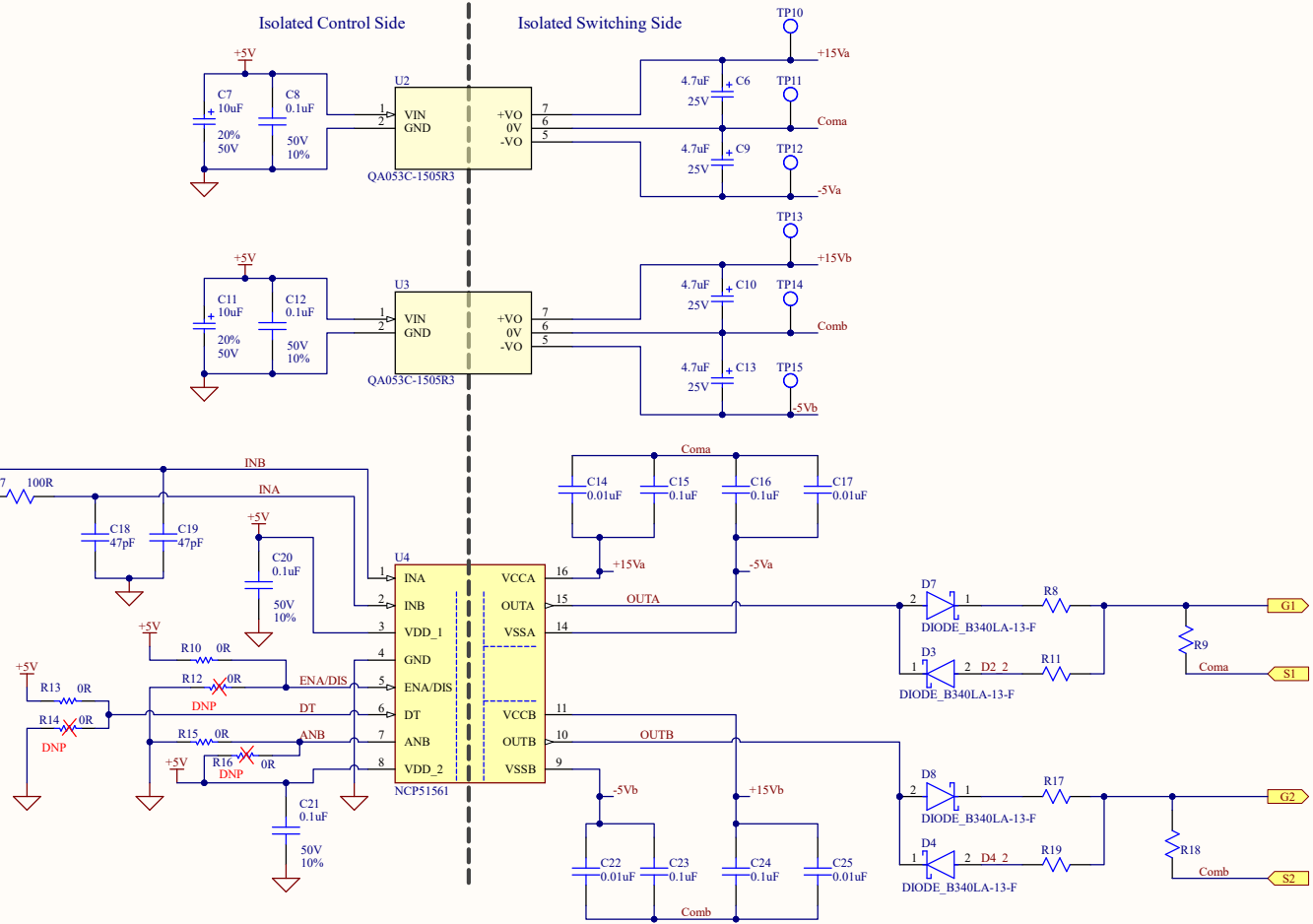


NP

Title			
Haddock Power Switching Module with Sensing			
Size	Number	Revision	
B		0.1	
Date:	3/08/2024	Sheet 1 of 3	
File:	G:\My Drive\...haddock-sm-s.SchDoc	Drawn By: Abhijcet Prem	

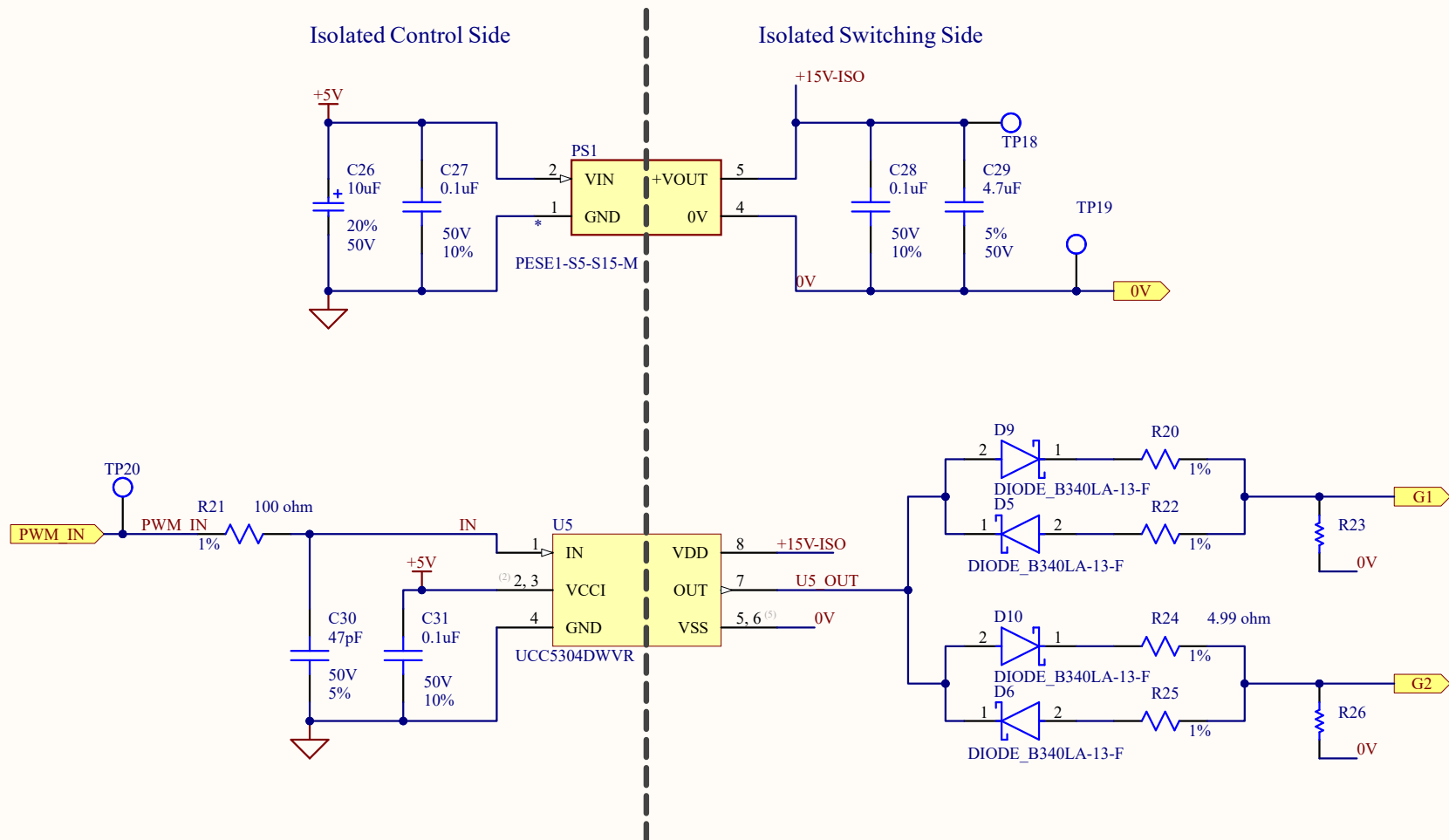
Isolated SiC Gate Driver Circuit

Pin	Config	Function
ENA	R10: +, R12: DNP	Normal operation
ENA	R10: DNP, R12: +	Outputs Disabled
DIS	R10: +, R12: DNP	Outputs Disabled
DIS	R10: DNP, R12: +	Normal operation
DT	R13: +, R14: DNP	MODE C: overlapping allowed
DT	R13: DNP, R14: +	MODE B: programmed dead time R14: 1K -- 300K
DT	R13: DNP, R14: DNP	MODE A: Minimum dead time = 10ns
ANB	R15: +, R16: DNP	Dual input, Dual output
ANB	R15: DNP, R16: +	Singel input (INA), Dual output



Title		
Isolated SiC Gate Driver Circuit		
Size	Number	Revision
B		A
Date:	3/08/2024	Sheet of
File:	G:\My Drive\...\SiC-gate-driver.SchDoc	Drawn By: Abhijeet Prem

Isolated Switching Side



Title			Isolated MOSFET Gate Driver Circuit		
Size	Number			Revision	
A				A	
Date:	3/08/2024			Sheet of	
File:	G:\Mv Drive\Si-gate-driver.SchDoc			Drawn By: <u>Abhijeet Prem</u>	