

## ***1. Description***

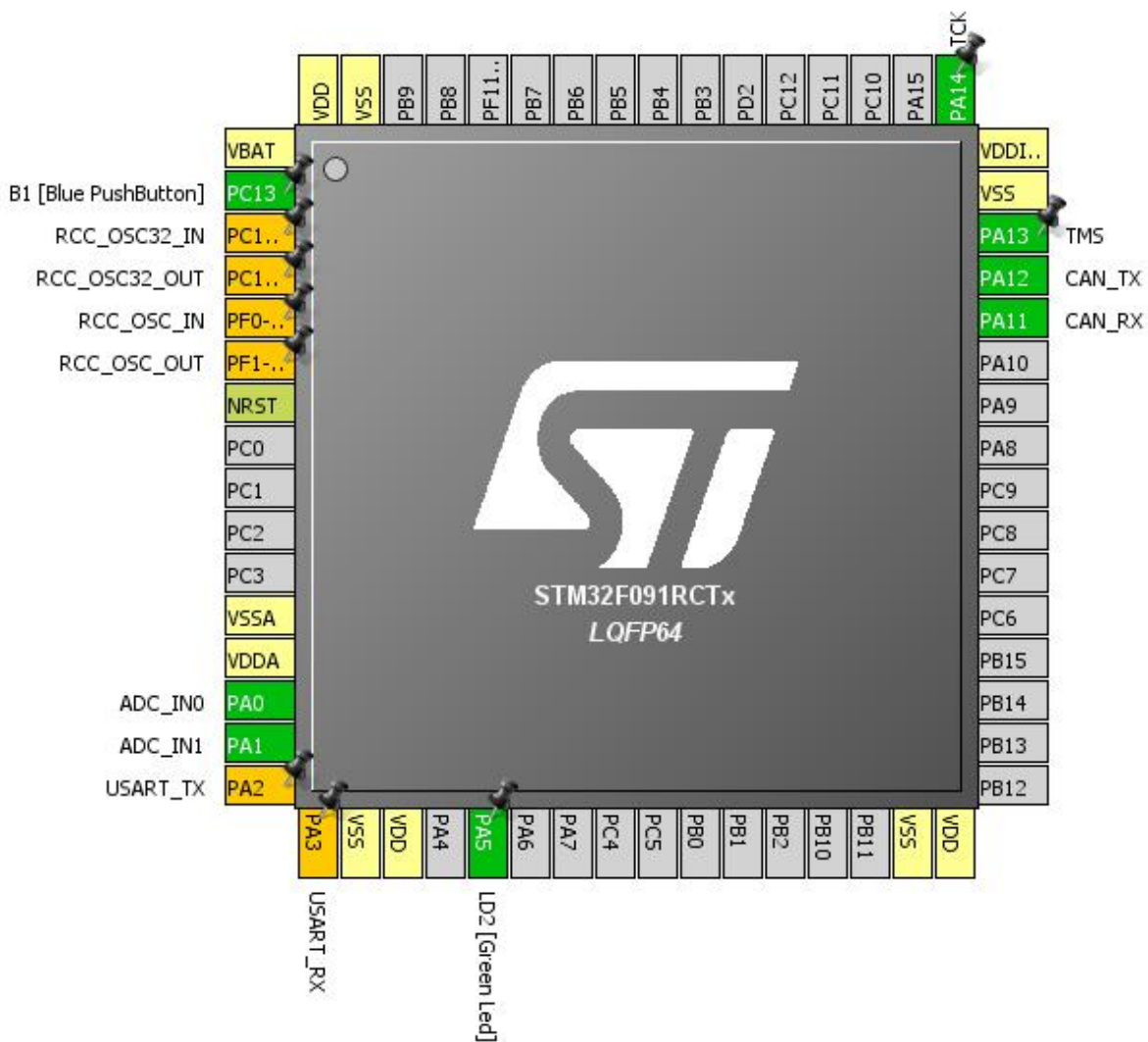
### 1.1. Project

Project Name	CAN
Generated with:	STM32CubeMX 4.7.1
Date	05/16/2015

### 1.2. MCU

MCU Serie	STM32F0
MCU Line	STM32F0x1
MCU name	STM32F091RCTx
MCU Package	LQFP64
MCU Pin number	64

## 2. Pinout Configuration



### 3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
ADC	IN0	ADC_IN0	PA0
	IN1	ADC_IN1	PA1
CAN	Mode	CAN_RX	PA11
		CAN_TX	PA12
SYS	Serial-WireDebug	SYS_SWCLK	PA14
		SYS_SWDIO	PA13

MiddleWare	Mode
FREERTOS	Enabled

## 4. Pins Configuration

Pin	Pos	Function(s)	Label
PC13	2	GPIO_EXTI13	B1 [Blue PushButton]
PC14OSC32_IN *	3	RCC_OSC32_IN	
PC15OSC32_OUT *	4	RCC_OSC32_OUT	
PF0-OSC_IN *	5	RCC_OSC_IN	
PF1-OSC_OUT *	6	RCC_OSC_OUT	
PA0	14	ADC_IN0	
PA1	15	ADC_IN1	
PA2 *	16	USART2_TX	USART_TX
PA3 *	17	USART2_RX	USART_RX
PA5 **	21	GPIO_Output	LD2 [Green Led]
PA11	44	CAN_RX	
PA12	45	CAN_TX	
PA13	46	SYS_SWDIO	TMS
PA14	49	SYS_SWCLK	TCK

\*\* The pin is affected with an I/O function

\* The pin is affected with a peripheral function but no peripheral mode is activated

## ***5. Power Plugin report***

### 5.1. Microcontroller Selection

Serie	STM32F0
Line	STM32F0x1
MCU	STM32F091RCTx
Datasheet	026284_Rev1

### 5.2. Parameter Selection

Temperature	25
Vdd	3.6

## 6. Software Project

### 6.1. Project Settings

Name	Value
Project Name	CAN
Project Folder	E:\Special Projects\CAN_HS-LS_GW40_Software\Board_STM32Nucleo
Toolchain / IDE	MDK-ARM V5
Firmware Package Name and Version	STM32Cube FW_F0 V1.2.1

### 6.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

### 6.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed