

THE 4th INTERNATIONAL CONFERENCE
ON VLSI, COMMUNICATION AND SIGNAL PROCESSING

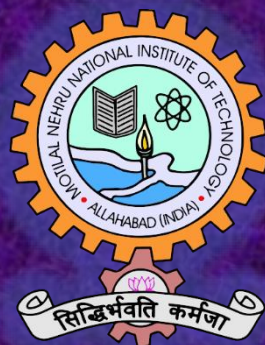
VCAS 2021

SEPTEMBER 24-26, 2021
@MNNIT ALLAHABAD

CALL FOR PAPER

“On VLSI, Communication and Signal Processing”

Selected & presented papers from the conference is to be published by **Springer** as a proceedings book volume indexed in **SCOPUS** database



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Organised By:

The Department of Electronics and Communication Engineering,
Motilal Nehru National Institute of Technology Allahabad,
Prayagraj, Uttar Pradesh, India, 211004.



Motilal Nehru National Institute of Technology (MNNIT) ALLAHABAD

(SEPTEMBER 24-26, 2021)

Conference Website: <http://www.mnnit.ac.in/vcas2021>

Organized by: Electronic and Communication Engineering Department, MNNIT Allahabad



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Category	Indian Delegates	Foreign Delegates
Student	INR 1,000/-	USD 50
Faculty	INR 2,000/-	USD 100
Industry	INR. 2,000/-	USD 100

CONTACT INFORMATION

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ABOUT THE CONFERENCE

The 4th IEEE International Conference on **VLSI, Communication and Signal Processing (VCAS 2021)** in will be held in the Sangam city of Uttar Pradesh, India, during September 24-26, 2021, hosting by the Department of Electronics & Communication Engineering, Motilal Nehru National Institute of Technology Allahabad, Prayagraj, India in association with Springer. The conference aims to offer a platform for the researchers, academicians, individuals working in the industry and students to showcase their research innovations and get recognition. VCAS 2021 aspires to bring together research professional fraternity for the discussions and exchange of information on advances in VLSI, Signal Processing and Communications.

MNNIT-VCAS Conferences Springer Publication Links:

VCAS 2018: <https://www.springer.com/gp/book/9789813297746>

VCAS 2019: <https://www.springer.com/gp/book/9789811568398>

VCAS 2020: In press

IMPORTANT DATES

<i>Paper Submission Starts</i>	<i>June 18, 2021</i>
<i>Paper Submission Deadline</i>	<i>July 31, 2021</i>
<i>Acceptance Notification</i>	<i>August 20, 2021</i>
<i>Start of Registration</i>	<i>August 25, 2021</i>
<i>End of Registration</i>	<i>September 10, 2021</i>
<i>Camera Ready Paper Submission Deadline</i>	<i>September 10, 2021</i>
<i>Conference Date</i>	<i>September 24-26, 2021</i>

EASY CHAIR LINK FOR PAPER SUBMISSION

<https://easychair.org/my/conference?conf=vcas2021>

PUBLICATION

Selected and presented papers from the conference is to be undergo quality and plagiarism checks by Springer. The papers that pass these checks will be published by Springer as proceedings in the book series Lecture Notes in Electrical Engineering (LNEE) (*Scopus and Web of Science indexed*).

TRACKS

VLSI

Analog, Digital & Mixed Signal Design, Testing & Testable Circuit, Low-Power Design, Packaging & Interconnect, 2D material-based Devices, MEMS & NEMS design and Technology, Devices Modeling and Process simulation, Thin Film, Ultra-High-Speed Transistors, HEMT/HBT, Reconfigurable Architecture Design, Power Devices and Reliability, Artificial Intelligence (Process & Device), Internet of Things (Process & Device), Emerging Technology

Communication

Communication Theory and Applications, Optical Networks and Systems Microwave Communications, Algorithms and Techniques for 5G and beyond Wireless and Ad Hoc Networks, IOT and Sensor Networks, Antenna Design for Communication Systems and Networks, Vehicular Communications and Networks, Wireless Modeling/Algorithms and Simulation, Cognitive Radio and Spectrum Management, Adaptive Communication Systems and Networks, Intelligent Protocols and Designing for Communication Systems and Networks, Computer Communication.

Signal Processing

Signal Processing: Theory, Methods and Applications, Signal Processing Systems and Architectures, Signal Processing Algorithms, Image/Video and Multidimensional Signal processing, Multimedia Signal Processing, Biomedical Signal Processing and Bioinformatics, Signal Processing for Communication and Networking, Signal Processing for Machine Learning and IoT, Gnostic Signal Processing, Coding and Compression, Signal Processing for Smart systems, Sparse Signal Processing, Signal processing for VLSI.

* The Topics of interest include but are not limited to the above-mentioned Tracks