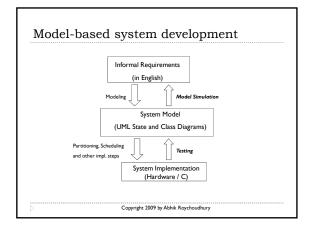


Flow of today's lecture

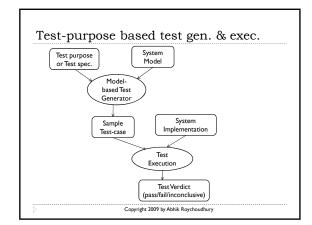
- ▶ Test generated from models
 - Run on implementation.
- ▶ How to find a "suitable" test case?
- ▶ What is the purpose of testing?
- Finding a "suitable" test case guided by test specification
 - Given a test specification, we search the model to find a test?
- ▶ Two questions
 - ▶ How to describe test specifications temporal logics.
 - ▶ How to search the system model model checking.

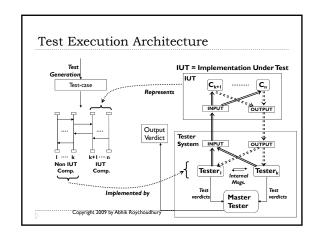
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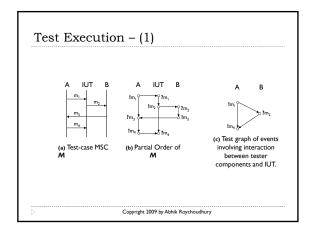


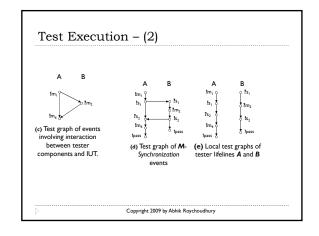
Model-based testing

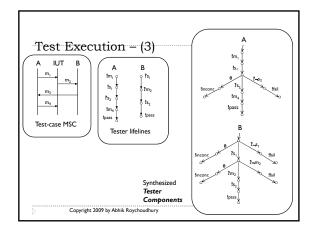
- Generate test-cases from model, run them on the implementation.
- ▶ What are the criteria for generating test cases?
- Generate a suite of test cases to ensure a structural coverage of the model
 - ▶ State coverage, Transition coverage for State Diagrams.
- Generate test cases from the model based on some test specification
 - ▶ How to describe the test specification?
 - $\ \ \Box \ \ \textit{Temporal logic (discussed later)}$
 - How to find a test satisfying a test specification?
 □ Model checking (discussed later)

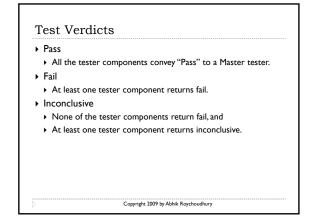


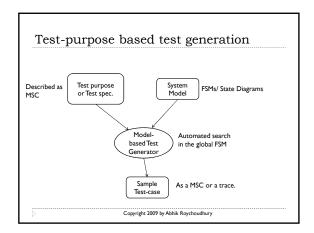


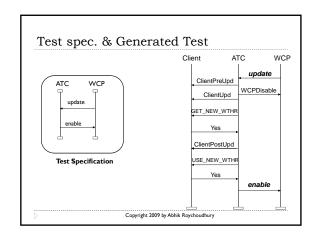








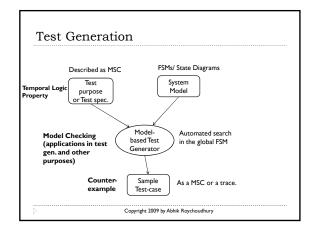




Test spec. & Generated test ▶ Test spec. is in the form of an MSC M.

- \blacktriangleright A trace σ satisfies a test specification M if σ contains at least one linearization of \boldsymbol{M} as a contiguous subsequence.
- \blacktriangleright A trace σ satisfies a test specification M if σ contains at least one linearization of M as a subsequence.
- ▶ Which def. did we follow in the previous slide?

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Organization

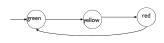
- ▶ So Far
- ▶ What is a Model?
- ▶ ATC Running Example
- ▶ How to model such requirements
- ▶ How to validate the models

 - ▶ Simulations,
 - Model-based testing,
- ► Model Checking (discussed now)
 - $\hfill\Box$ Temporal logics (the property specification)
 - □ Checking method
- Also, model-based testing accomplished by model checking

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The big picture ↓ Simulate System to be System Model built (Dream or requirements) (Rough Checking Method Properties to Satisfy Refine the (caution) (Automated) Temporal logics and model checking have a general usage in model / system validation, apart from test generation in model-based Counter-Copyright 2009 by Abhik Roychoudhury

Example System Model





Infinite length traces Possible to have infinitely many traces.

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Temporal Logic

- ► On June 1 2007, I am teaching temporal logics which will be followed by teaching of model checking on June 8,
- ▶ Teaching of temporal logics occurs I week before the teaching of model checking.
- ▶ Teaching of temporal logics is always eventually followed by the teaching of model checking.
- ▶ Teaching of temporal logics is always immediately followed by the teaching of model checking.

Example properties

- ▶ The light is always green.
- ▶ Whenever the light is red, it eventually becomes green.
- ▶ Whenever the light is green, it remains green until it becomes yellow.
- **)** ...
- Are these properties true for the 2 example models in the previous slide?
 - Let us try the second property for example ...

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When is a property satisfied?

- A property is interpreted on the traces of a system model.
 - Given a trace of the system model x and a property p, we can uniquely determine a yes/no answer to whether x satisfies p.
- A property p is satisfied by a system model M, if all traces of M satisfy p.
- ▶ So, given a system model what are its traces?

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Traces of a system model



- Only one trace, it has infinite length
- (green, yellow, red) repeated forever (green, yellow, red) ^ω

Written as

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Traces of a system model



- Infinitely many traces, each of infinite length
 - ▶ (green)[∞] I trace
- ► (green)* yellow (red) - many traces
- (green)*yellow (red)* (green) ^ω
- **...**
- $\blacktriangleright\,$ (green, yellow, red) $^\omega$

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Property Specification Language

- ▶ Properties in our property spec. language will be interpreted over infinite length traces.
- Finite length traces can be converted into infinite length traces by putting a self-loop at last state.
- A property is satisfied by a system model if all execution traces satisfy the property.
 - In general, we cannot test the property on each exec. trace infinitely many of them.
 - ▶ Model checking is smarter we discuss it later!
- We formally describe the property spec. lang. or logic

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Why study new logics?

- ▶ Need a formalism to specify properties to be checked
- ▶ Our properties refer to dynamic system behaviors
- ▶ Eventually, the system reaches a stable state
- Never a deadlock can occur
- We want to maintain more than input-output properties (which are typical for transformational systems).
 - Input-output property: for input > 0, output should be > 0
 - ▶ No notion of output or end-state in reactive systems

Why study new logics?

- Our properties express constraints on dynamic evolution of states
- Propositional/first-order logics can only express properties of states, not properties of traces
- We study behaviors by looking at all execution traces of the system.
 - Linear-time Temporal Logic (LTL) is interpreted over execution traces of a system model.

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Formally, system model is

- ▶ Model for reactive systems
 - $M = (S, I, \rightarrow, L)$
 - ▶ S is the set of states
- \blacktriangleright S0 \subseteq S is the set of initial states
- $\blacktriangleright \ \rightarrow \ \subseteq S \times S$ is the transition relation
 - ▶ Set of (source-state, destination-state) pairs
- \blacktriangleright L: is the labeling function mapping S to 2^{AP}
 - ▶ Maps each state s to a subset of AP
 - These are the atomic prop. which are true in s.

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Atomic Propositions

- All of our properties will contain atomic props.
- These atomic props. will appear in the labeling function of the system model you verify.
- The atomic props. represent some relationships among variables in the design that you verify.
- ▶ Atomic props in the following example
- reen, yellow, red (marked inside the states with obvious labeling function).



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Linear-time Temporal Logic

- The temporal logic that we study today build on a "static" logic like propositional logic.
 - ▶ Used to describe/constrain properties inside states.
- ▶ Temporal operators describe properties on execution traces.
 - Used to describe/constrain evolution of states.
- ▶ Time is not explicitly mentioned in the formulae
 - $\,\blacktriangleright\,$ Properties describe how the system should evolve over time.

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Linear-time Temporal Logic

- Does not capture exact timing of events, but rather the relative order of events
- We capture properties of the following form.
 - ▶ Whenever event e occurs, eventually event e' must occur.
- ▶ We do not capture properties of the following form.
 - At t = 2 e occurs followed by e' occurring at t = 4.

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Notations and Conventions

- > An LTL formula ϕ is interpreted over an infinite sequence of states π = s0,s1, ...
- Use M, π |= ϕ to denote that formula ϕ holds in path π of system model M.
- ▶ Define semantics of LTL formulae w.r.t. a system model M.
- An LTL property φ is true of a system model iff all its traces satisfy φ
- M $|=\phi$ iff M, π $|=\phi$ for all traces π in system model M

Notations and Conventions

- M,π |= φ
 - Path $\pi = s_0, s_1, s_2,...$ in model M satisfies property ϕ
- M,π^k |= φ
 - \blacktriangleright Path \boldsymbol{s}_k , \boldsymbol{s}_{k+1} , ... in model M satisfies property ϕ
- ▶ We now use these notations to define the syntax & semantics of LTL.

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LTL - syntax

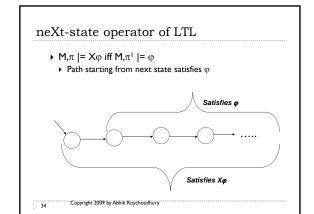
- ▶ Propositional Linear-time Temporal logic
- $\qquad \qquad \bullet \quad \phi = \mathsf{X} \phi \mid \mathsf{G} \phi \mid \mathsf{F} \phi \mid \phi \; \mathsf{U} \; \phi \mid \phi \; \mathsf{R} \; \phi \mid$ $\neg \phi \mid \phi \land \phi \mid \mathsf{Prop}$
- ▶ Prop is the set of atomic propositions
- ▶ Temporal operators
- X (next state)
- F (eventually), G (globally)
- ▶ U (until), R (release)

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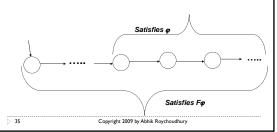
Semantics of propositional logic

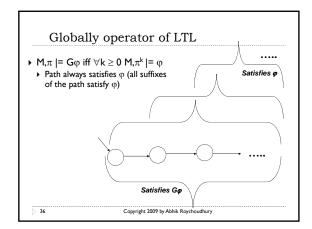
- \blacktriangleright M,π |= p iff s0 |= p i.e. p \in L(s0) where L is the labeling function of Kripke Structure M
- M, $\pi \mid = \neg \phi$ iff $\neg (M, \pi \mid = \phi)$
- M, π |= ϕ I \wedge ϕ 2 iff M, π |= ϕ I and M, π |= ϕ 2

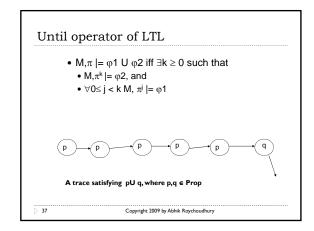
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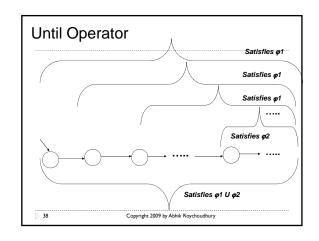


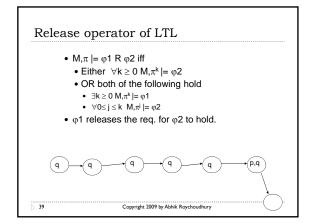
Finally operator of LTL $\blacktriangleright \ M,\pi \mid = F\phi \ iff \ \exists k \geq 0 \ M,\pi^k \mid = \phi$ \blacktriangleright Path starting from an eventually reached state satisfies ϕ Satisfies φ

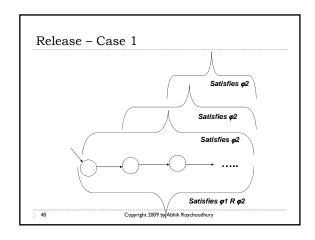


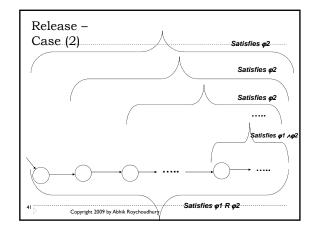


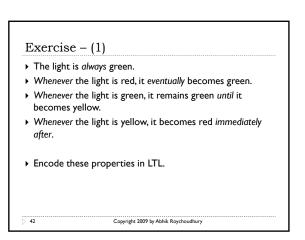












Exercise - (2)

 Check whether the four LTL properties in the previous slide are satisfied by our simple traffic light controller.



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LTL Exercise – (3)

Consider a resource allocation protocol where n processes P_1, \dots, P_n are contending for exclusive access of a shared resource. Access to the shared resource is controlled by an arbiter process. The atomic proposition req, is true only when P_i explicitly sends an access request to the arbiter. The atomic proposition gnt, is true only when the arbiter grants access to P_i . Now suppose that the following LTL formula holds for our resource allocation protocol.

 $\blacktriangleright \ \mathsf{G} \ (\mathsf{req}_i \Rightarrow \mathsf{F} \ \mathsf{gnt}_i)$

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LTL Exercise - (3)

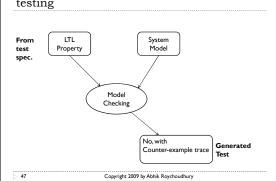
- ▶ Explain in English what the property means.
- ▶ Is this a desirable property of the protocol ?
- Suppose that the resource allocation protocol has a distributed implementation so that each process is implemented in a different site. Does the LTL property affect the communication overheads among the processes in any way?

45

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Model Checking System Model Checking OR No, with Counter-example trace

Recap: Model Checking for model-based testing



Encoding test specifications

- Def I
- A trace σ satisfies a test specification M if σ contains at least one linearization of M as a **contiguous** subsequence.
- ▶ Given MSC M,
- ▶ define Lin(M) = set of linearizations of M.
- ► For each linearization $\sigma = e_1, e_2, ..., e_k$ define

 □ Define $\text{prop}_{\sigma} = \mathbf{F}(e_1 \wedge \mathbf{X}(e_2 \wedge \mathbf{X}(... \mathbf{X}(e_k)...)))$
- Define property φ_M corresponding to M as
 □ φ_M = ¬ (∨_{σ∈Lin(M)} prop_σ)
- \blacktriangleright A counter-example to ϕ_M is a test satisfying M.

> 48

Encoding test specifications Def. 2 A trace σ satisfies a test specification M if σ contains at least one linearization of M as a subsequence. Given MSC M, define Lin(M) = set of linearizations of M. For each linearization σ = e₁,e₂,...,e_k define □ σ = (e₁ ∨ e₂ ∨ ... ∨ e_k) □ prop_σ = (n_σ U (e₁ ∧ X(n_σ U(e₂ ∧ X(... X(n_σ U ek)...)))) Define property φ_m corresponding to M as □ φ_m = ¬(∨ α_e Lan(m) prop_σ) A counter-example to φ_M is a test satisfying M.

