

Scholastic Achievements

- Secured an All India Rank of **2319** in JEE **Advanced** out of **0.22** million aspirants [**2017**]
- Secured All India **99.90** percentile in JEE **Mains** among **1.5** million aspirants [2017]
- Scored **94.26** percentage in Central Board of Secondary Education 12th Standard [2016]
- Class Topper in Maths, Physics, Chemistry and CS in **AISSC Examination [2016]**
- Secured a **State Rank of 30** and **All India Rank of 2666** in **NSTSE [2011]**
- Certified by **INFO-TEC** for proficiency on Windows XP/7, MS Dos, Assembling, Installing and Trouble-shooting Desktop PC's

Positions of Responsibility

Coordinator | Hospitality and Public Relations | Mood Indigo 2018, IIT Bombay [Aug'17 - Jan'18]

Worked as a part of Asia's largest college cultural festival with 1,41,000+ footfall

- Contacted students of other Colleges to appoint them as their CR for events
- Managed hospitality demands of 1,40,000+ visitors, artists, judges, stars etc. working in a team of 40 coordinators
- Coordinated accommodation arrangements through Ferrous portal after discussing the accommodation needs of each college through its college representatives.
- Managed accommodation arrangements in Hostel-6, from taking care of mattress requirements to providing the most efficient but comfortable room structure

Internships/Technical Projects

First Place | XLR8 | Team Leader [2018-19]

Largest remote controlled bot building competition using IC's and electronic circuits

- Build a mobile app controlled bot from scratch using IC's, Resistors, wires, Plastic Boards Rubber tyres and many other electrical components
- Implemented class room learning and came up with innovative ideas like increasing friction between tyres and floor by sticking rubber bands around tyres

Modulo-8 Ripple Counter | Course Project | Digital Electronics [Nov'18]

Guiding Professor: Joseph John, Department of Electrical Engineering, IIT Bombay

- Successfully built a Modulo-8 asynchronous counter using D flip flops
- Used positive edge triggered D flip flops so that count increases at each positive edge of the clock signal

Three Point Bend Test | Course Project | Solid Mechanics[April'19]

Guiding Professor: Parag Tandaiya, Department of Mechanical engineering, IIT Bombay

- Operated UTM Machine and associated apparatus to determine modulus of elasticity of specimen through three point bend test
- Evaluated deflection under bending w.r.t load increments for different geometries

Extra Curricular Activities

- Part of team of 30 students selected for **NSO Football** from entire first year Btech Batch 2017-18
- Played for Hostel-6 in the Annual Interhostel Football GC
- Represented Kerala state in National Speedball Championship and secured **3rd** place in Doubles event
- Passionate about travelling to new places, meeting new people and going for long drives