

Operating System

→ In case the program execution (.java file) is done by OS compiler is just addition to OS.

→ But in JAVA Execution is done by JVM and hence JAVA is platform independent.
 → User point of view: operating system

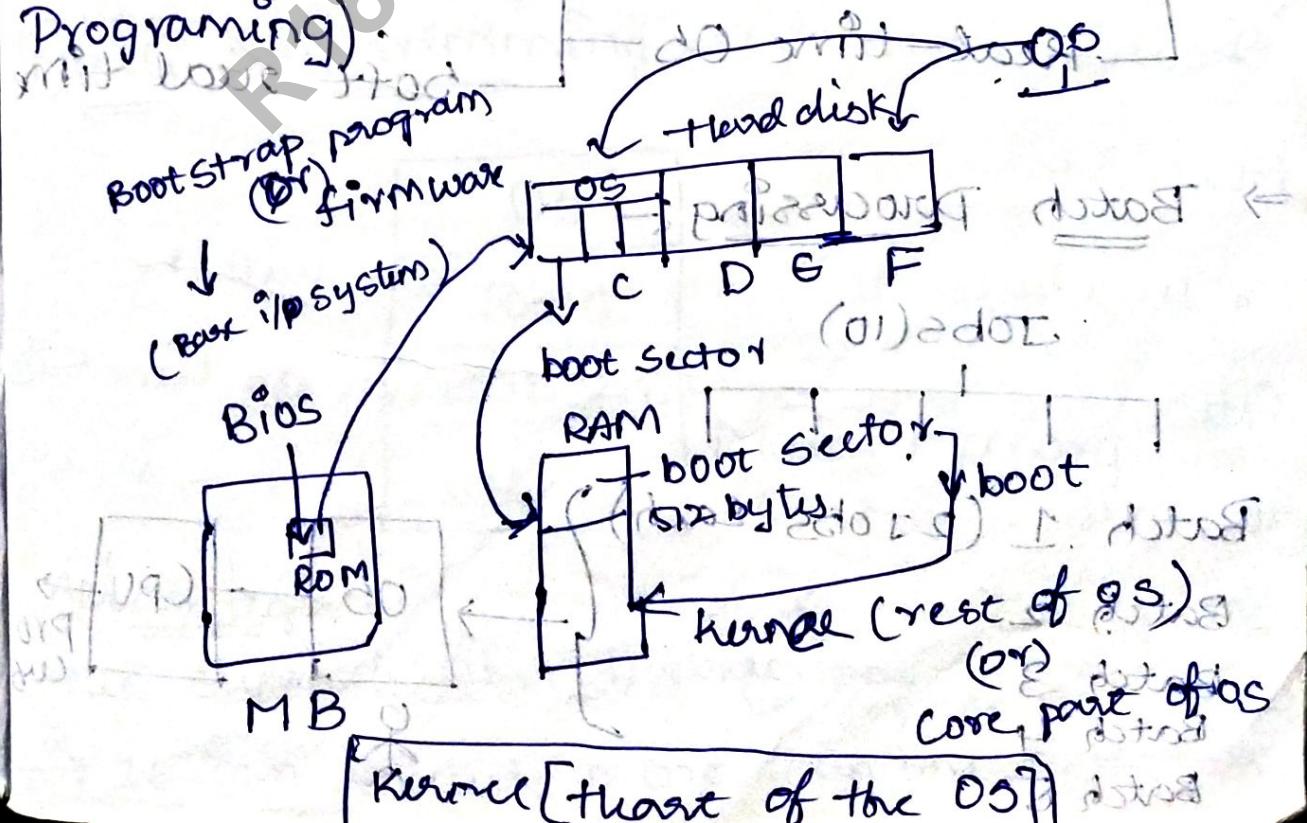
→ It is a system software that acts as a interface between user and computer.
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→ System point of view: system can be defined as resource allocator (or) resource manager.

→ The OS can be defined as allocator (or) resource manager.

→ A resource can be (a hardware component or it can be software component (Application or it can be Software component (Application)).

Programming).

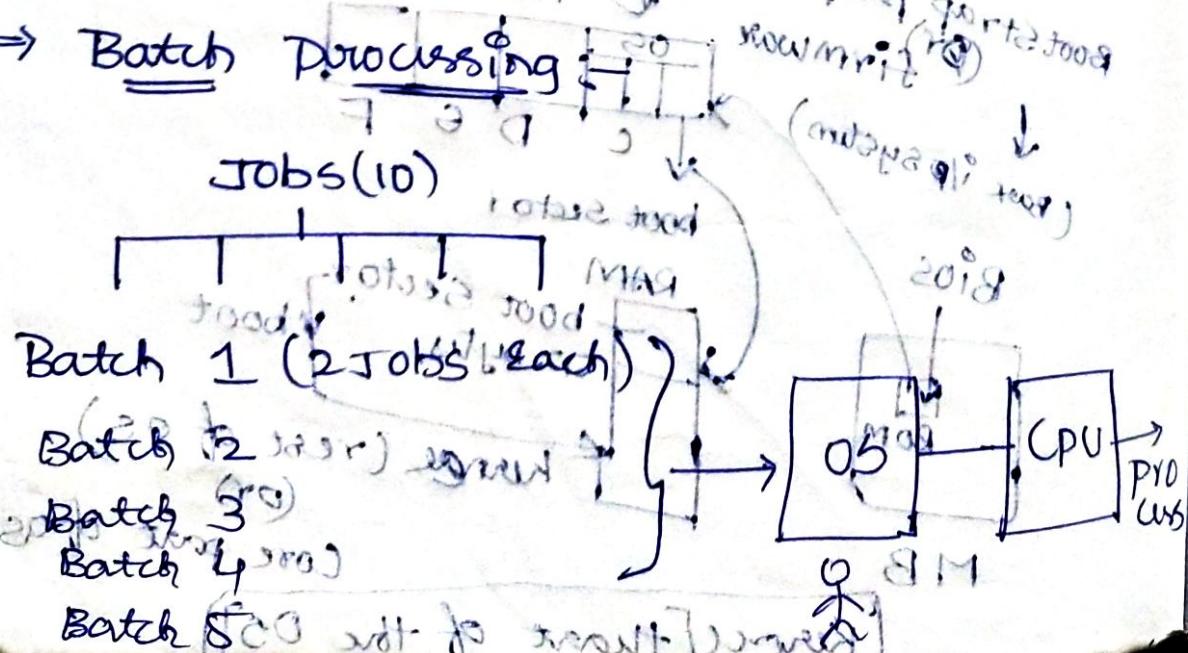


OS functioning
 objective, or Goal or core functionality of
 convenience → easy to use, easy to understand, simple
 efficiency
 ability to evolve
 protection and security

TYPES of Operating System:

- Batch processing
- Multi programming
- Multi tasking
- Multi processing
- Distributed
- Network
- Real-time OS
 - Hard real-time
 - (primarily for aerospace & military)
 - Soft real time

→ Batch Processing



Batch processing :-

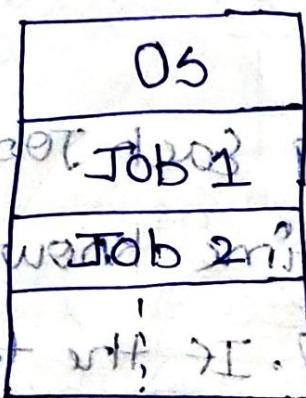
→ Jobs are divided into batches. The jobs in each batch will have a similarity.
 Ex:- Payroll systems, IBM's MVS OS.

DisAdvantages :-

- NO user interaction in Batch processing
- with CPU.
- only one job will be executed once.
- If a batch has a problem next batches will not be Executed.

→ In order to Execute stop that job & we can continue with other jobs; but in some cases order must be followed in order to complete work, which follows sequential steps, entire work will be stopped.

Multi programming :-



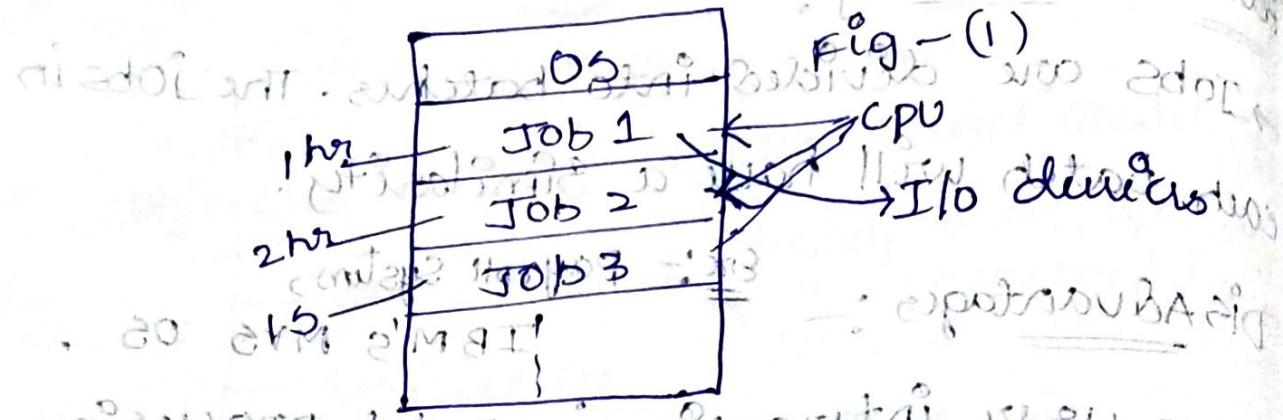
→ program - itum

→ It allows two or more programs to run simultaneously in Main Memory.

Advantages: → User interaction is enhanced.

→ It supports user interaction.

→ It can execute more than one program.



preceding jobs in sequence run on

→ For eg :-

if I/O operation of Job 1 is performing we can assign CPU to Job 2 instead of keeping CPU idle.

Disadvantages :-

- If I/O operation of Job 1 is performing then Job 2 can be executed. (or) Process time is less but the bias is to wait for more time until Job 1 and Job 2 gets processed (starvation happens in last person should be waiting for longer duration).

Multi-tasking :-

→ In multi-tasking each job will be allocated certain time known as time slice for getting executed. If the task is not completed in given time slice, it has to wait till it gets its time slice. Moreover, the next from stack and etc.

→ If we consider above eg: job 3 has to wait. Only for idle time condition so now if to avoid starvation and CPU idle time → go starvation. However, if CPU is always busy, it is decreased.

→ User interaction is allowed.

Multi-processing :-

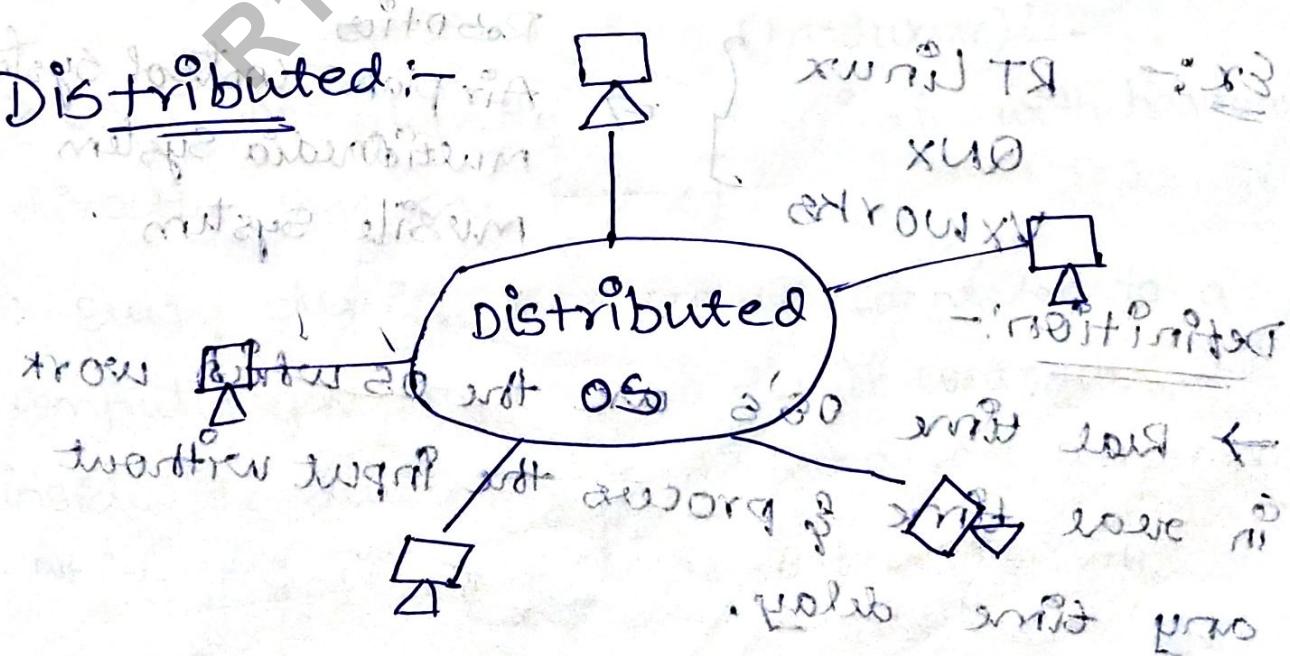
→ It enables the concept of concurrent execution of more number of processes at a time.

→ It will have more number of processors.

For eg:- It performs Job 1 and Job 2 and so on at a time.

→ Processes are performed in parallel at a time.

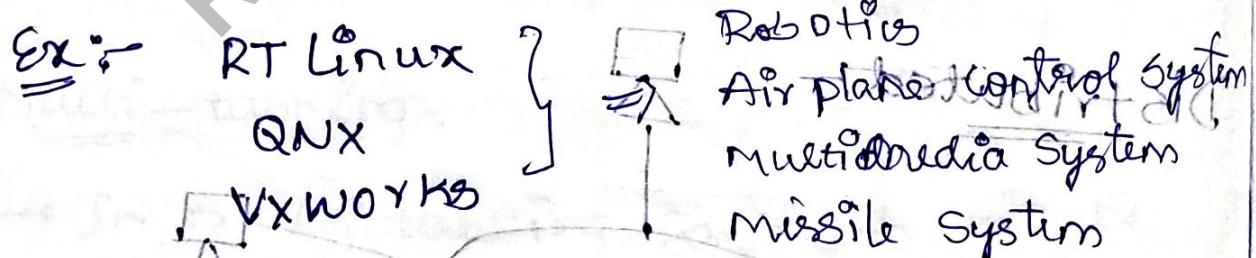
Distributed:



- Entire OS will not be in client machine it will be working over the network.
- The client will have module (or) layer of OS which can communicate with Network.

Eg:- OS's of Indian Railways

- Network OS to respond with actions according to incoming requests from user like file I/O, memory allocation etc.
- The Network OS generally resides in the Server Computer, that serves the number of clients.
- There are 2 types of Real-time OS:
 - Hard real-time
 - soft real-time



Definition:-

- Real time OS's are the OS which work in real time & process the input without any time delay.

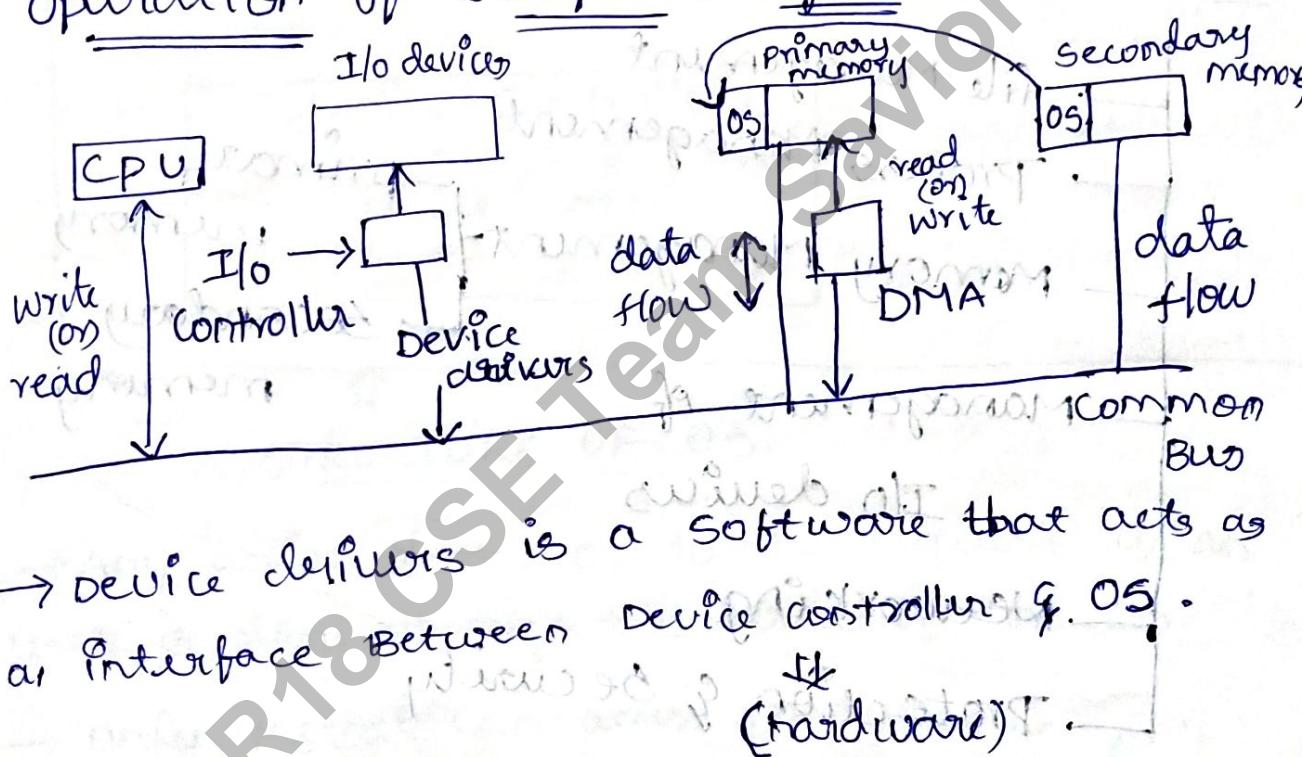
→ If within a deadline if systems could not deliver output it creates a disastrous consequences.

→ soft real time :-

→ It is the OS in which the time delay may be accepted to certain extent.

Ex:- Banking system

operation of computer system

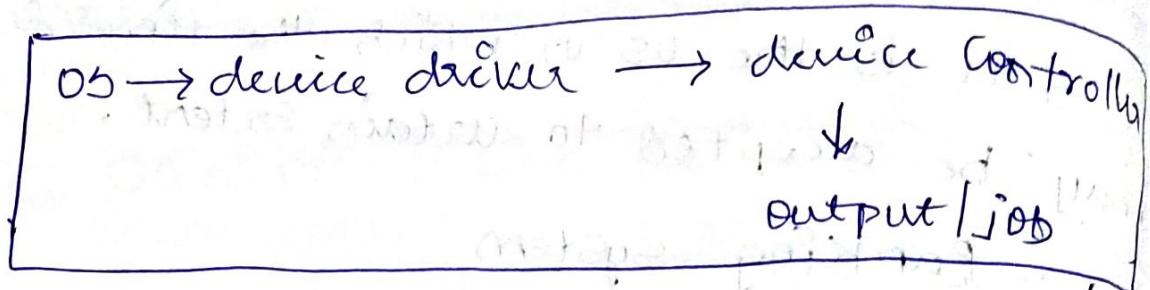


→ Device drivers is a software that acts as an interface between device controllers & OS.

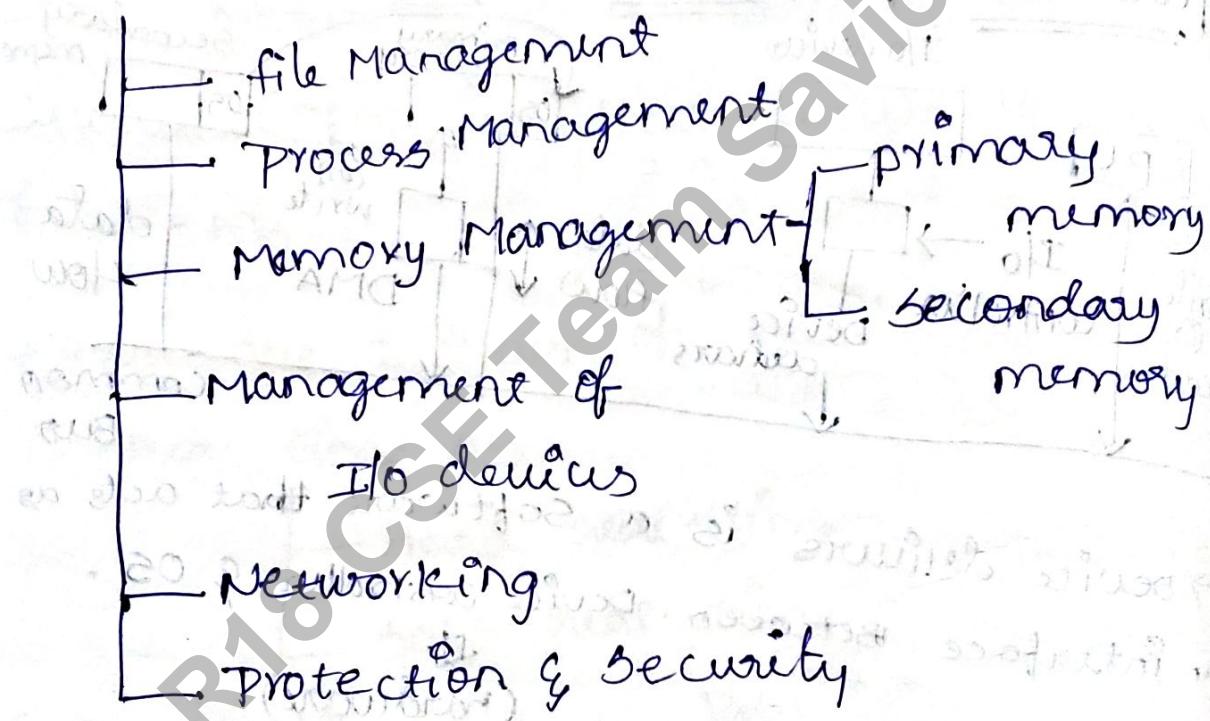
→ OS cannot directly communicate with hardware directly that can connect..

→ Every device that can be connected to a computer generally have a device controller, it is inside the device, it is a hardware component which has its own local buffer (its own memory) connected to main memory

→ The specialised software which is designed to perform a specific operation are called drivers.



OS Services:-



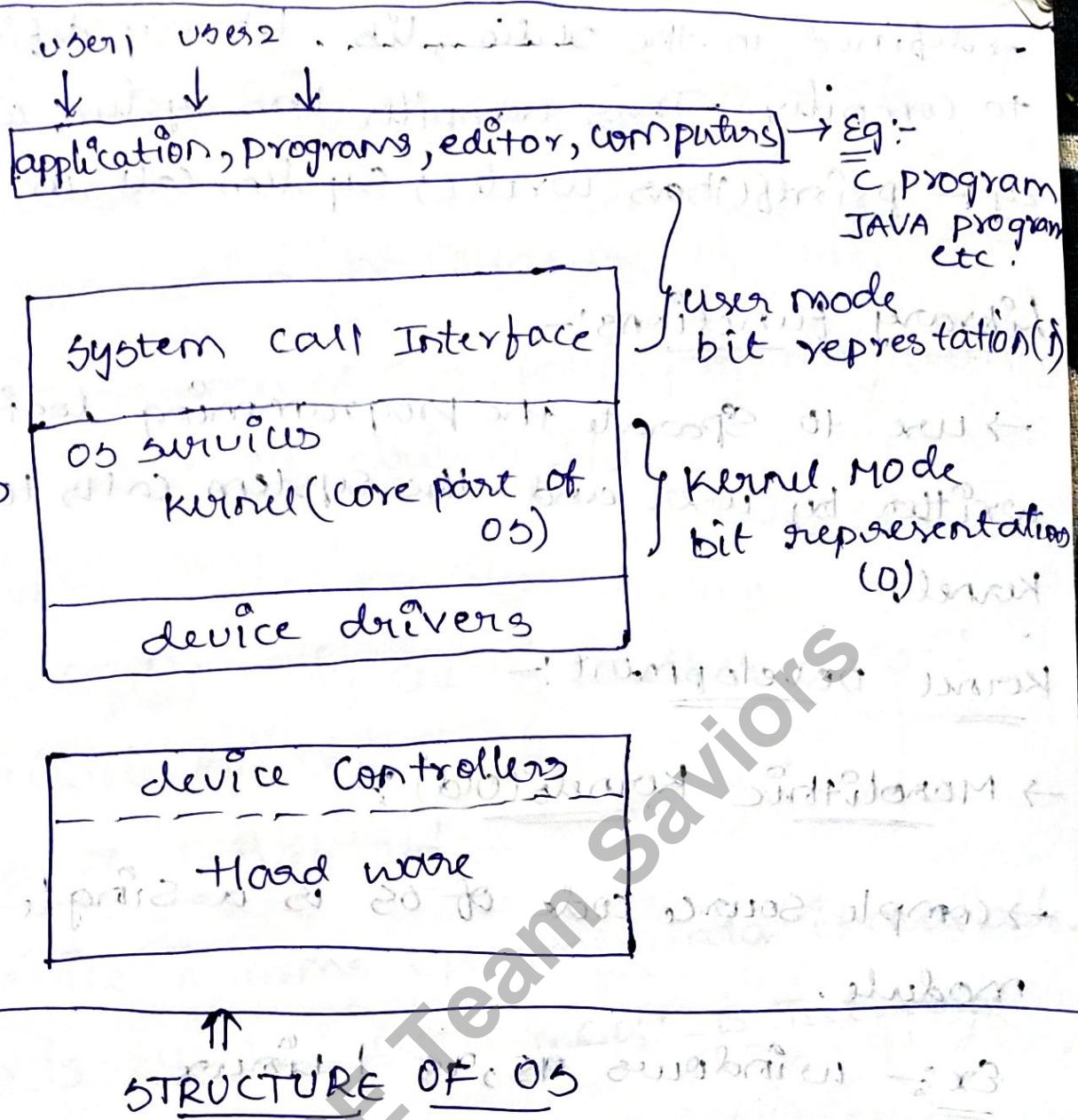
1) interrupts

2) system calls

3) kernel (mode of operation)

operating systems structure

⇒ If anything is loaded into primary memory then it becomes process.



→ Any action of the OS to the request of an user is done through the kernel of the OS.

→ Only kernel can access hardware

API :- Application programming level interface

→ These are interface with some system calls. These system calls are called in the compiler.

• Eq:- `write()` is a system call

`printf()` by user calls the printf definition

→ defined in the studio, lib library defined
to compiler. This compiler has systems call
Ex - printf() has write() system call in it.

Library functions:-

→ use to separate the programming logic
written by user and the system calls to
kernel.

Kernel development:-

→ Monolithic kernels (old)

→ complex source code of OS is a single
module.

Ex - Windows 95, 98, ME, Linux

→ Micro kernels (new)

Ex - Advanced windows, QNX

→ Each service of OS is a module.

→ kernel only, controls the modules.

Interrupts :-

→ Interrupt informs CPU that data is available.

I/O operation (on request of CPU)

↳ programmed I/O, polling or busy waiting

↳ interrupt driven I/O

Polling :-

→ CPU continuously checks whether data is available (or) not.

Interrupt driven I/O :-

→ Device informs CPU that data is available

→ OS is event driven & CPU is interrupt driven

Interrupts :-

→ It is an event generated by the a resource

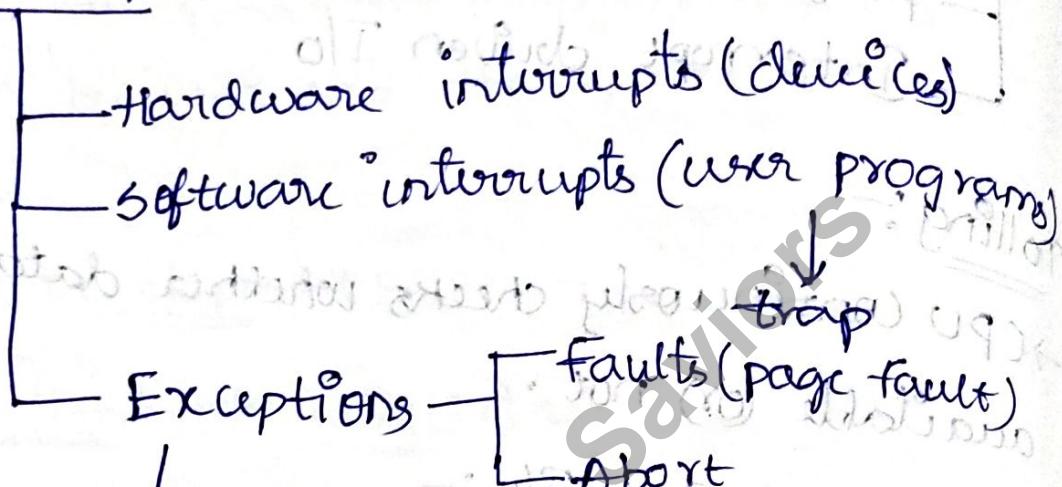
(can be hardware (or) software) that
interrupts the CPU to get the attention of
CPU towards it.

→ When Interrupt happens the context of
the current job scheduled on CPU is saved
then CPU is switched to the Interrupt
handler which processes the interrupt

Service routine (part of OS) is invoked.

→ Later it resumes the old job (or) if may execute new job based on the type of the interrupt (or) job.

Interrupts



→ Exceptions are interrupts that occurs within CPU.

→ All the system calls are software interrupts.

Memory Mapped I/O

→ The device buffers are stored in RAM, so we require only one bus.

System calls

→ It provides the functionality of OS to the user applications.

- It acts as a interface b/w application program and Kernel.
- It seems like a function call but when it is called the state of the system is changed from user mode to kernel mode.
- When system call is invoked a piece of code gets executed by interrupting the CPU, through interrupt service routine.
- User function call and its definition may be relocated in the memory but system calls & its definition is at the same location even if it is called multiple ~~multiple~~ locations ~~times~~ & at different intervals of time
- OS supports hundreds of system calls.

System calls in UNIX :-

- Process Management
(fork(), wait(), exit())
- file Management
(open(), read(), write(), close())
- device Management
(read(), write())
- Information Maintenance
(getpid(), alarm(), sleep())

→ Communication
(pipe(), stringt())

→ security

(chmod())

OS Services [continued]

→ Accounting

→ Error detection

→ resource allocation

→ memory management

→ file management

→ process management

→ device management

→ system calls

→ interrupt handling

→ memory management

→ file management

→ process management

→ device management

→ system calls

→ memory management

→ file management

→ process management

→ device management

→ system calls

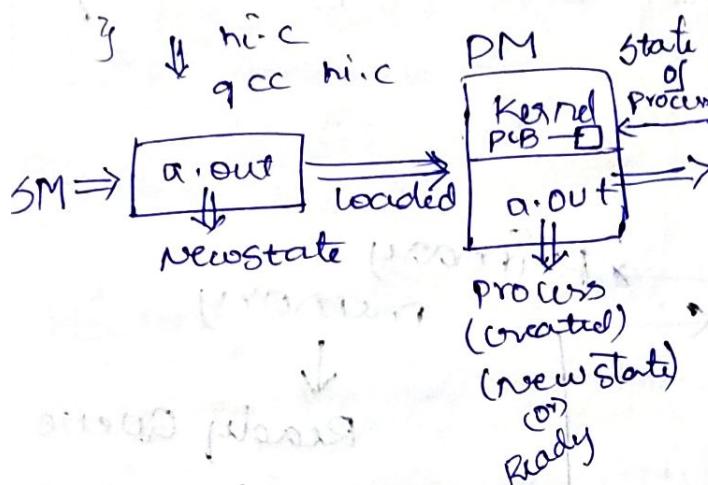
→ memory management

→ file management

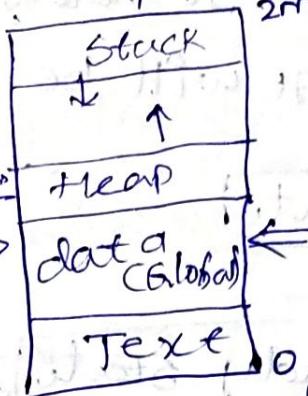
* PROCESS MANAGEMENT :-

Program

```
void main()
{
    printf("welcome");
}
```



Memory MAP OF Process



→ It notes the state of block
PCB → Process Control Block

→ Block Process : It maintains in kernel address block

→ program under execution

→ It is an active entity

→ life span is longer

→ It is on secondary storage.

→ It won't use many resources.

→ lifespan is shorter

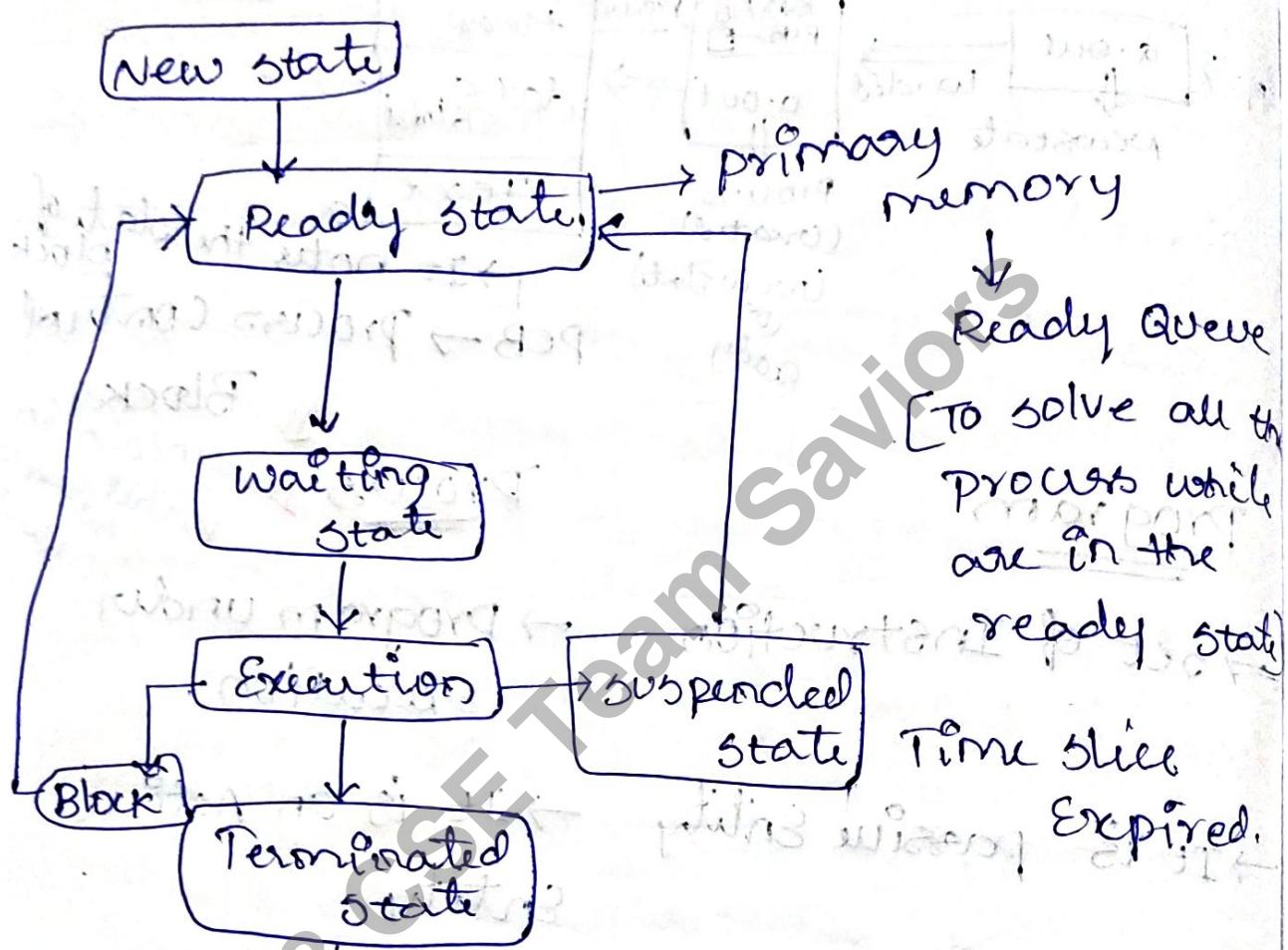
→ It is in primary memory.

→ It uses many resources & DS.

Process State :-

New state $\xrightarrow{\text{a.out}} \text{secondary memory}$

Job pool :- It holds all the just started processes. It will be in secondary memory.



New state :-

→ The process just now created.

Ready state :-

→ The processes which are ready to get executed.

Waiting State

→ The processes which are in the ready queue & waiting for the attention of CPU.

Executing State

→ The process which is getting Executed.

Blocked State

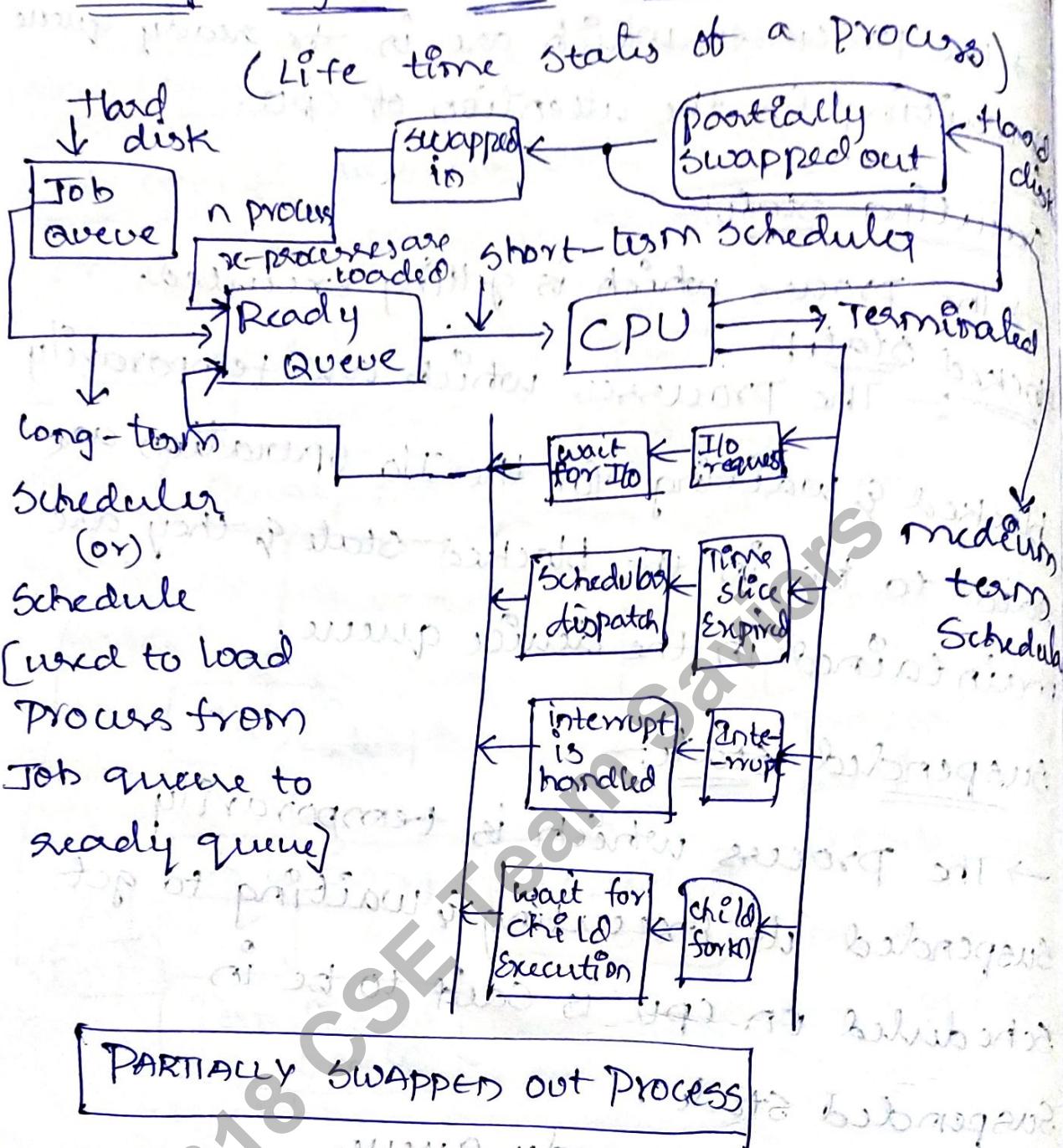
→ The processes which are temporarily blocked & waiting for the I/O operation are said to be in the blocked state & they are maintained in the device queue.

Suspended State

→ The process which is temporarily suspended its Executing & waiting to get scheduled on CPU is said to be in Suspended State.

→ There are two types of suspended states:
1) Blocked by I/O Device
2) Blocked by another process

Queuing diagram of a process



→ The maintenance and management of a process is done through a special data structure allocated in kernel address space by the OS called as PCB (Process Control Block).

→ PCB (Process Control Block) :-

Pid (Number)
Process State
Process Priority (Number)
CPU Registers
Memory limits
CPU Scheduling time information

The state may be new, ready, wait etc

The registers vary in number, size etc depending on architecture

This info includes process priority, pointers to scheduling queues & any other parameters

Process Management:

- Process creation
- Process execution
- Process scheduling (FIFO, SJF, priority based, CPU scheduling Round Robin, LRU)
- Process synchronization (Software based, Hardware based, preemptive non pre-emptive, semaphores, locks, monitors)
- Inter Process Communication
 - message passing, shared memory, pipes (named pipes, unnamed pipes)
- Process terminating

→ init() :-

- It is the first process getting executed by kernel after OS is booted.
- init() is used for initialising drivers.
 - init() process id is 1.
 - init() process id for identifying a process.
 - OS uses process id for identifying a process.
 - init() either directly (or) indirectly will be parent for all processes.
 - (background, daemon process)

init() → process id is 1

directly (or) indirectly

parent of
all processes

init()

directly (or) indirectly

parent for all
processes

Creation of process : fork()

→ fork() is used to create new process.

Program:-

```
#include <stdio.h>
#include <sys/types.h>
#include <stdlib.h>
#include <unistd.h>

int main()
{
    fork(); // main() (parent) - P Process
    printf("Hi");
    return 0;
}
```

↓
Child - Child process

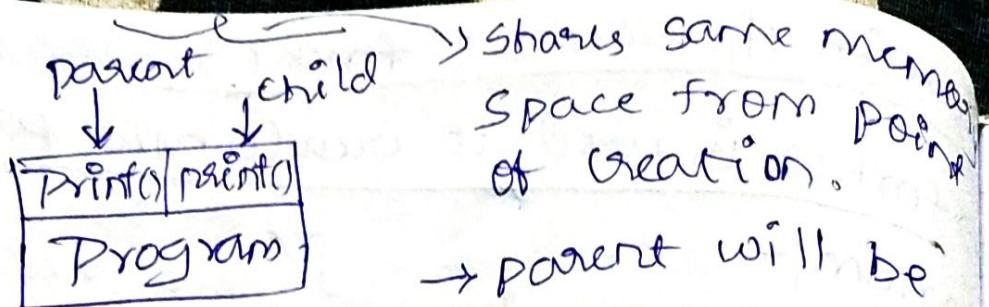
Output:-

Hi

Hi

→ fork() is a system used to create a child process.

→ The child and parent processes both share the same memory space from the point of creation of child process.



→ parent will be executed & once, currently child will be executed.

→ fork() will return an integer value, if fork() is failed it returns negative value (-1), on success it returns two values, for child process it returns zero, for parent it returns the process id of child.

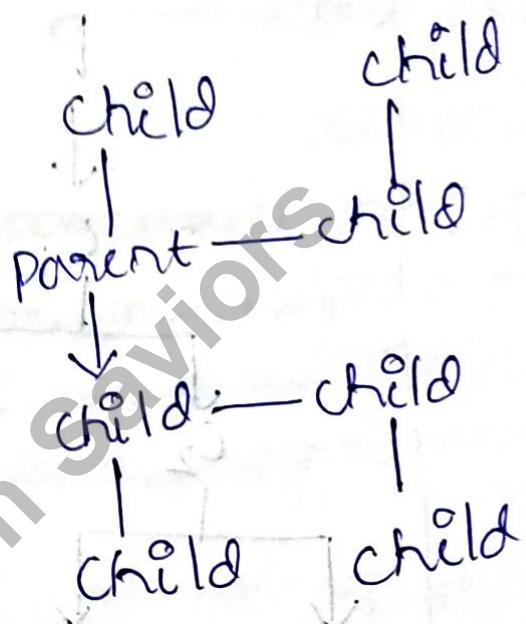
Program :-

```
#include <stdio.h>
#include <sys/types.h>
#include <stdlib.h>
#include <unistd.h>
int main()
{
    printf("The process is %d, Kernel id is %d,\n"
           "getpid(), getppid());\n"
           "It may vary at different times\n"
           "It is constant");
    return 0;
}
```

Program

```
#include <stdio.h>
#include <sys/types.h>
#include <stdlib.h>
#include <unistd.h>

int main()
{
    fork();
    fork();
    fork();
    printf("hi");
    return 0;
}
```



Output

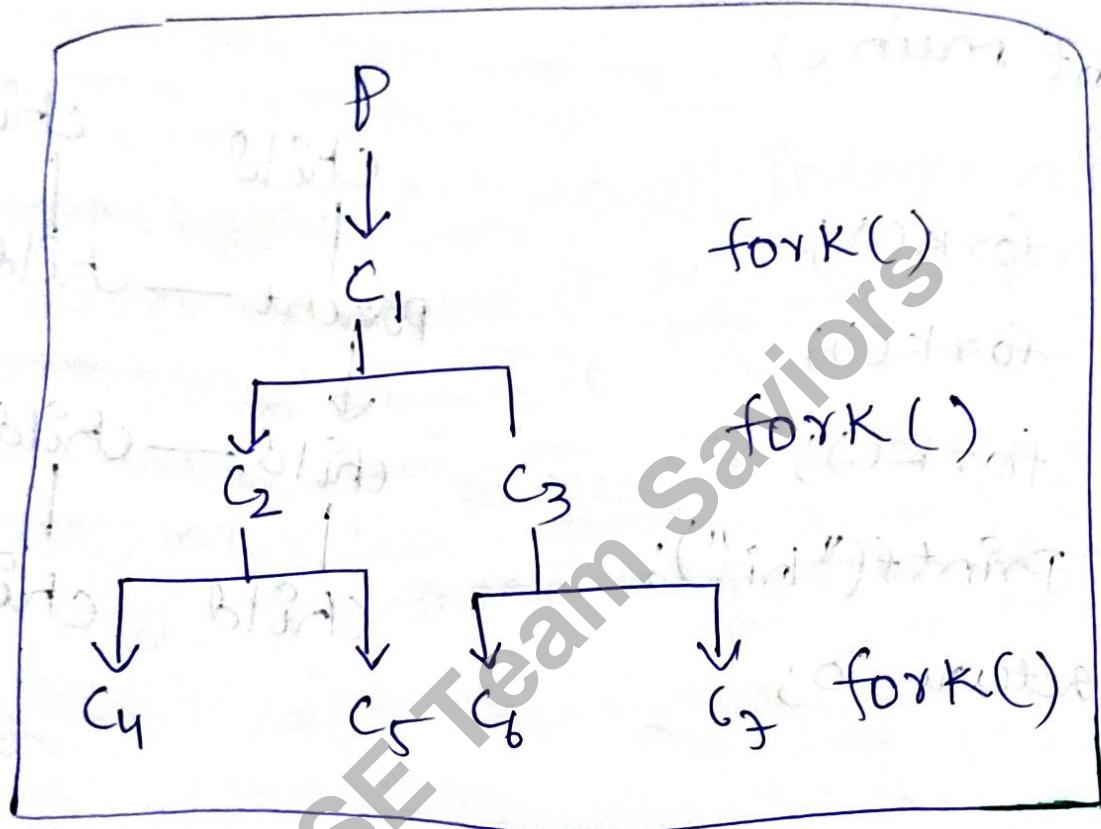
```
hi
hi
hi
hi
hi
hi
hi
hi
```

$\Rightarrow \text{for } (i=0; i < n; i++)$

`fork();`

\therefore There will be

2^n processes and
 $2^n - 1$ child process



Program

```
#include <stdio.h>
```

```
#include <sys/types.h>
```

```
#include <stdlib.h>
```

`pid_t`

```
#include <unistd.h>
```

```
int main()
```

{

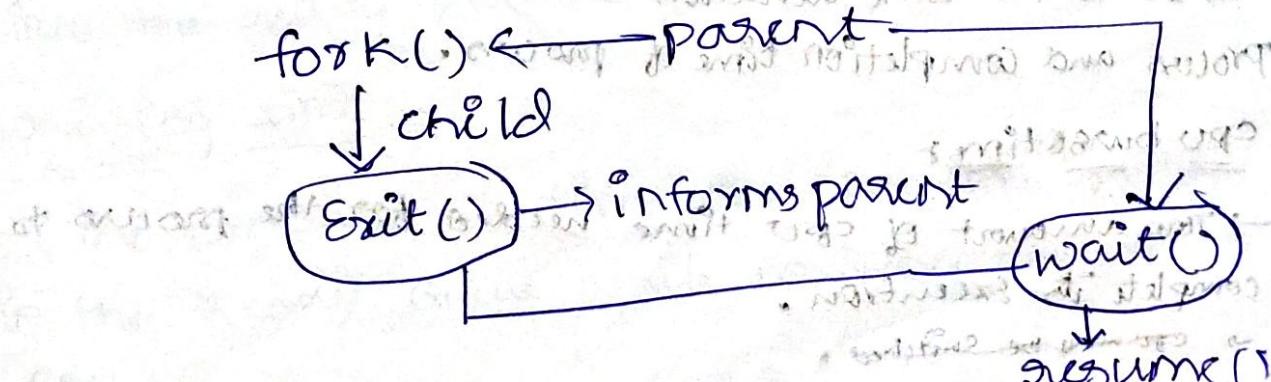
`Pid_t Pid;`

```

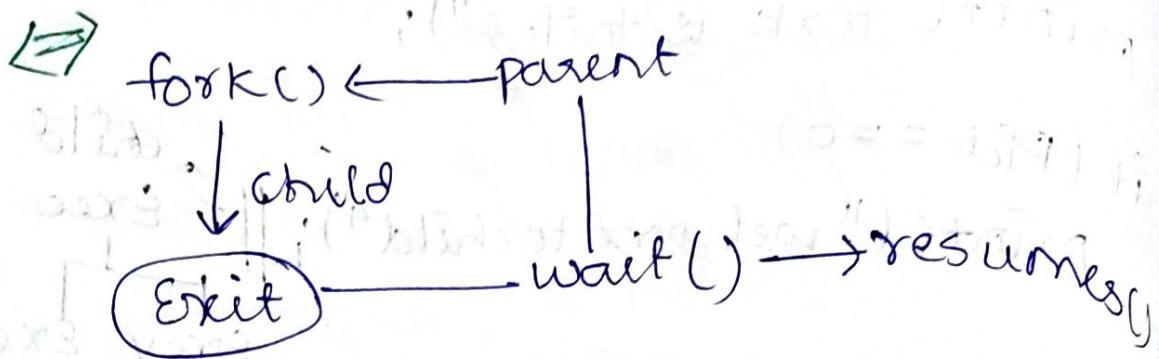
Pid = fork();
if (Pid < 0)
    printf("fork is failed");
if (Pid == 0)
    printf("welcome to child"); // child
else
{
    printf("welcome to parent"); // parent
    printf("if, else both Executed");
}
return 0;
}

→ Both if & else gets printed but
like in multithreading any block if (or)
else may print first.
→ Exec is used to replace the parent
memory space with a new process Executed
by the child.

```



→ When the execution of child thread completed it invokes `exit()`.



→ If no parent is waiting for a child who is under execution, if child completes execution, such process is called as "Zombie Process".

→ If parent is terminated ^{forcefully (abnormal)} but child is in execution, such a child is called "Orphan child.."

Waiting Time :-

process (CPU want release the CPU but executing a process)

→ The amount of time the process spends in the ready queue while waiting for CPU.

Turn Around Time :-

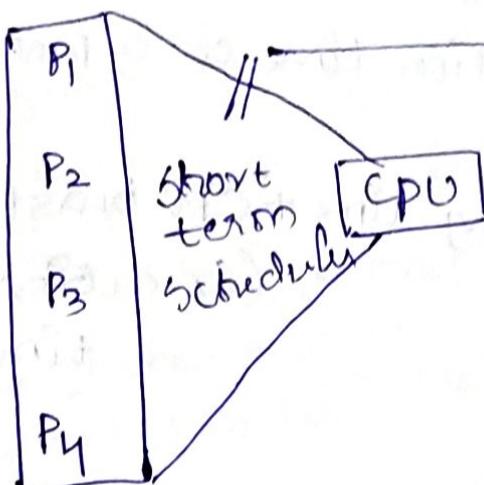
→ It is the time duration b/w the arrival time of a process and completion time of process.

CPU burst time

→ The amount of CPU time needed for the process to complete its execution.

→ CPU may be switched.

CPU scheduling (or) Process Scheduling :-



} F (FO)
Shortlist Job First (SJF)
Priority based
Round Robin
(time slice)

} multilevel Queue
multilevel feedback
queue scheduling.

Objectives of Scheduling:-

- 1) To reduce CPU idle time.
- 2) To improve throughput of the system.
- 3) To improve the multi-programming.
- 4) Utilization of resources.
- 5) To reduce average waiting time, turn around time of a process.

Arrival time :-

→ The time at which the process is arrived into the ready queue.

Waiting time :-

→ The amount of time the process spends in the ready queue while waiting for the CPU.

Turnaround Time :-

→ It is the time duration plus the arrival time of a process & completion time of a process.

Turn around time = waiting time + CPU burst time
 (Execution time)

CPU Burst Time :-

→ The amount of CPU time needed for process to complete its execution.

Eg:- FCF5 (First come First serve),

Q)

<u>Process</u>	CPU burst time	Arrival time
P ₁	27	0
P ₂	3	0
P ₃	3	0

Find avg waiting, turn around time?

NOTE:- If arrival time is not mentioned, all processes are present in ready queue at $t=0$.

CPU Scheduling

preemptive

when CPU is executing a process the CPU may be switched over to another process.

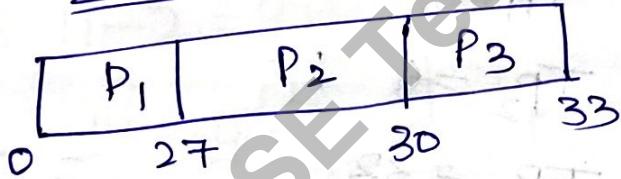
non-preemptive

If CPU is allocated to a process, the process won't release the CPU till it gets completed.

→ The FCFS is a non-preemptive Scheduling.

Ans: for previous question

Gantt chart



Waiting time

$$WP_1 = 0$$

$$WP_2 = 27$$

$$WP_3 = 30$$

Turn around time

$$TP_1 = 27$$

$$TP_2 = 30$$

$$TP_3 = 33$$

$$\frac{(0+27+30)}{3}$$

Average waiting time of a process =

$$= 19 \text{ ms}$$

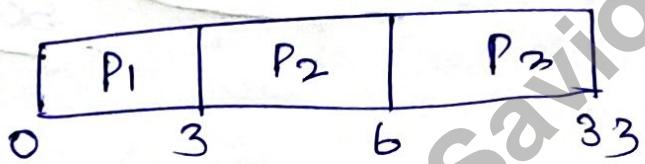
Average turnaround time of a process

$$= \frac{(27+30+33)}{3} = 30 \text{ ms}$$

$\Rightarrow FCF5$

Process	CPU burst time (ms)	Arrival time
P ₁	3	0
P ₂	3	0
P ₃	27	0

Ans^r Gantt chart



Waiting time

$$WP_1 = 0$$

$$WP_2 = 3$$

$$WP_3 = 6$$

Turn around time

$$TP_1 = 3$$

$$TP_2 = 6$$

$$TP_3 = 33$$

Average waiting time of a process

$$= \frac{(0+3+6)}{3}$$

$$= 3 \text{ ms}$$

Average turnaround time of a process

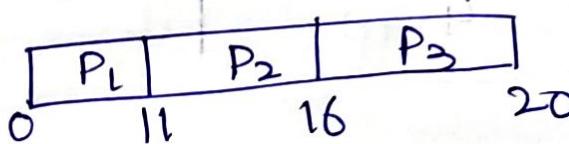
$$= \frac{(3+6+33)}{3}$$

$$= 14 \text{ ms}$$

Process	CPU burst (ms)	Arrival time
P ₁	11	0
P ₂	5	2
P ₃	4	4

Ans:-

Gantt chart



Waiting time

$$WP_1 = 0$$

$$WP_2 = 11 - 2 = 9$$

$$WP_3 = 16 - 4 = 12$$

Turnaround time

$$TP_1 = 0 + 11 = 11$$

$$TP_2 = 9 + 5 = 14$$

$$TP_3 = 12 + 4 = 16$$

Average waiting time of a process:

$$WT = \frac{0+9+12}{3}$$

$$FT = \frac{0+9+12}{3}$$

$$= \frac{(0+9+12)}{3}$$

$$= \frac{31}{3} = 10.33$$

Average Turnaround time of a process

$$= \frac{(11+14+16)}{3}$$

$$= \frac{41}{3} = 13.67$$

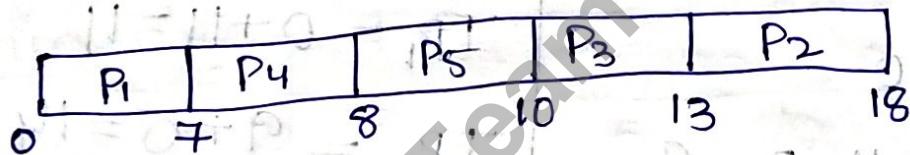
$$= 13.67 \text{ ms}$$

Shortest Job First (SJF) algorithm :-
 → SJF is non-preemptive scheduling.

Process	Arrival time	CPU burst time	
		T. (second)	
P ₁	0	7	
P ₂	1	5	
P ₃	2	3	
P ₄	3	1	
P ₅	4	2	

Anst

Gantt chart



$$WP_1 = 0$$

$$WP_4 = 7 - 3 = 4$$

$$WP_5 = 10 - 4 = 6$$

$$WP_3 = 10 - 2 = 8$$

$$WP_2 = 13 - 12 = 1$$

$$TP_1 = 7$$

$$TP_4 = 10 - 7 = 3$$

$$TP_5 = 13 - 10 = 3$$

$$TP_3 = 13 - 10 = 3$$

$$TP_2 = 17 - 13 = 4$$

$$\rightarrow \text{Average waiting time} = \frac{(0+4+6+8+12)}{5} = 28/5 \text{ seconds}$$

$$\rightarrow \text{Average turnaround time} = \frac{(7+5+6+11+17)}{5} = 46/5 \text{ seconds}$$

\Rightarrow SJF (preemptive) algorithm :- [SJF]

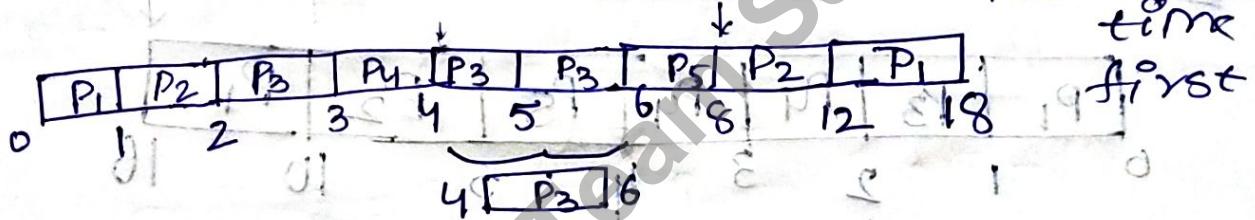
Q) Process	Arrival time	CPU burst time
✓ P ₁	0	8.0
✓ P ₂	1.8	5.40
✓ P ₃	2.1	3.210
✓ P ₄	3.0	1.0
✓ P ₅	4.5	2.0

Ans:-

Gantt chart

SJF

↳ shortest remaining time



→ If any conflict arises then use FCFS.

$$P = 90\%$$

$$WP_1 = 11$$

Waiting time

$$WP_2 = 6$$

= completion time - Execution time

$$WP_3 = 1$$

$$C = 90\%$$

$$WP_4 = 0$$

$$E = 100\%$$

$$WP_5 = 2$$

$$A = 0\%$$

$$TP_1 = 18 \quad TP_2 = 11 \quad TP_3 = 4$$

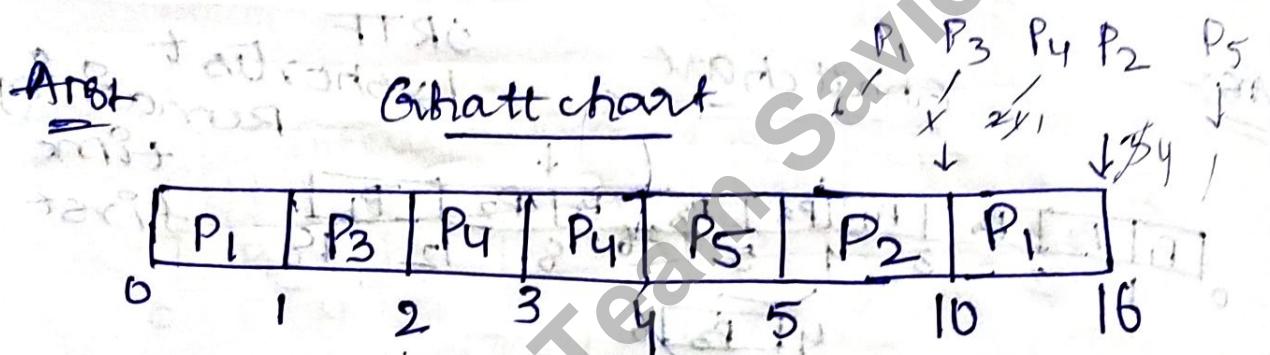
$$TP_4 = 1 \quad TP_5 = 4$$

$$Avg = 4 ms$$

$$Avg = \frac{38}{5} ms$$

Q) SJRTF (preemptive)

Process	Arrival time	CPU burst time
✓ P ₁	0	7
✓ P ₂	3	5
✓ P ₃	1	10
✓ P ₄	2	1
✓ P ₅	7	10



$$WP_1 = 9$$

$$WP_2 = 2$$

$$WP_3 = 0$$

$$WP_4 = 0$$

$$WP_5 = 8T \quad T = 9T \quad 81 - 91$$

$$P = 8T \quad 1 = T$$

$$\text{avg } \frac{38}{2} = PVA$$

$$\text{avg } P = PVA$$

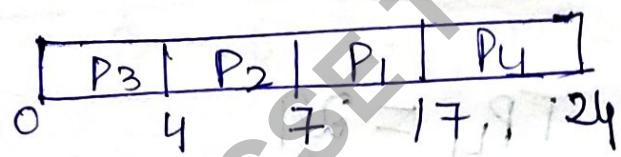
Priority Scheduling: (non-preemptive)

UNIX: (low number → high priority)

Process	Priority	CPU Burst Time
P ₁	3	10
P ₂	2	3
P ₃	1	4
P ₄	4	7

Ans:

Gantt chart



$$WP_3 = 0$$

$$WP_2 = 4$$

$$WP_1 = 7$$

$$WP_4 = 17$$

$$\begin{aligned} Avg &= \frac{28}{4} \text{ ms} \\ &= 7 \text{ ms} \end{aligned}$$

$$TP_3 = 4$$

$$TP_2 = 7$$

$$TP_1 = 17$$

$$TP_{24} = 24$$

$$Avg = 13 \text{ ms}$$

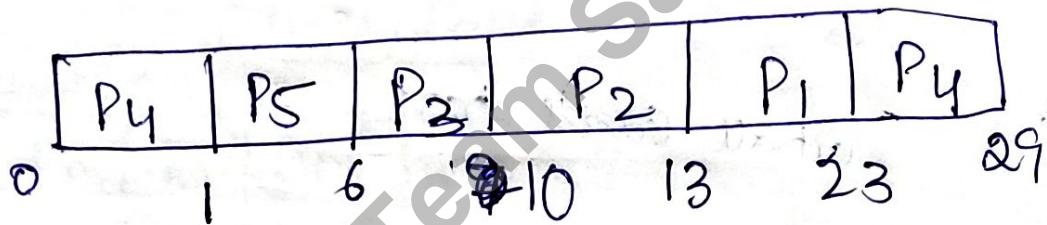
Process	Priority	CPU burst time	Arrival time
P ₁	3	10	1
P ₂	2	3	2
P ₃	1	4	4
P ₄	4	7.6	0
P ₅	0	5	1

→ preemptive

Ans

Gantt chart

P₄ P₅ P₃ P₂



$$WP_4 = \frac{22}{22} = 1$$

$$TP_4 = 29$$

$$WP_5 = 0$$

$$TP_5 = 5$$

$$WP_3 = \frac{8}{2} = 4$$

$$TP_3 = 9.6$$

$$WP_2 = 8$$

$$TP_2 = 11$$

$$WP_1 = 12$$

$$TP_1 = 22$$

$$AVG = \frac{44}{5} ms$$

$$AVG = \frac{73}{5} ms$$

Round Robin Scheduling [time slice = 6ms]

(time quanties)

→ process

CPU burst

time (time slice = 3ms)

(preemptive)

P₁

27 21 18

P₂

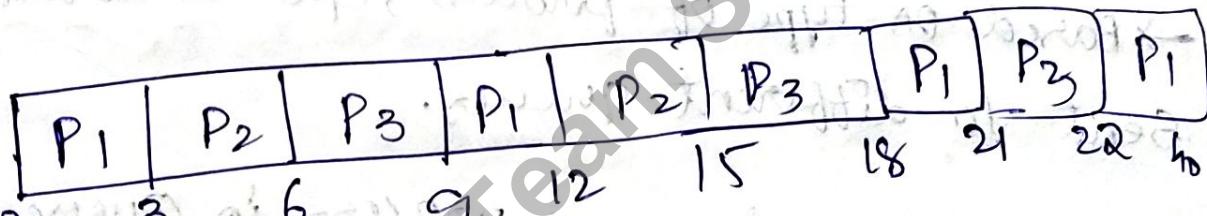
6 3 0

P₃

7 4

Ans

Gantt chart



$$WP_1 = 13$$

$$TP_1 = 40$$

$$TP_2 = 15$$

$$WP_2 = 9$$

$$TP_3 = 22$$

$$WP_3 = 15$$

~~WP~~

$$Avg = \frac{37}{3} ms$$

$$Avg = \frac{77}{3} ms$$

Multilevel Queue Scheduling :-

Priority

high



low

System processes Queue 1

Interactive processes Queue 2

Interactive editing
processes Queue 3

Batch processes " 4

Student processes " 5

→ Based on type of process, CPU scheduling sent to different queues.

→ Generally, first all processes in Queue 1 get finished then it enters into Queue 2.

→ Sometimes process in Queue 1 take long CPU time. To overcome this time slicing is done. (Round robin fashion)

(Exclusive pre-emptive)

e.g. MQfS

Queue 1 (P₁, P₂, P₃) → Time slice 8ms

Queue 2 (P₃) → Time slice 10ms

Queue 3 (P₃) → Time slice 24ms
→ FCFR₃

- If P₃ is not completed in Queue 1, then it moves to Queue 2 and it is Executed.
- At last level Queue n → The algorithm is used is FCFS.

Final answer obtained from wait and turn provided two combined priority program.

Final answer from result given along with

Ranking of processes (independently) according with their previous execution time and of the priority numbers (according to the number of processes).

Priority 1 → P₁ (Execution time = 10 units)

Priority 2 → P₂

Priority 3 → P₃

Execution time = 10 units

Execution time = 10 units

UNIT-II

Process Synchronization

→ A transaction in DBMS is same as process in OS.

→ when there are multiple process simul-taneously getting executed and sharing the common data there may arise data inconsistency problem.

→ To avoid this the processes (dependent processes) communicating are to be synchronization.

→ The process of synchronizing the ~~data~~ ^{processes} is called process synchronization.

next produced

int buffer[size]

P₁ count = 0
in = 0
out = 0

Producer

while (count == bufferSize);
do nothing;

buffer[in] = nextproduced
in = (in + 1) % bufferSize
Count ++;

next consumed

consumer
while (count == 0)
do nothing

next consumed
= buffer[out]

out = (out + 1) % bufferSize

Count --;

count = 5

Time

$T_0 \rightarrow \text{register } 1 = \text{count} (5)$

$T_1 \rightarrow \text{register } 1 = \text{register } 1 + 1 (6)$

$T_2 \rightarrow \text{register } 2 = \text{count} (5)$

$T_3 \rightarrow \text{register } 2 = \text{register } 2 - 1 (4)$

$T_4 \rightarrow \text{count} = \text{register } 1 \rightarrow 6$

$T_5 \rightarrow \text{count} = \text{register } 2 \rightarrow 4$

Producer

count ++

```

register1 = count
register1 = register1 + 1
count = register1
  
```

Consumer

count --

```

register2 = count
register2 = register2 - 1
count = register2
  
```

solutions for process synchronization

software solution (pictor's solution 1981)

hardware synchronization

test and set()

swap()

semaphores (OS supports)

Monitors (user defined)

→ When concurrent processes shares a common data, the outcome depends upon the order of execution of process. It is called as race condition.

→ Critical Section:

→ It is a portion of process source code used to access the shared data between the processes.

Critical section Problem:

→ When one process is in its critical section no other process is allowed to enter into its critical section.

Solution to Critical section Problem:

Mutual Exclusion
Progress

Bounded waiting

Bounded waiting:

→ There exists a finite (or) bounded count before a process can enter into its critical section when other processes are waiting to enter into their critical section.

Software Solution (Peterson Solution)

→ let two concurrent process P_i and P_j

→ let an array $\text{flag}[2] = \{\text{false}, \text{false}\}$

turn can be $P_i(0) < P_j(1)$ or $P_j(1) < P_i(0)$

while $\underline{P_i}$ private \rightarrow This solution works

do {

$\text{flag}[i] = \text{true}$; if $i = j$

turn = j ; else

only 2 processes
in a critical section

critical section

while ($\text{flag}[j] = \text{true}$ & $\text{turn} == j$)

enter into critical section

(wrt) writer

$\text{flag}[i] = \text{false};$

} while (true);

P_j

do {

$\text{flag}[j] = \text{true};$

turn = $i;$

wrt = $[i]\text{post}$

while ($\text{flag}[i] = \text{true}$ & $\text{turn} != j$);

Enter into critical section

$\text{flag}[j] = \text{false};$

} while (true);

wrt = $[i]\text{post}$

(wrt) writer

General section of Critical Section:

do {

entrysection

→ used to check if any one is CS, if exists wait, otherwise enter

Critical section

exitsection

→ notify others that you came out of CS

remainder section;

} while (true);

Po

do {

flag[i] = true;

turn = i;

while (flag[j] == true && turn != j), post

i (i = do nothing);

Count++

flag[i] = false;

} while (true);

; (exit) & cleanup

i9

{ 02 }

{ i = newj }

; (exit) & cleanup

i10

{ 02 }

; (exit) & cleanup

{ 02 }

((ba)8) 28.06.2017

do {

flag[i] = true ;

turn = i ;

while (flag[i] == true && turn == i) ;

do nothing ;

count --

if (j == 0) {

 S1 ;

 S2 ;

} else if (j == 1) {

 S3 ;

} else if (j == 2) {

 S4 ;

} else if (j == 3) {

 S5 ;

} else if (j == 4) {

 S6 ;

} else if (j == 5) {

 S7 ;

} else if (j == 6) {

 S8 ;

} else if (j == 7) {

 S9 ;

} else if (j == 8) {

 S10 ;

} else if (j == 9) {

 S11 ;

} else if (j == 10) {

 S12 ;

} else if (j == 11) {

 S13 ;

} else if (j == 12) {

 S14 ;

} else if (j == 13) {

 S15 ;

} else if (j == 14) {

 S16 ;

} else if (j == 15) {

 S17 ;

} else if (j == 16) {

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} else if (j == 102) {

 S99 ;

} else if (j == 103) {

 S99 ;

} else if (j == 104) {

 S99 ;

} else if (j == 105) {

 S99 ;

} else if (j == 106) {

 S99 ;

} else if (j == 107) {

 S99 ;</

```
while (Test and Set(&lock));
```



```
boolean TestandSet(*target) {
```

```
i (boolean *temp) = [i]psh;
```

```
temp = *target; // bottom of
```

```
*target = true;
```

```
return temp;
```

```
}
```

→ Processor needs to find memory reservation

(processor) → Processor organization

bottom up test

i ((KASLR) + 2 base + offset) aligned

position will

be =

Swap :-

P1

lock = false

key = true

do {

 if (lock == false) {

 if (key == true)

 {

 swap(&key, &lock);

}

CS

lock = false;

 } while (true);

 the Deceleration of
 lock & key

 void swap(boolean *a,
 boolean *b)

 {

 boolean *t;

 *t = *a;

 *a = *b;

 *b = *t;

 }

 Description about the source file

 1 pair of variables with the same type

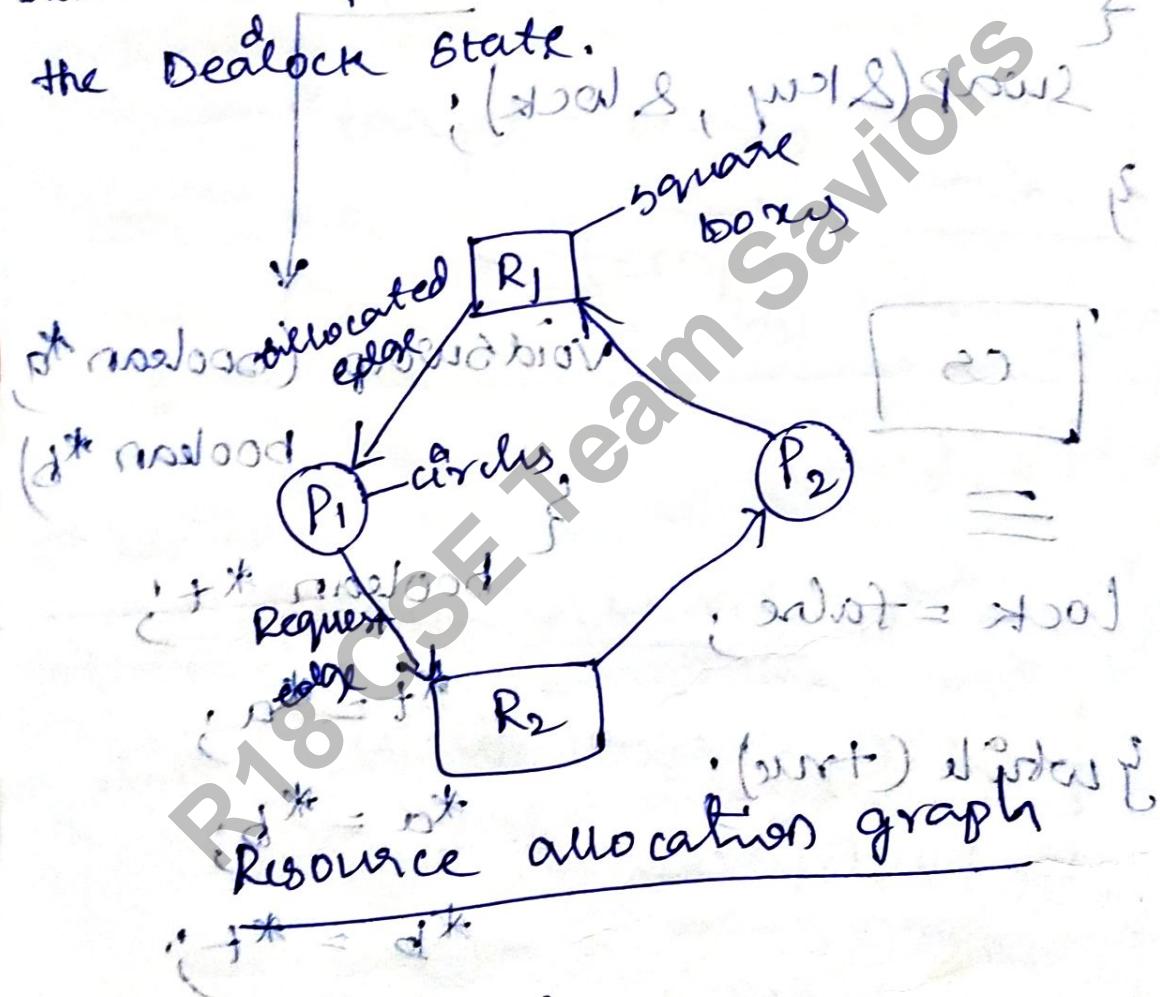
 both are shared with the same type

 so instead of passing a variable with file

 so instead of passing a variable with file

Deadlocks :-

→ when there are set of processes executing concurrently competing for the resources such that a process P_i holding the resource R_j and requesting a resource R_j which is held by the process P_j and process P_j requesting resource R_i ; thus Process P_i, P_j may be in the Deadlock State.



NOTE:-

- If resource allocation graph contains a cycle and if the resources are single instance resource then there is a deadlock.
- If the resources is multiple instance resource then there may be deadlock.

uses of Resource :-

① request

→ process of requesting the resource.

② wait

→ wait resource until resource becomes free.

③ Release

→ process of releasing the resource.

Necessary conditions for occurrence of deadlock

1) Mutual Exclusion

2) Hold and Wait

3) No Preemption

4) Circular Wait

Mutual Exclusion :-

→ when one process is using a resource it should not be allocated to other process (non sharable resources).

2) Hold and Wait :-

→ One process is holding a resource and waiting for a resource which is held by other process.

other process

process

mutually

wait

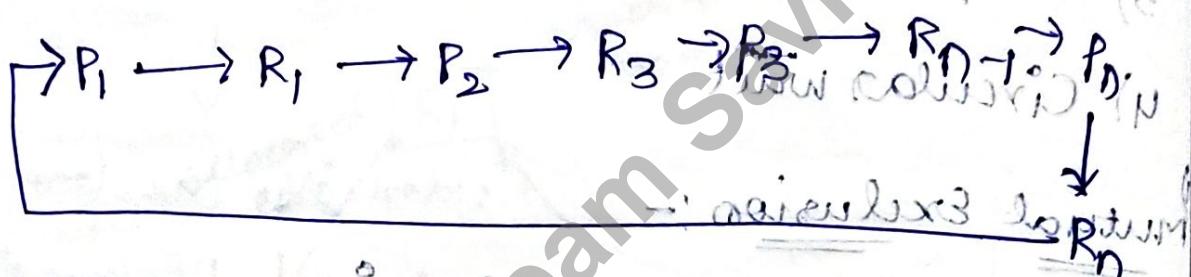
process

mutually

3) No preemption:-

→ If a process is using a resource it won't release the resource till its completion. (revocation of a resource is not possible, it should be done voluntarily by process)

4) Circular Wait:-



If a process is assigned more resources than required

Deadlock → Deadlock detection and recovery

↳ Deadlock prevention (deadlock avoidance)

↳ Deadlock avoidance

↳ Deadlock detection and Recovery

↳ Deadlock avoidance

Deadlock Avoidance → Bankers Algorithm

↳ Resource request, allocation algorithm

↳ Safe state detection algorithm

i) Deadlock prevention:-

- prevent the occurrence of any one of necessary condition.
- i) let the sharable resources be shared.
- ii) a process is not allowed to request a resource if it is already holding a resource.
(or)
- If a process request a resource first let the process release the resource it is holding.
- iii) If a process request a resource, if it is not given immediately then the process is supposed to voluntarily release the resources held by it.

iv)

$f(\text{tape driver}) = 1$
$f(\text{disk drivers}) = 5$
$f(\text{printer}) = 12$

$$\rightarrow F: R \rightarrow N$$

$$f(R_j) > f(R_i)$$

i.e. first lesser resources needed process should be allocated.

Deadlock Avoidance :- (Banker's Algorithm)

Safe state :-

→ The system is said to be in Safe State if

- Such that the system can distribute the resources and the processes in the system so as to finish their executions in time.

→ If system is in safe state then deadlock does not occur. If it is in unsafe state then there may be a chance of deadlock.

Resource request algorithm (P_i)

for a \tilde{t} to Safe state detection

Problem			$A = 10$	$B = 5$	$C = 6$	$D = 2$	$E = 1$	
Allocations			Available			Matrix		Need
P ₀	0 1 0 1	1 3 3 2	7 5 B	6 2 0 0 2	7 4 3			
P ₁	2 0 0	3 2 1 2	3 2 1 2	2 2				
P ₂	3 0 2	9 0 2 6	0 0 0 0					
P ₃	4 1 7	2 2 2	1 1 1					
P ₄	(18) 6 < 218 9	2	4 3 1 Vx					
		51 = (17) 9						

and our best answer is closed tariff s.i.

DATA STRUCTURE used in Banker's algorithm :-

- 1) Available[i] = K of length M (no. of resources)
- 2) Request[i][j] = K [no instances of Resource type R_j]
 ↓ ↓
 P_i R_j
- 3) Max[i][i] = K } Allocation[i][j] = K
- 4) Need[i][i] = K }

Banker's algorithm for resource request (partitions)

- 1) Request_i < Need_i [yes goto step 2 else
 request cannot be granted process goes to waiting state]
 if
- 2) Request_i ≤ Available_i
 Allocation_i = Allocation_i + Request_i
 Need_i = Max_i - Allocation_i
 Available_i = Available_i - Request_i

else
 process is in waiting state

and then go to next unit
initially busy state priority
as number of up ad down

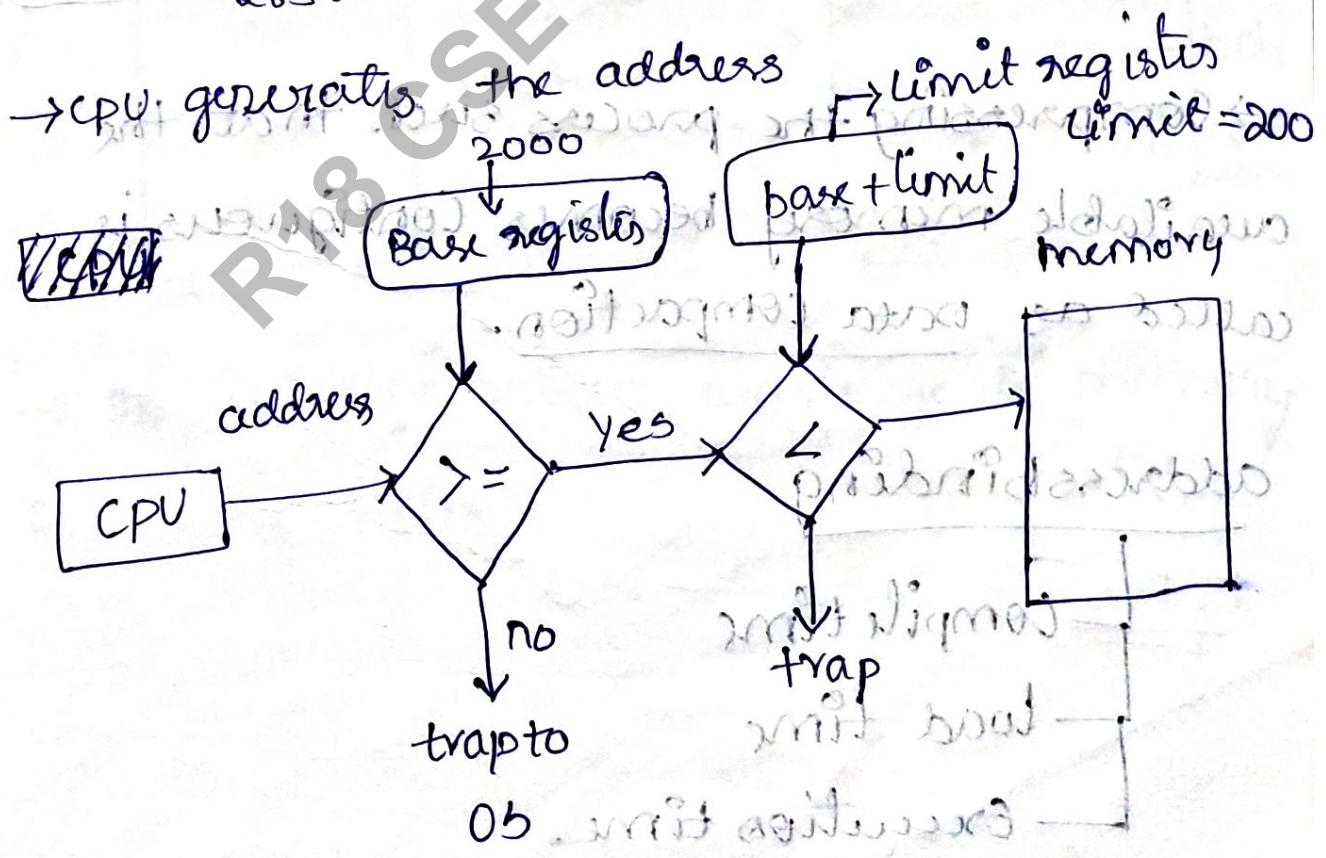
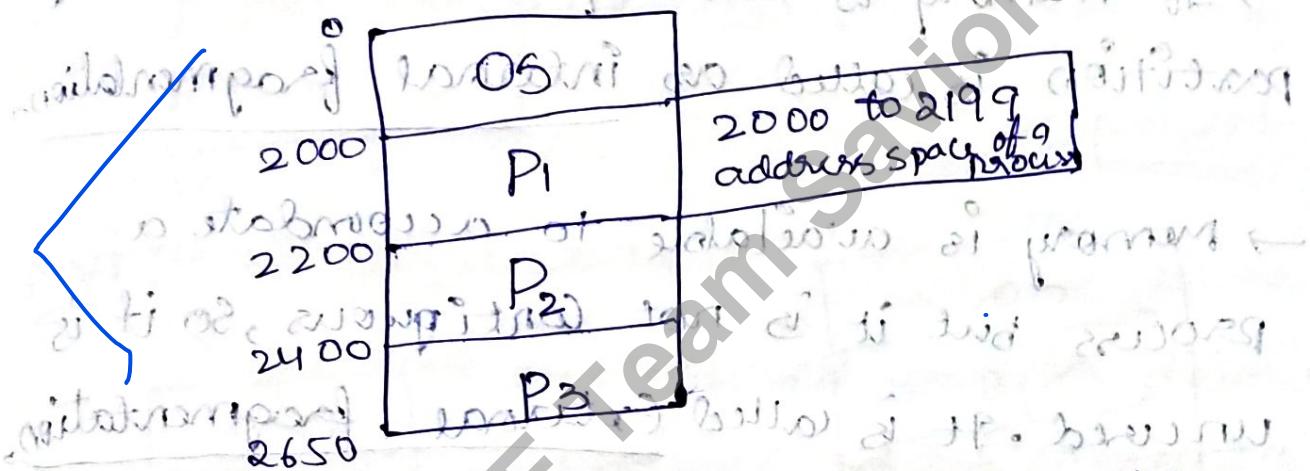
safe state detection algorithm:-

- 1) $\text{finish}[i] = \text{false}$ for all i .
Available = K
- 2) find a process such that
if $\text{finish}[i] = \text{false}$, Request \leq Available
else
 goto step 4
- 3) Available = Available + allocation $[i]$
 $\text{finish}[i] = \text{true}$ goto step 2
- 4) $\text{finish}[i] = \text{true}$ for all i then
System is in safe state
 $\text{finish}[i]$ is not true for all i then
System is in unsafe state.
↓
If P_2 process is unsafe state
then the process P_2 will be in
waiting state and preemption
will be go to process P_3

UNIT - III

Memory Management

- Cache memory Management algorithm
- Primary memory Management
- Virtual memory management
- Secondary memory Management



Hardware protection to memory address

Primary Memory Management

- multiple partitions technique (MFT) → fixed size partition
- multiple variable partition Technique (MVT)
- Paging
- Segmentation
- paging with segmentation

→ If memory is wasted/unused within the partition, it is called as internal fragmentation.

→ Memory is available to accommodate a process but it is not contiguous, so it is wasted. It is called external fragmentation.

→ Compressing the process such that the available memory becomes contiguous is called as Data compaction.

Address Binding

→ Compile time

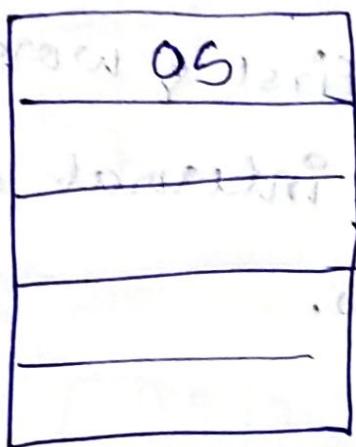
→ Load time

→ Execution time

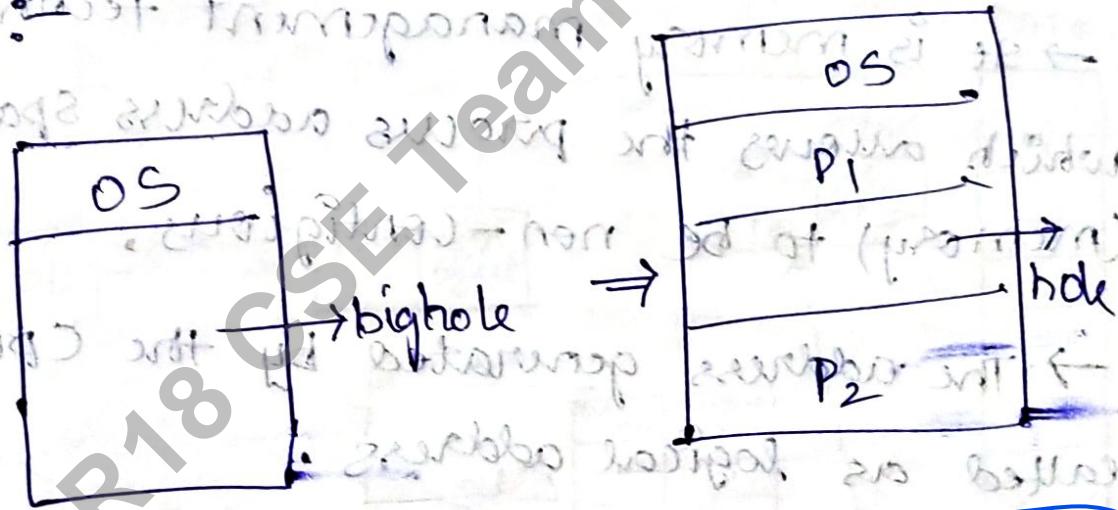
Data Loading

- load time loading (static)
- dynamic loading

MFT :-



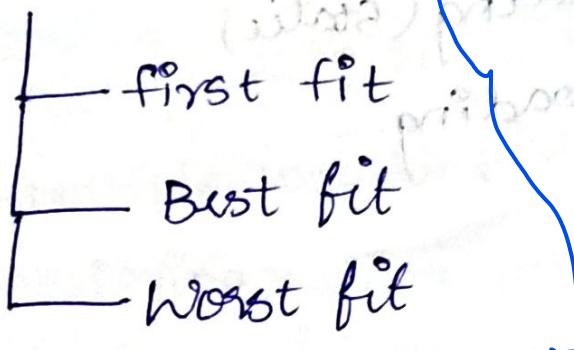
MVT:



→ In this technique wastage of memory is no of holes.

$$UMM - UMP = VMM$$

Holes Management :-



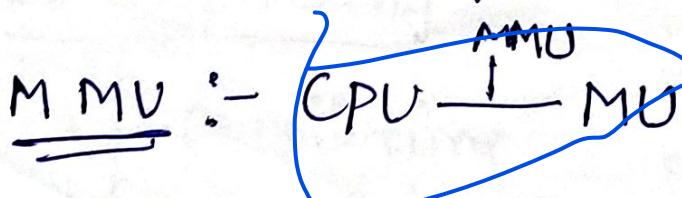
→ Apart from Best fit, first & worst fit suffer from both internal and external fragmentation.

Paging :-

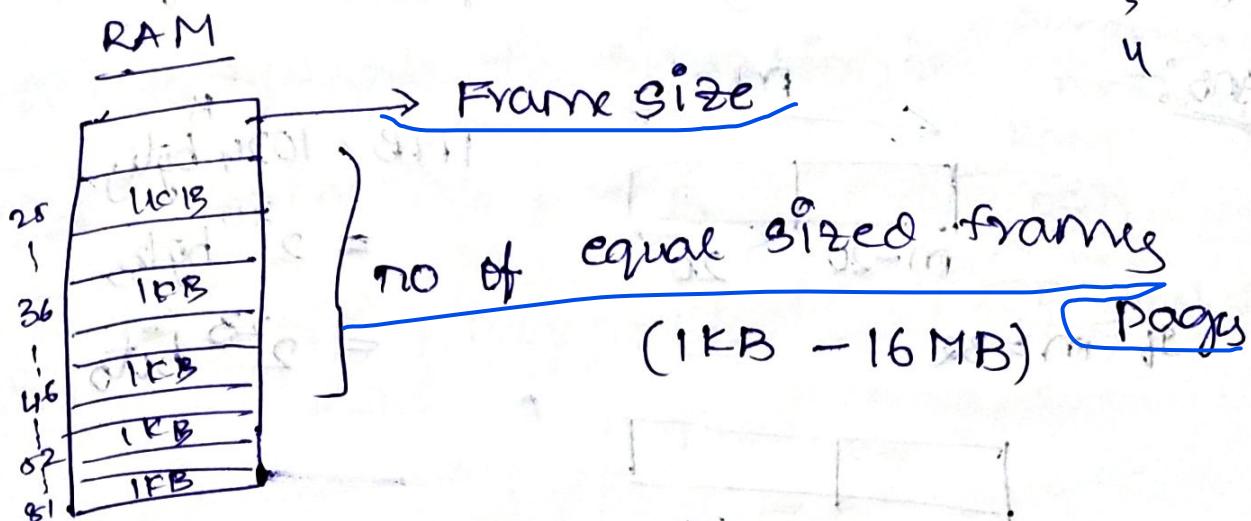
→ It is memory management technique which allows the process address space (memory) to be non-contiguous.

→ The address generated by the CPU is called as logical address.

→ The address seen by the memory unit is called as physical address.

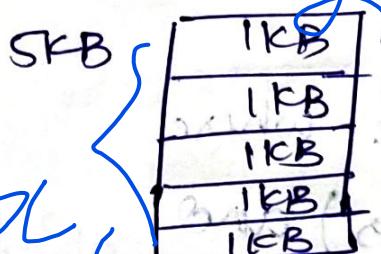


paging tablet



process

SKB



Fyol
ESY

pages

Frame size = page size

Physical Properties

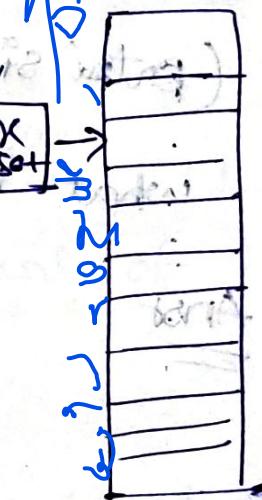
24

A diagram illustrating the flow of memory addresses. On the left, there is a rectangular box containing the letters "CPU". An arrow originates from the bottom right corner of this box and points towards the right. To the right of the arrow, the words "logical address" are written in cursive, with an arrow pointing from the end of the word "address" towards the right.

A hand-drawn diagram of a page table entry. It consists of two adjacent boxes. The left box is labeled "Page no." and the right box is labeled "Page offset".

Frame no Page off set

MAX no	frame no
0	25
1	36
2	46
3	52
4	81

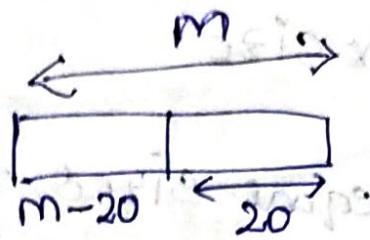


Main
muddy

display content

→ logical address m-bits, size of the page is 1MB. How many pages CPU can refer.

Ans:

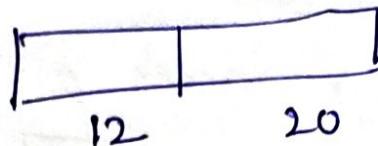


$$1\text{ MB} = 1024 \text{ bytes}$$

$$= 2^{20} \text{ bytes}$$

$$= 2^{23} \text{ bits}$$

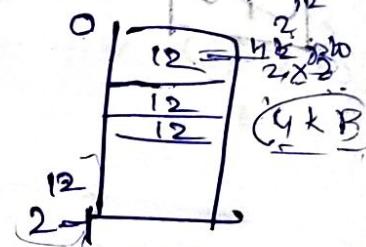
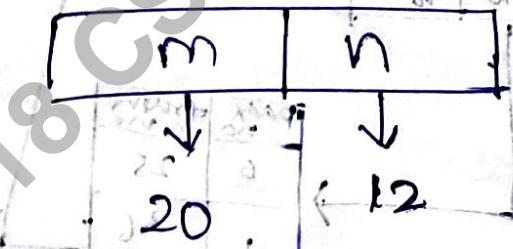
$$\therefore m = 32 \text{ (as } 31 - 11)$$



$$\therefore \text{Pages} = 2^{12} - 1$$

⇒ what is the page table size where
(Actual size of page is 10 bytes) and
what is size of page?

Ans:



$$\text{Size of page} \doteq 4 \text{ KB}$$

$$2^2 \times 2^{10} \text{ Bytes}$$

$$\text{Size of Page Table} = 2^{20} \times 10 \text{ bytes}$$

$$= 10 \text{ MB}$$

CPU Bus

Cache

RAM

USB

F
H

Continue before question
→ OS size is 512 MB if there are 4096 frames. what would be the max size of RAM to operate the situation?

Ans: Frame Size = 4KB

RAM → 512 MB + Page Table

+ Frames

$$= 512 \text{ MB} + 10 \text{ MB} + 4096 \times 4 \text{ KB}$$

$$= 512 \text{ MB} + 10 \text{ MB} + 16 \text{ MB}$$

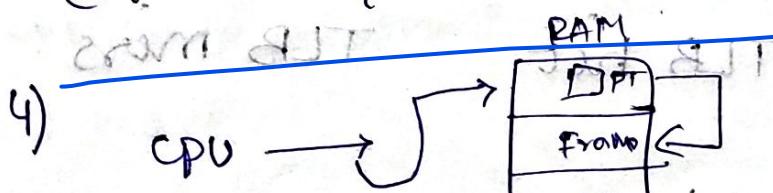
Limitations with page :-

1) There is internal fragmentation

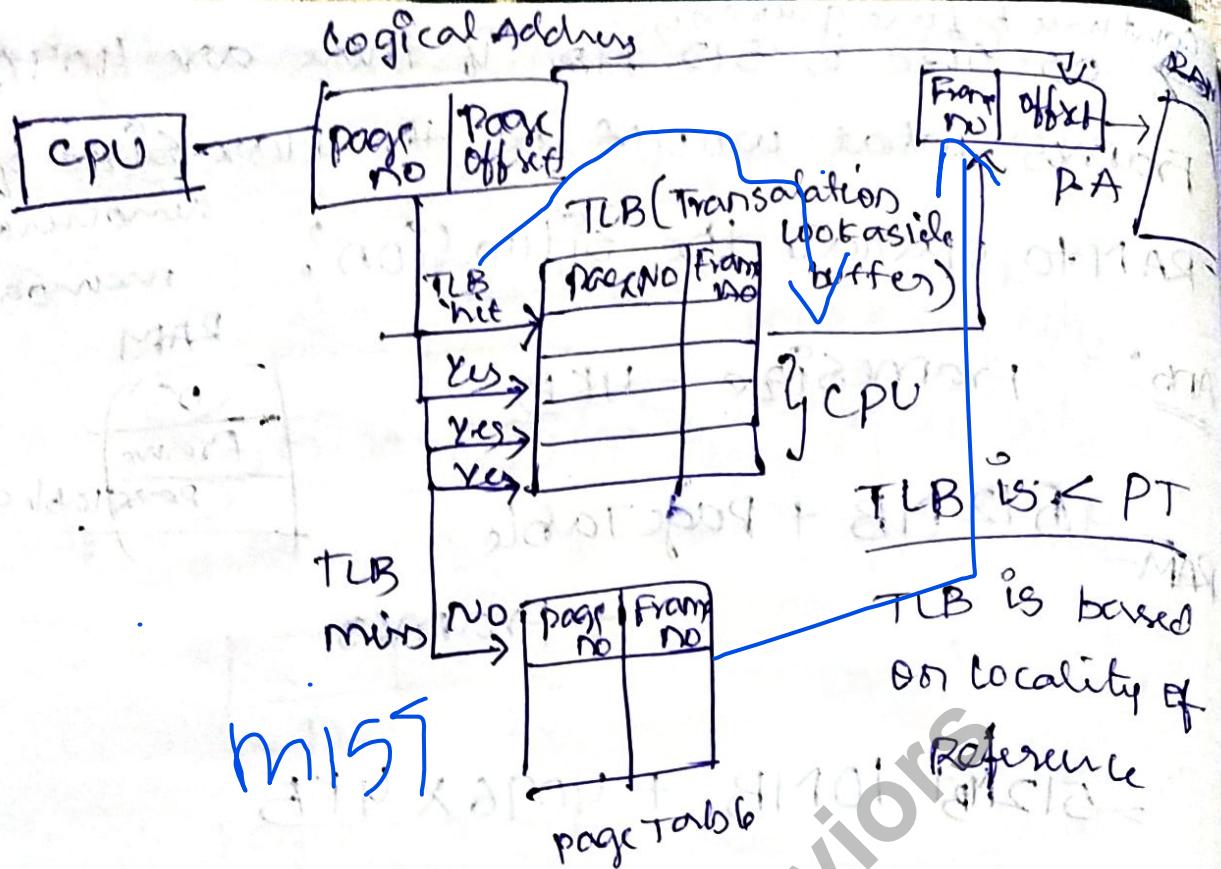
2) Page Table may consume huge amount of primary memory.

3) The operations on page table are costly.

(Time complexity, Space complexity)



RAM need to access twice.

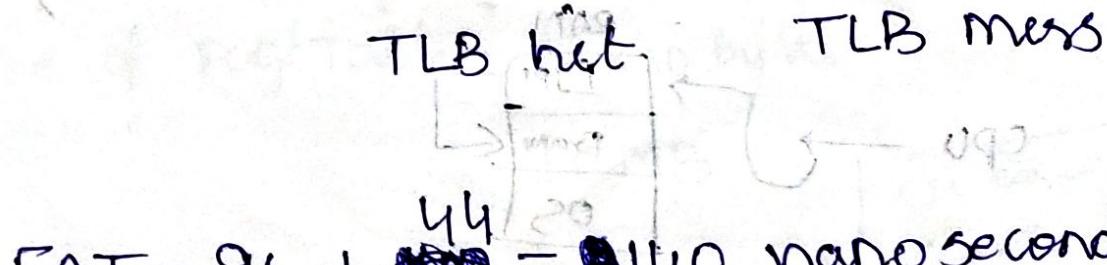


→ Page no which are frequently used are stored in ~~for generating address~~ ~~in TLB~~

Problem:-

- 1) 80% probability for TLB hit
TLB access time is 20 nanoseconds
main memory access time is 100.
 $EAT = 0.8 \times (20 + 100) + 0.2 \times (20 + 100) = 88 \times 120 + 100$

Ans. $EAT = 0.8 \times (20 + 100) + 0.2 \times (20 + 100) = 88 \times 120 + 100$



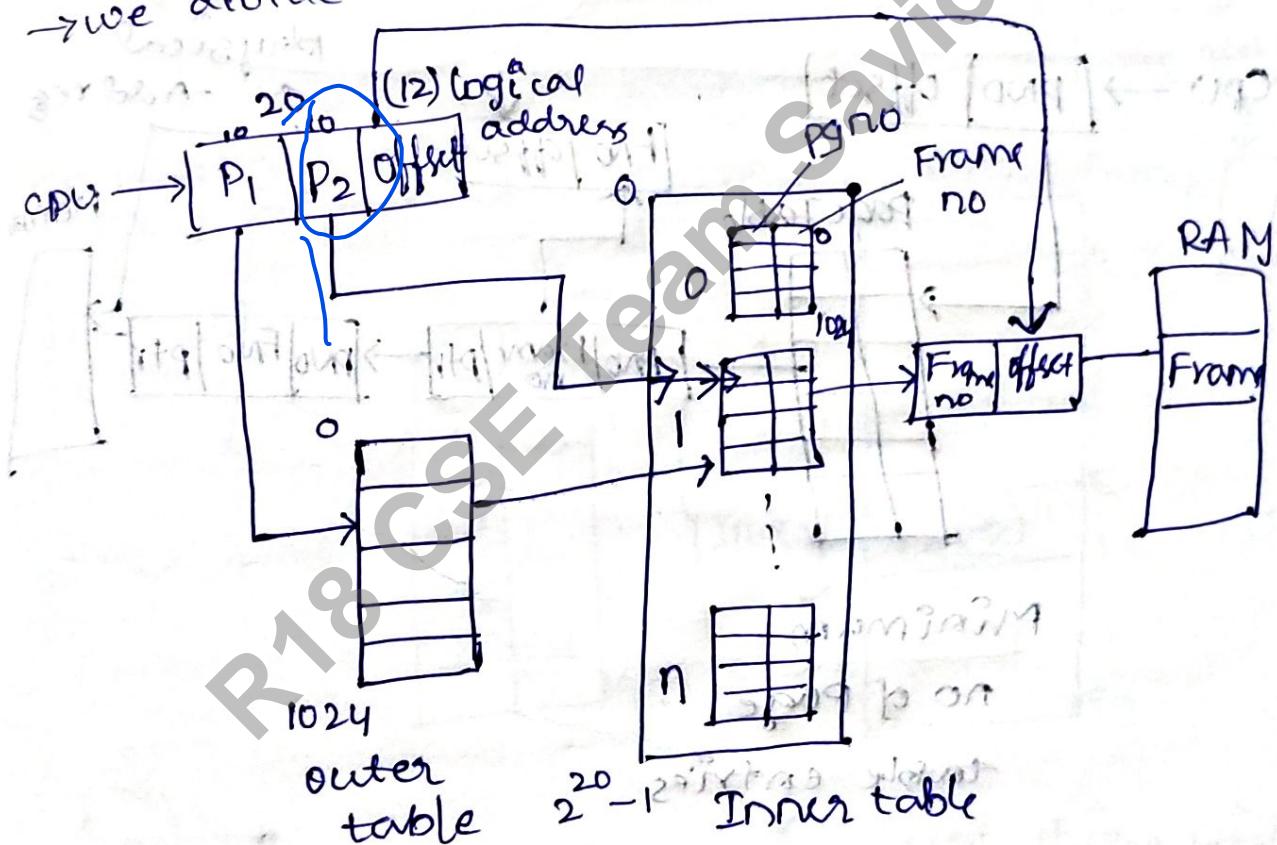
$$EAT = 96 + 44 = 140 \text{ nano seconds}$$

Page-tables :-

- 1) Multi-level Page table } Page table
for each process
- 2) Hash-based Page table
- 3) Inverted Page Table } Single page table
for processes

1) Multi-level Page Table:

→ we divide all the possibilities



→ Outer table gives info about which table i.e.,
1 or 2

→ P2 gives the line number in that table

from there we will get frame no.

2) Flash based page Table:

$$a[7] = \{0, 1, 2, 3, 4, 5, 6\}$$

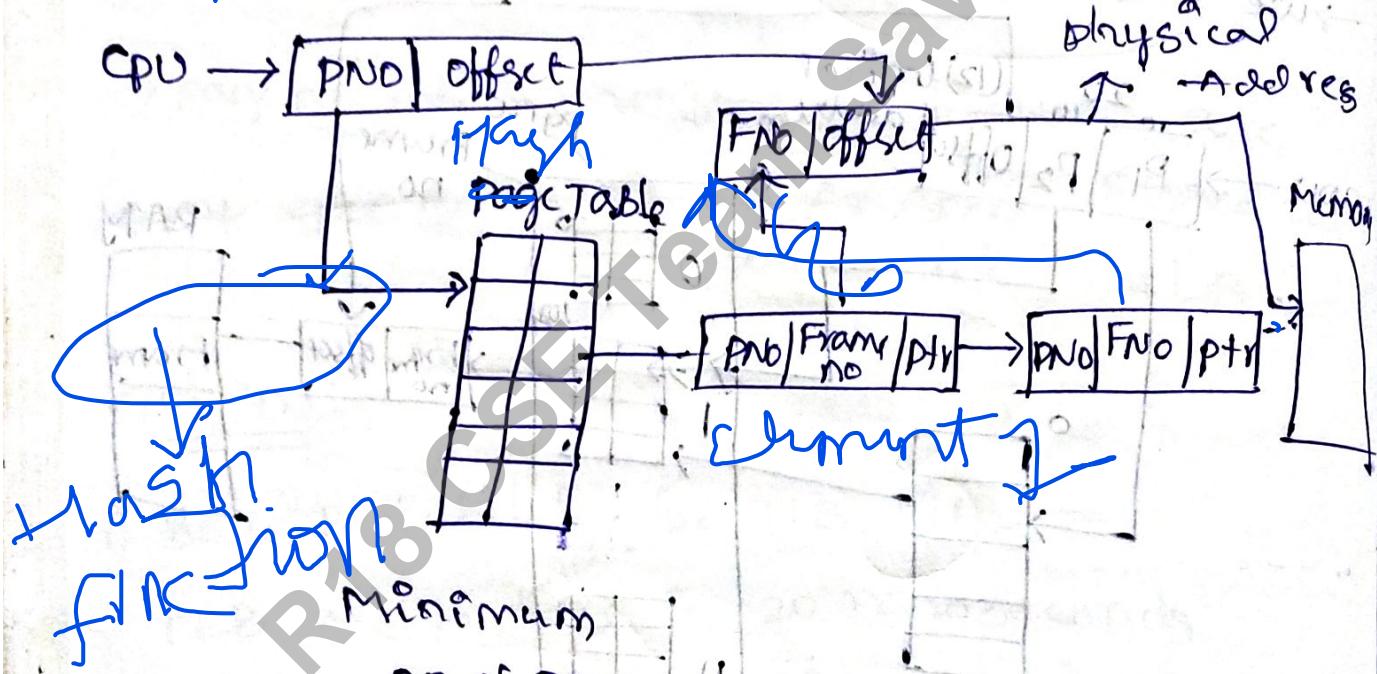
Linear Search

$$T(n) = O(n) (W.C)$$

$$T(n) = \Omega(1) (B.C)$$

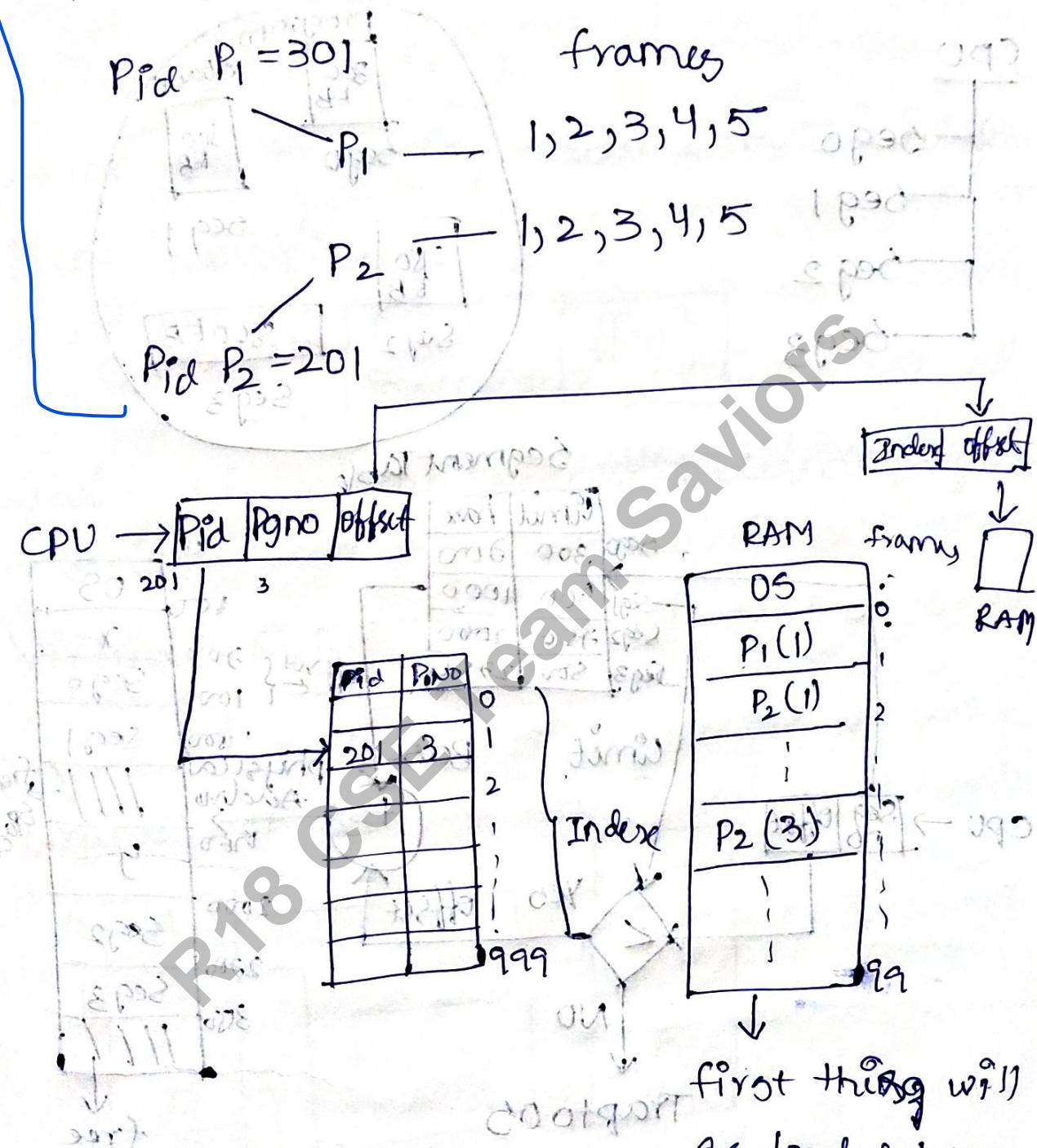
$$T(n) = \Theta(n/2) (A.C)$$

$$\rightarrow O(1)$$



3) Inverted page Table :-

→ One pageTable is maintained for all the processes.

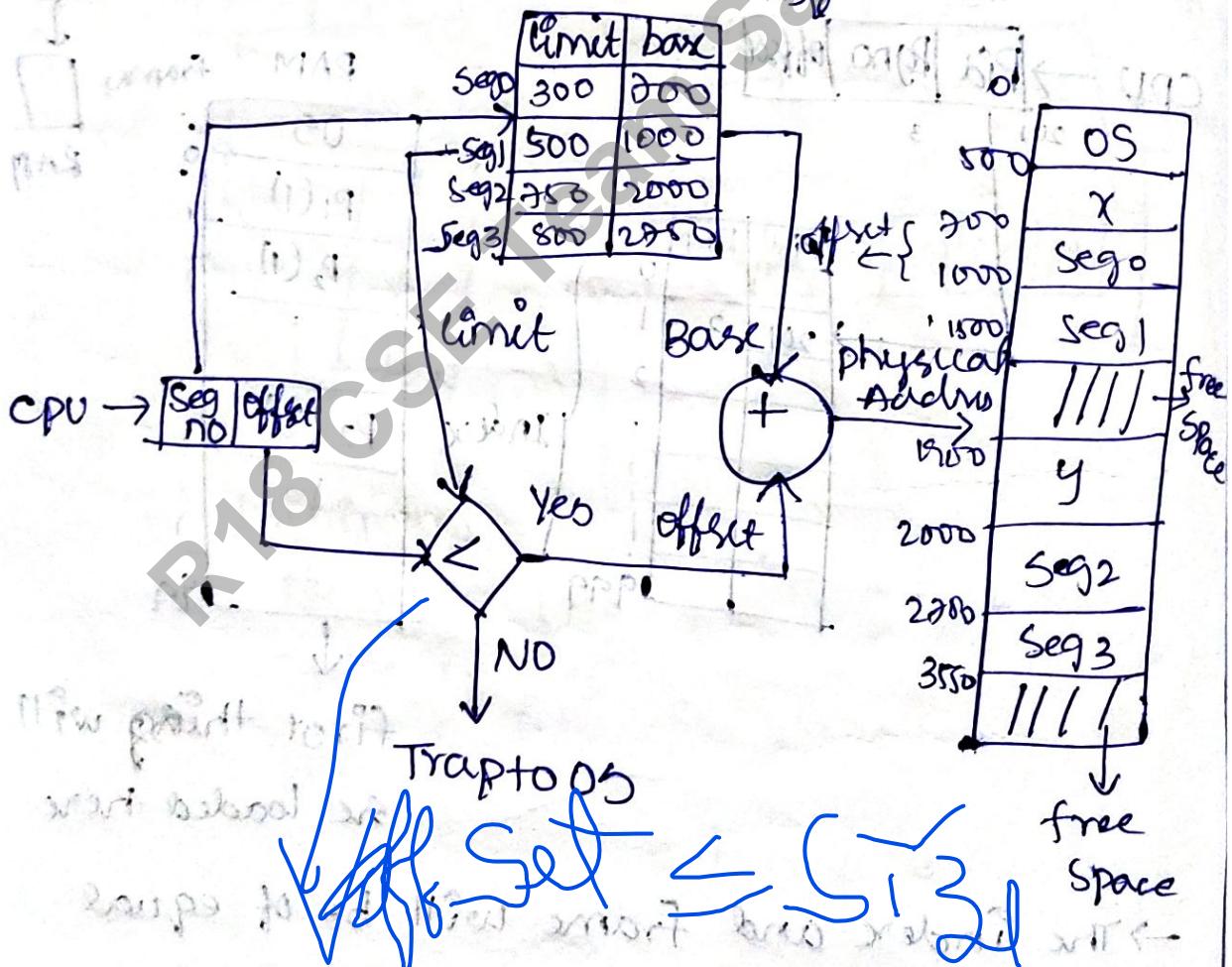
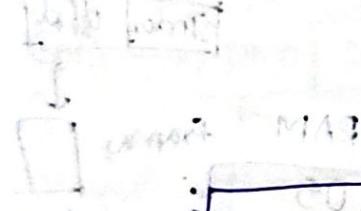
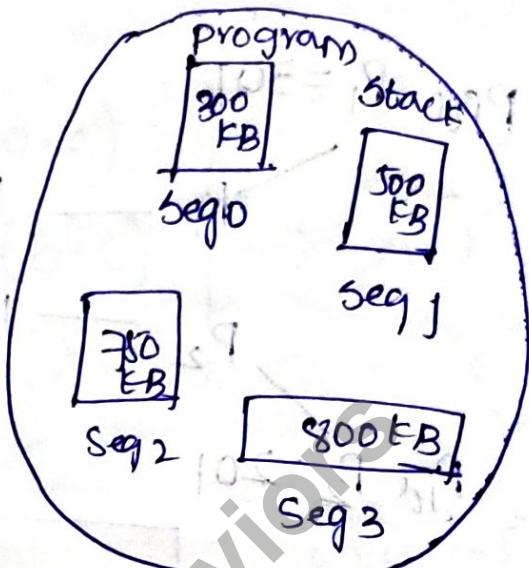
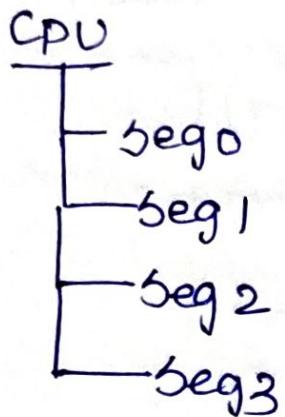


→ The index and frame will be of equal number and the index of the required page will be the same for the frame number.

$$\text{Index} = \text{frame no}$$

Segmentation:

→ It allows process address space to be non-contiguous.



Trap to OS
Segment Set
Segment Set

or main → main

Paging with Segmentation

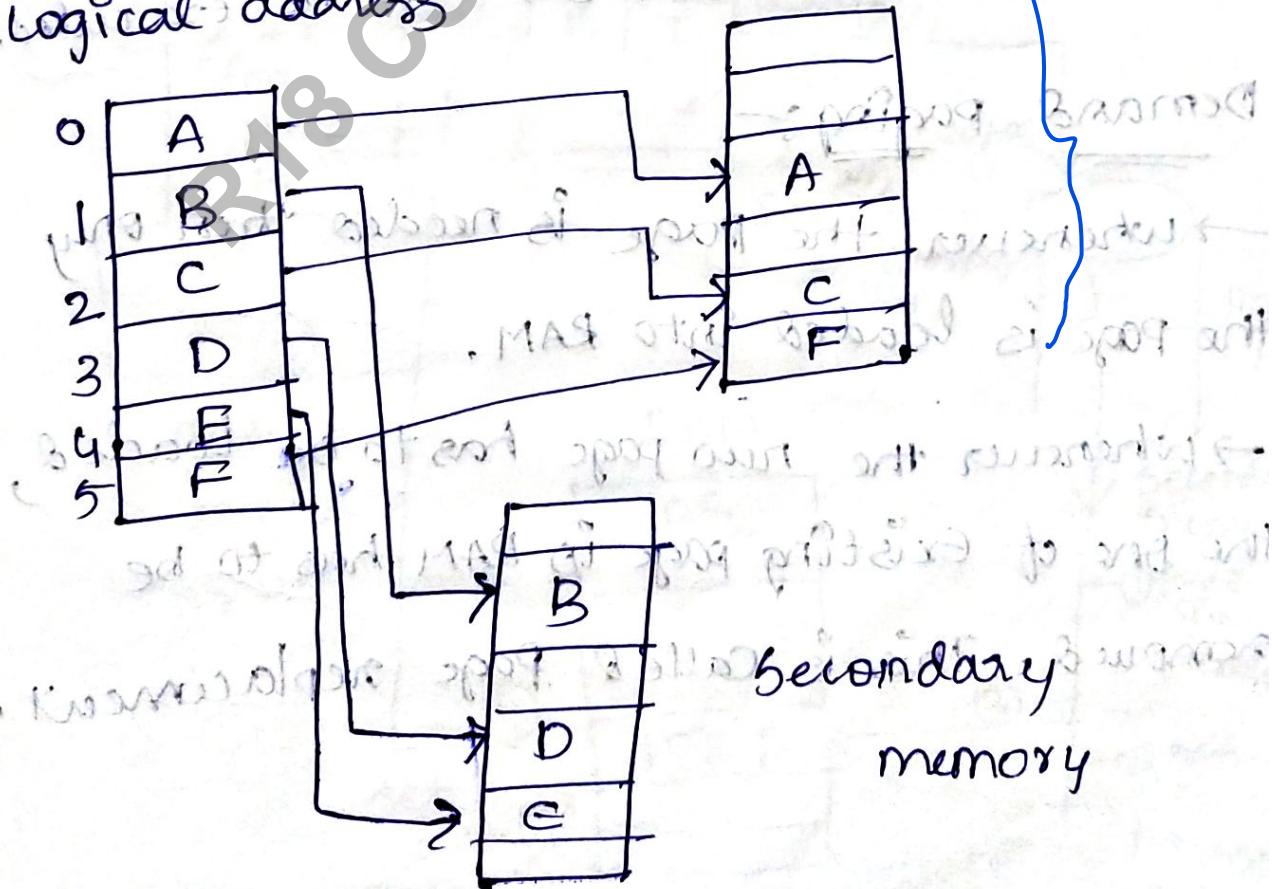
- In segmentation, we divide memory into segment. Now, for each segment we will have a page table.
- The CPU producing address is divided into 3 parts

SegNO	PageNo	Offset
Segment		

- With this we can reduce external fragmentation.

Virtual Memory

PO:
Logical address



→ In this technique, only few pages of the process are stored in the RAM and the rest are stored in secondary memory.

→ whenever the process is getting executed they will be added to RAM] → Demand

~~functionality of demand paging~~ Paging

→ The CPU can generate range of addressing larger than the primary memory addresses, the extra memory addressed by CPU is called as Virtual memory.

→ The particular section of hard disk is used to store the entire pages of a process.

→ Due to virtual memory a process having size more than RAM can also be Executed.

Demand Paging :-

→ whenever the page is needed then only the page is loaded into RAM.

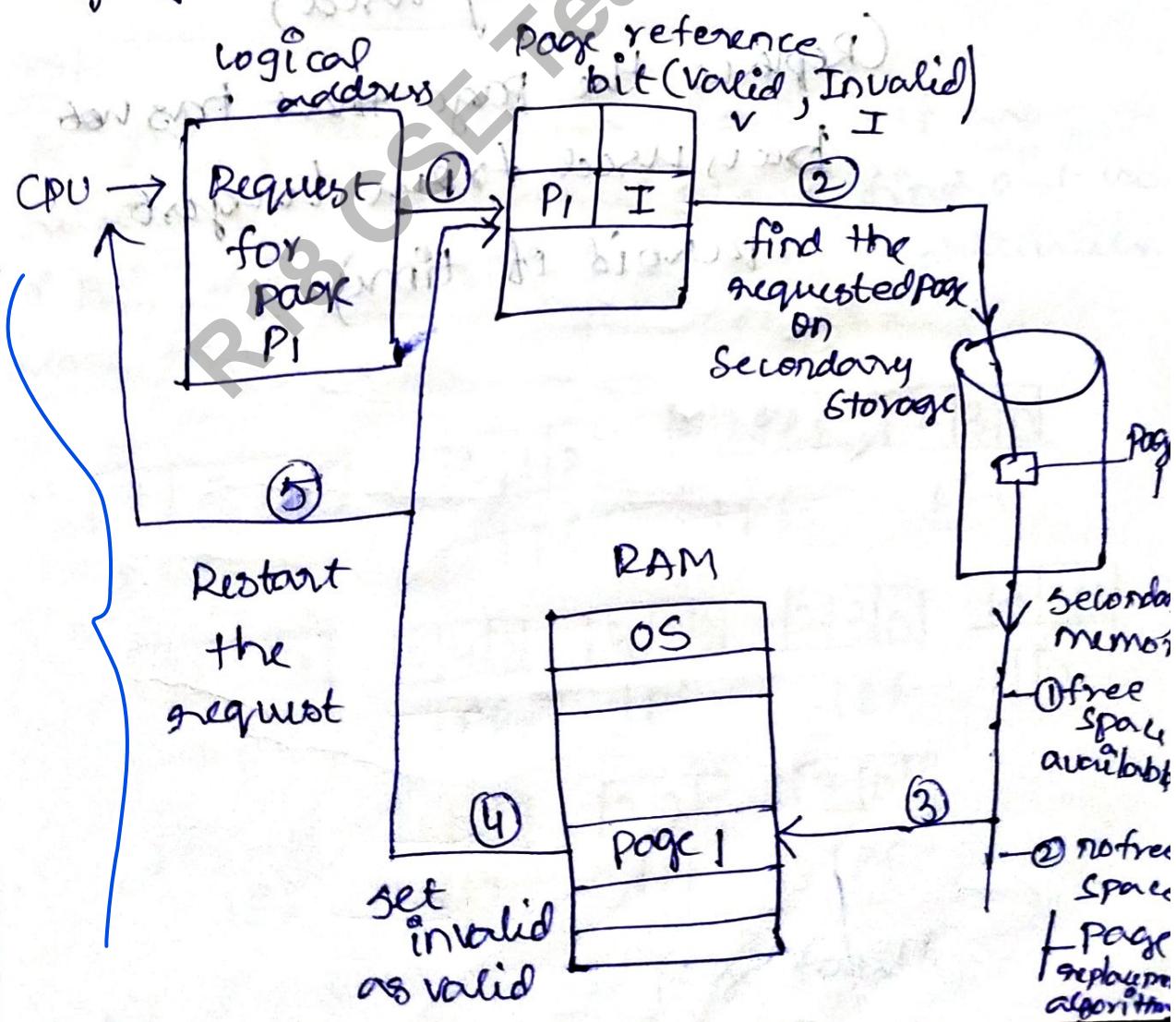
→ whenever the new page has to be loaded, the one of existing page in RAM has to be removed. This is called Page replacement.

Page fault :-

→ If the Requested page is not available in primary/ RAM the physical/memory then it is called page fault.

Pure Demand Paging :-

→ If we want to execute a process the first request would be a page fault, because no pages are loaded. When the execution of process starts with page fault and gets completed is called pure demand paging.



Page replacement algorithms

Issues that arise during Virtual Memory

- ① Frame allocation
 - ② Page replacement

(In multiprocessor systems)

FIFO → Replace oldest page

Optimal Page replacement

(replace the page that will not be used for longest period of time)

LRU (least recently used)

(Replace) the page that has not been used from longest period of time).

Reference String :-

→ The order of requests for the pages to execute a process is represented in the form of a memory string is called reference string of a process.

Example :-

FIFO :-

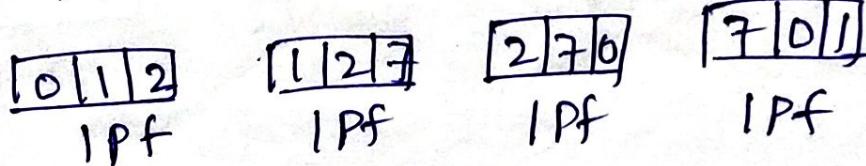
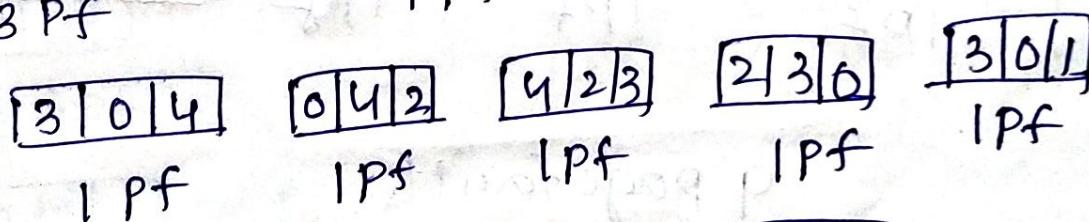
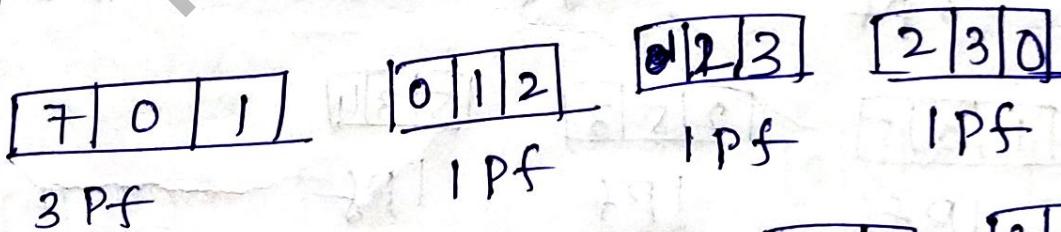
Reference String :-

7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 2, 1, 2, 0, 1, 7, 0, 1

→ Initially there are 3 free frames.

Ans

For the given reference string, if the available free frames are 3, find out no of page faults using FIFO page replacement algorithms.



∴ There are 15 page faults.

$\Rightarrow \underline{\text{FIFO}}$

RS = 1, 2, 3, 4, 1, 2, 5, 1, 2, 3, 4, 5

free frames = 2

Ans 1
=

1	2
---	---

2 Pf

2	3
---	---

1 Pf

3	4
---	---

1 Pf

4	5
---	---

1 Pf

1	2
---	---

1 Pf

2	5
---	---

1 Pf

5	1
---	---

1 Pf

1	2
---	---

2	3
---	---

1 Pf

3	4
---	---

4	5
---	---

1 Pf

1 Pf & 2 Pf = 3 Pf

\Rightarrow 12 page faults

\Rightarrow FIFO using 3 frames

RS = 1, 2, 3, 4, 1, 2, 5, 1, 2, 3, 4, 5

1	2	3
---	---	---

3 Pf

2	3	4
---	---	---

1 Pf

3	4	1
---	---	---

1 Pf

4	1	2
---	---	---

1 Pf

1	2	5
---	---	---

1 Pf

2	5	3
---	---	---

1 Pf

5	3	4
---	---	---

1 Pf

1	0	F
---	---	---

1 Pf

1	0	1
---	---	---

1 Pf

0	1	2
---	---	---

1	2	3
---	---	---

1 Pf

2	3	4
---	---	---

1 Pf

1	0	1
---	---	---

1 Pf

1	0	F
---	---	---

0	1	C
---	---	---

1 Pf

1	2	I
---	---	---

1 Pf

0	1	I
---	---	---

1 Pf

1	0	I
---	---	---

0	1	I
---	---	---

1 Pf

1	2	I
---	---	---

1 Pf

0	1	I
---	---	---

1 Pf

⇒ use the LRU page replacement algorithm
to find no of page faults available free
frames = 3

reference string

= 7, 0, 1, 2, 0, 3, 0, 4, 1, 2, 3, 0, 3, 2, 1, 2, 0, 1, 7, 0, 1

AⁿS^t

7	0	1
---	---	---

3 Pf

0	1	2
---	---	---

1 Pf

1	2	0
---	---	---

1 Pf

2	0	1
---	---	---

2	3	0
---	---	---

1 Pf

3	0	4
---	---	---

1 Pf

0	4	2
---	---	---

1 Pf

4	2	3
---	---	---

1 Pf

2	3	0
---	---	---

1 Pf

2	0	3
---	---	---

0	3	2
---	---	---

1 Pf

1	3	2
---	---	---

3	1	2
---	---	---

1 Pf

1	2	0
---	---	---

1	2	0
---	---	---

0	1	2
---	---	---

1 Pf

1	7	0
---	---	---

7	0	1
---	---	---

⇒ 12 Pf

AⁿS^t optimal

7	0	1
---	---	---

3 Pf

2	0	1
---	---	---

1 Pf

2	0	3
---	---	---

1 Pf

2	4	3
---	---	---

1 Pf

2	9	3
---	---	---

1 Pf

2	0	1
---	---	---

1 Pf

7	0	1
---	---	---

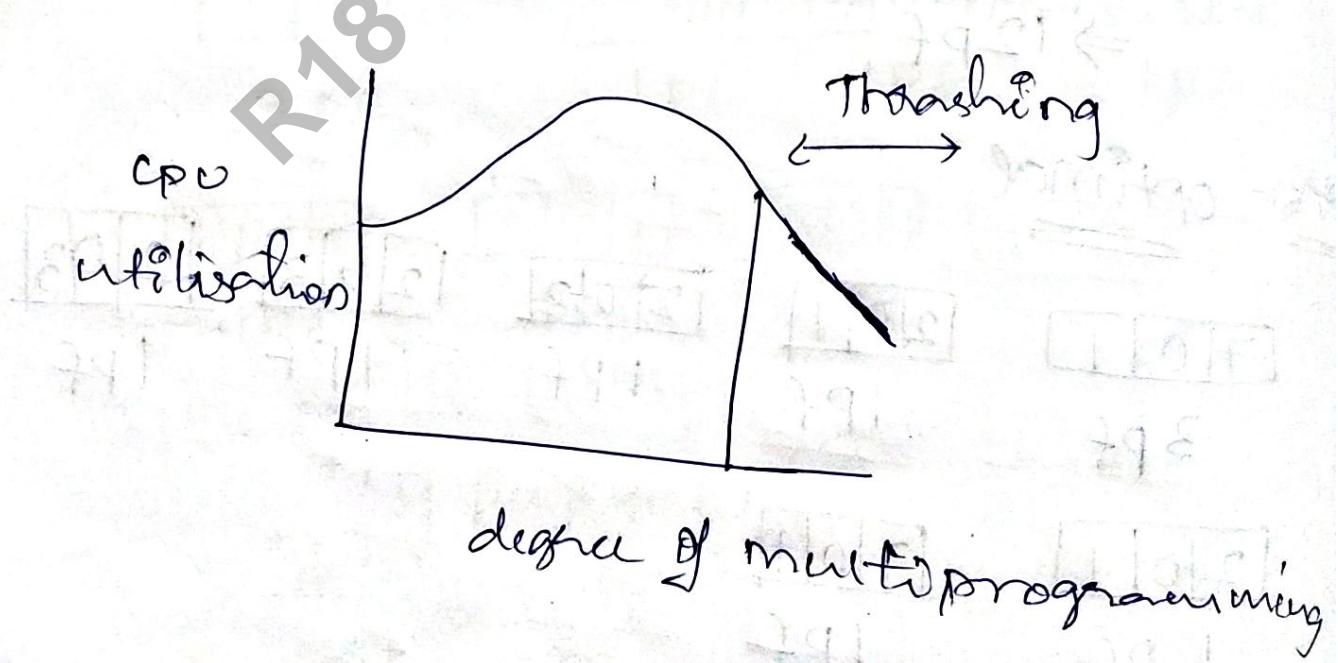
1 Pf

⇒ 9 page faults

Thrashing :-

- The high paging activity is called thrashing.
- If no of page faults are more than the CPU utilization is less.
- In such situation, the CPU is spending more time in servicing the page fault rather executing the process. It is called thrashing.
- A process is said to be thrashing if it spends more time in paging.

★ → Thrashing decreases the degree of multi-programming,



Semaphores :-

→ Semaphores is a integer variable used to implement mutual exclusion to a critical section problem.

→ It is generally accessed by two methods

1) wait(s) → S-operation → used to decrement s value.

2) signal(s) → P-operation → used to increment s value.

s → Semaphore

$$\boxed{s=1}$$

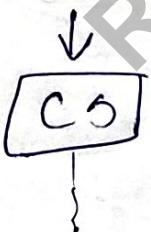
→ s=1 Means no process in CS.

→ P₁ tries to access but has to wait.

P₀

{

wait(s) ← wait(s) {



which ($s \leq 0$)

no operation

signal(s)

$s--;$

signal(s) {

$s++;$

P_0 is in CS and P_1, P_2, P_3, \dots are waiting then if P_0 complete then a random process will enter CS. Hence it satisfying mutual exclusion but bounded waiting is not satisfied.

~~Ques~~

Semaphore

Binary (0 or 1)

Counting (takes values in unrestricted domain)

—
Wait(S) {

$S--;$

if ($S < 0$)

add process to process blocked list

block();

}

Signal(S) {

$S++;$

if ($S \leq 0$)

wake up first process of blocked list

wakeup(P)

}

classic problem of synchronization:-

- 1) producer and consumer
- 2) reader and writer problem
- 3) dining philosopher's problem

empty = n

mutex = 1

full = 0

① producer :-

do {

 wait (empty);

 wait (mutex);

 produce item

 signal (full);

 signal (mutex);

 } while (true);

Consumer:

do {

 wait(full);

 wait(mutex);

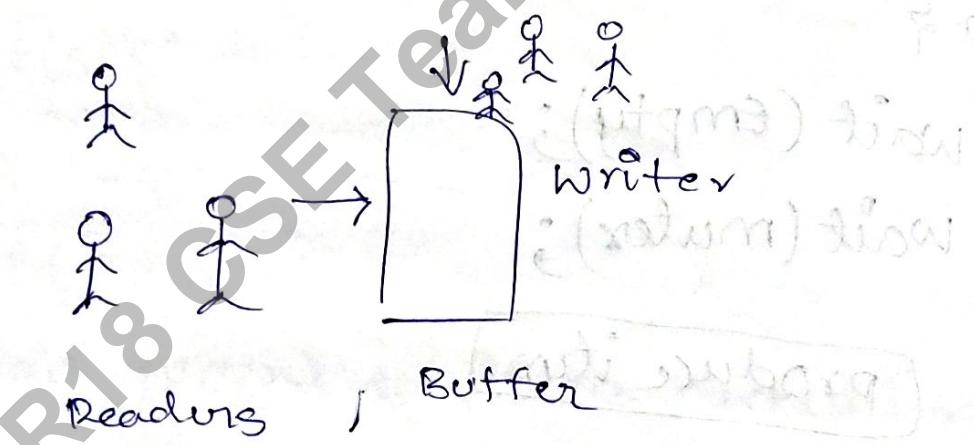
 boxed (consume items);

 signal(empty);

 signal(mutex);

} while (true);

② Readers and writers Problem:-



- i) when one writer is writing no reader is allowed to read.
- ii) when readers are reading no writer is allowed to write
- iii) when one reader is reading if one more reader is ready to read please allow.

wrt. \rightarrow binary Semaphore

$\downarrow = 1$ (no one in CS)

$= 0$ (process in CS)

read initially = 0

count = to count the no of readers

mutex = 1 (for mutual exclusion on
read count by readers).

Writers code :-

```
do {  
    wait(wrt);  
    write data;  
    Signal(wrt);  
} while (true);
```

Readers code :-

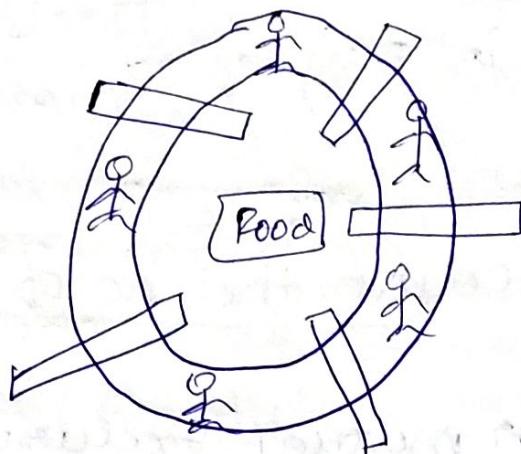
```
do {  
    wait(mutex);  
    read count++;  
    if (read count == 1)  
        wait(wrt);
```

Signal(mutex);

Read data CB operation

```
    wait(mutex);  
    read count --;  
    if (read count == 0)  
        Signal(wrt);  
} while (true);
```

③ Dining philosopher problem:-



eg:- 5 Philo

5 Chopstick

do {

 wait(chopstick[i])

 wait(chopstick[(i+1)%5])

 eating;

 signal(chopstick[i]);

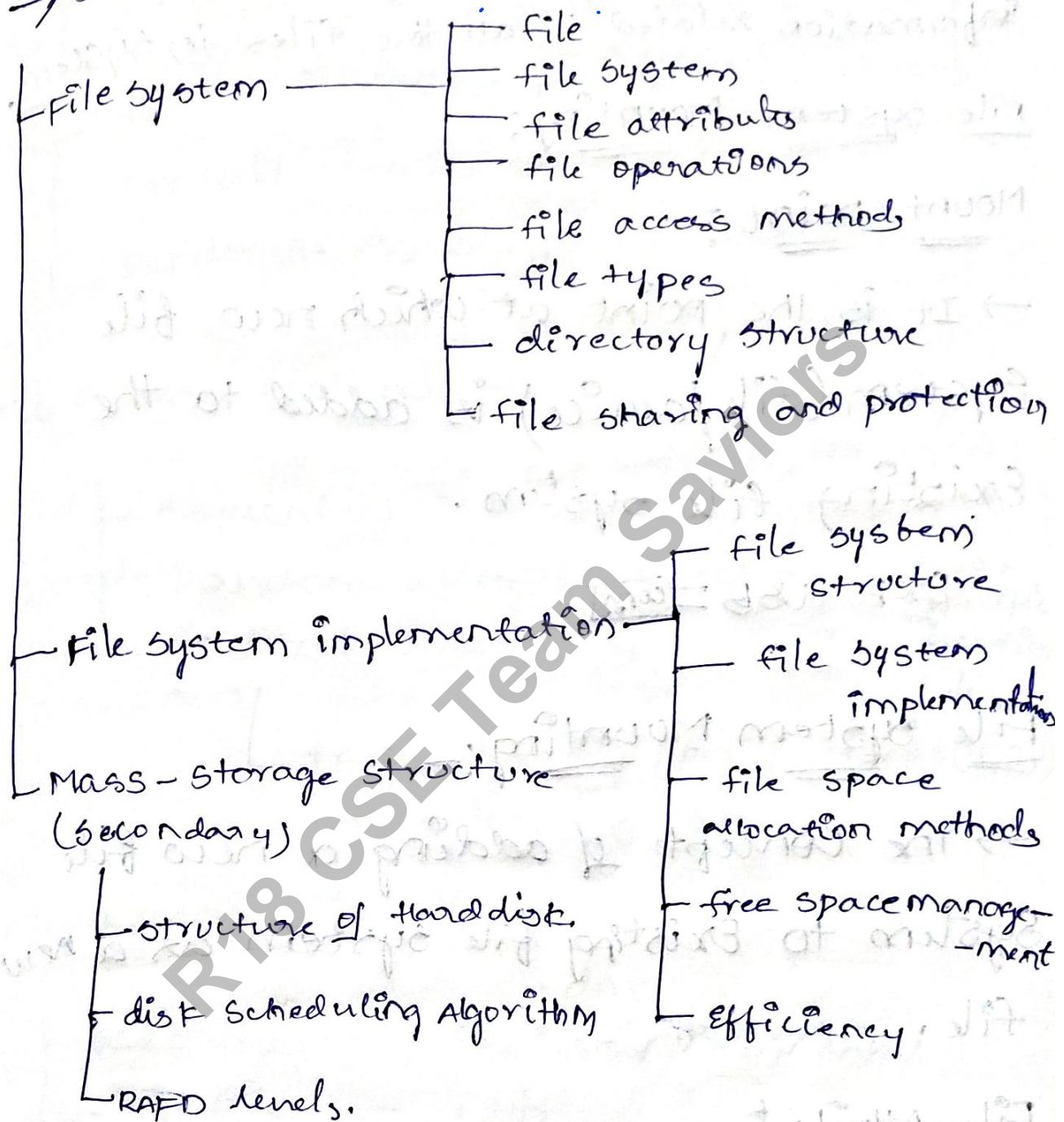
 Signal(chopstick[(i+1)%5]);

}while(true);

UNIT-4

File Management

→ service offered by the OS to the public ..



FILE :-

- Named collection of related data stored permanently on secondary storage device.

File system:-

→ It is a collection of files, directory structure [DS → It provides and organises the information related to all the files in System]

File system Mounting:-

Mount point :-

→ It is the point at which new file system (file/device) is added to the existing file system.

Ex:- USB [com]

File system Mounting:-

→ The concept of adding a new file system to existing file system as a new file.

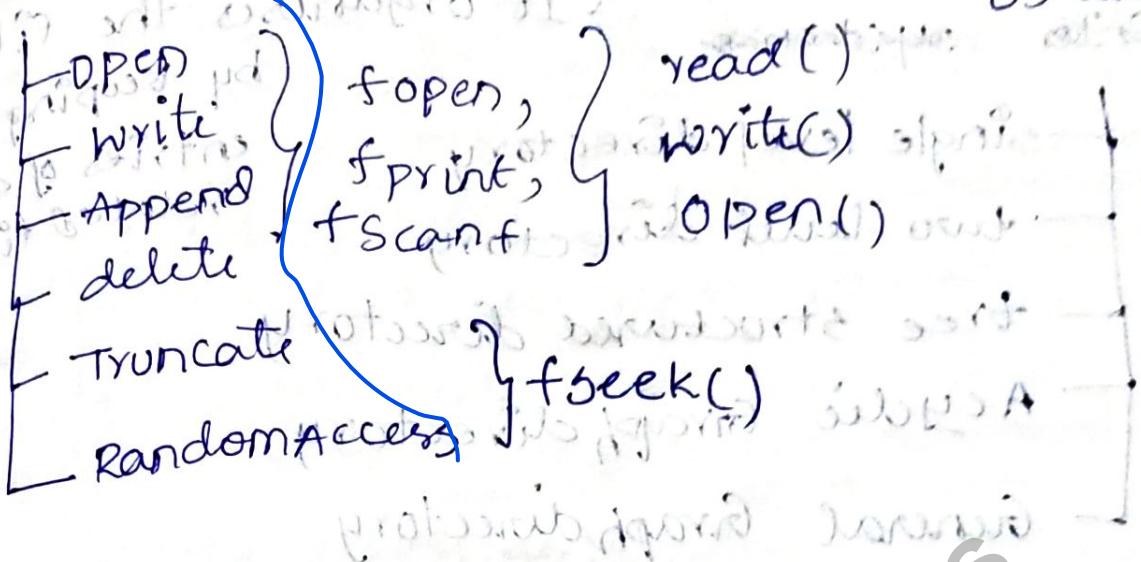
File Attributes:-

- name of the file
- file identifier
- file location
- file size
- file type
- file protection

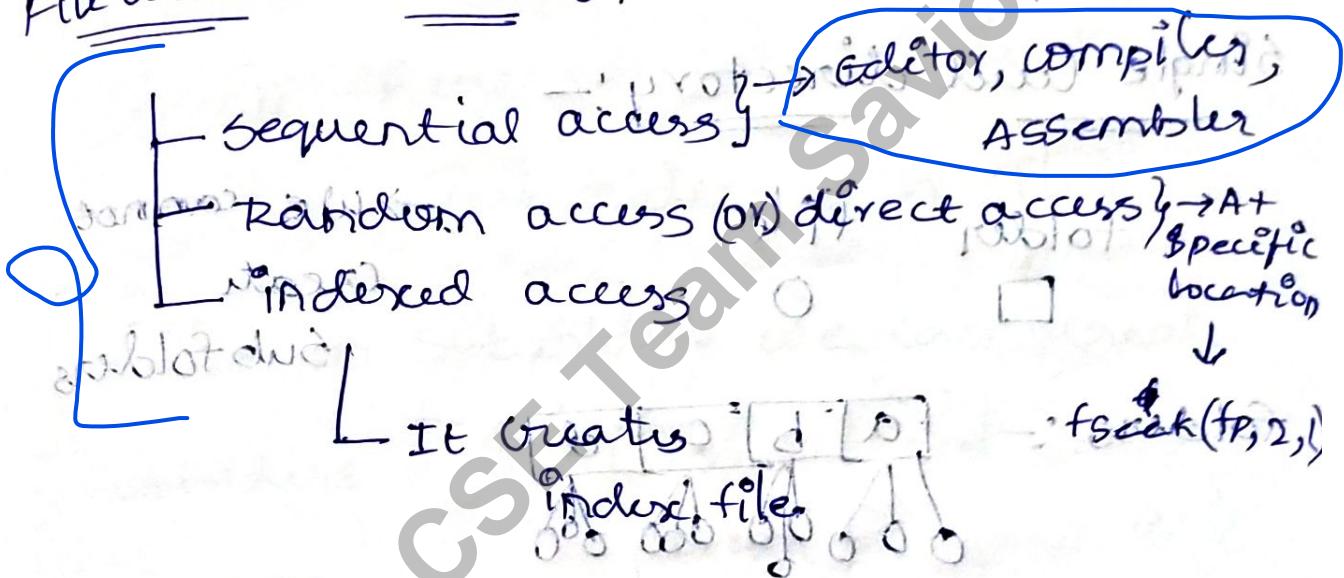
- owner
- time, date of creation

file operations:-

System calls at
OS level



File access Methods:-



File types: ~~HTML~~ probabili à suu2i traformi
doç

• doc

Text file - .txt, .xml, .pdf, .docx

— zip file — .zip

→ Programming language — C, CPP, Java,

→ Executable file

• .exe , • .lib , • .bin

Read only file .ppf, in PEG

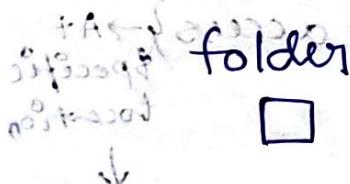
batch file .bat, .sh

DIRECTORY STRUCTURE

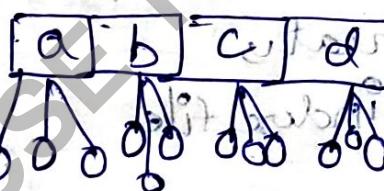
folders)

- ~~Or It's~~ ~~system~~ → It organises the file by keeping entries of a related file.
- Single level directory
- two level directory
- tree structured directory
- Acyclic Graph directory
- General Graph directory

Single Level directory :-



folders :-



→ We cannot create subfolders

Important issues of Directory Structure

1) Name collisions

2) Grouping

pure CPTC - proposed printing

→ All directory will have same

issues,

dir. addresses

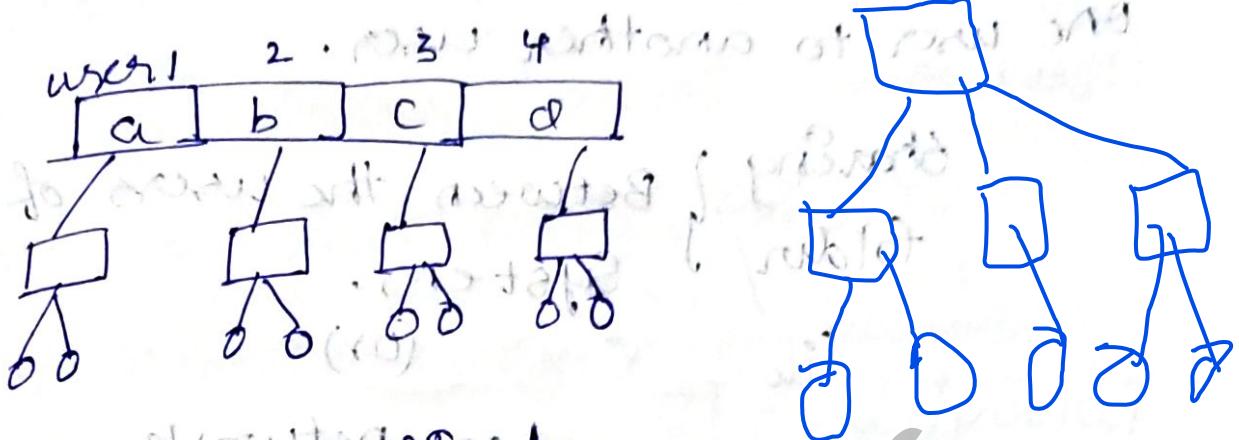
and. dir. ex.

pure CPTC. dir. file

de. add. dir. add.

Two-level directory: ~~the directory is simple~~

→ each user can create its own sub-folders

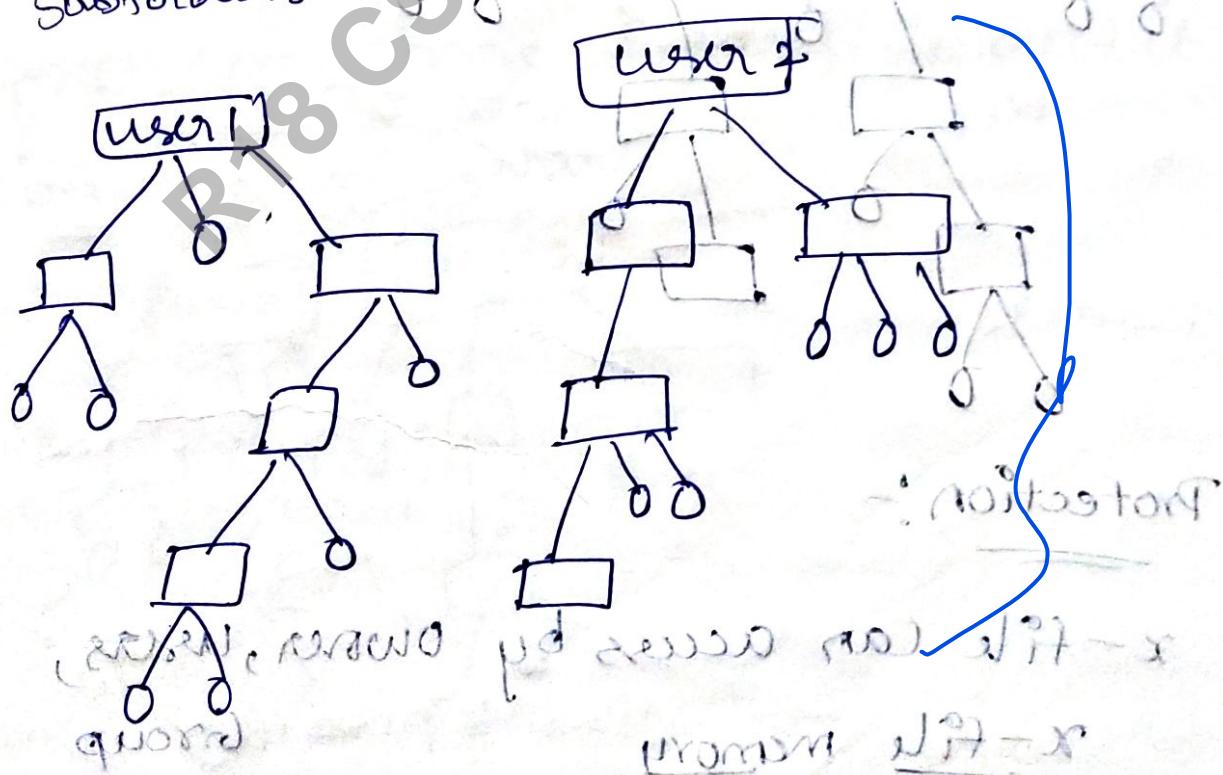


Tree structured directory:

user 1 → user can create n

subfolders & n files

→ And in subfolders we can create subfolders and files.



process HT-X

H ← busy
S ← direc
I ← users

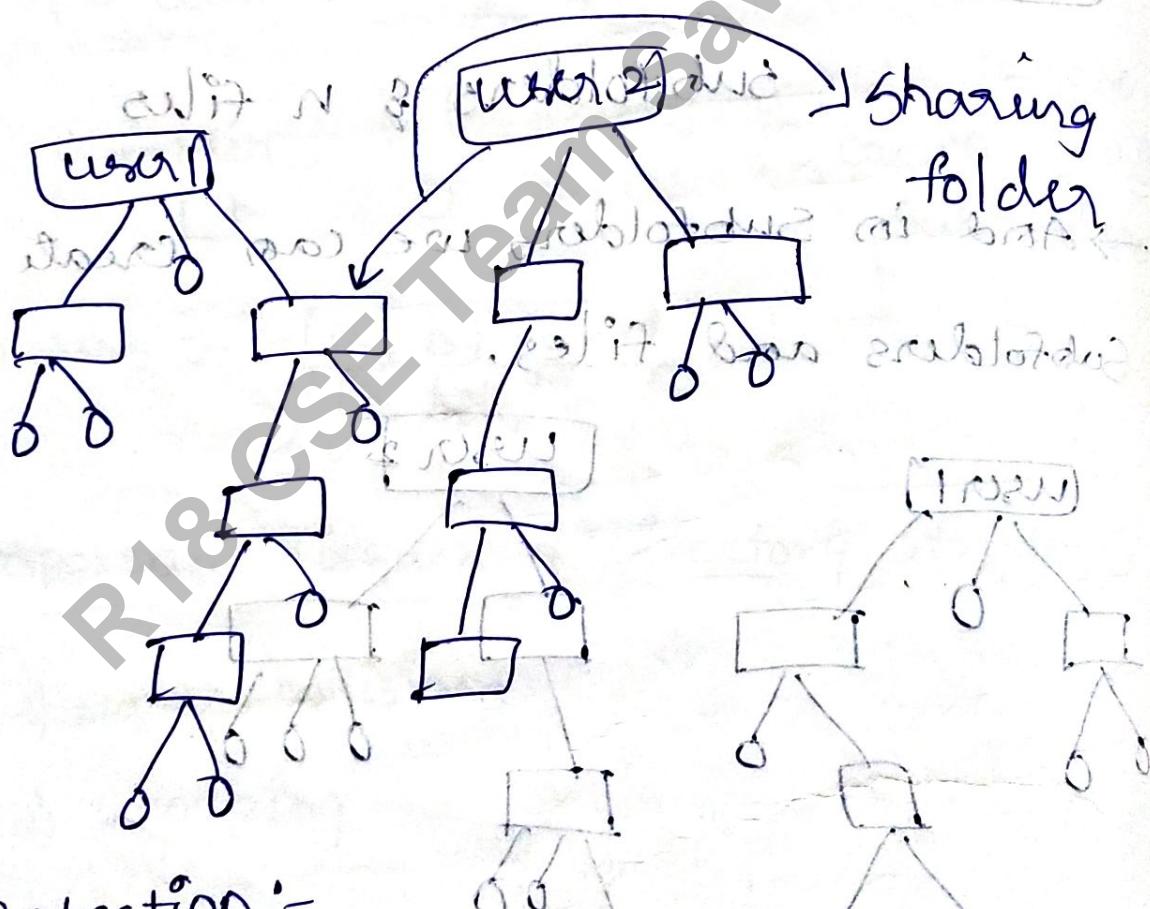
⇒ Acyclic graph directory!

→ In this a folder can share from one user to another user.

Sharing } Between the users of
folder } system.
file. (or)

Local network
(or)

It stores on client server



Protection :-

x-file can access by owner, user, group.

x-file memory

read → 4

write → 2

execute → 1

In Linux

command :

>> chmod

Group

761

owner

file Permissions
for Group,
owner

owner can read, write, execute.

Group can read, write

user can execute

[user/public]

→ In Windows Acyclic Graph directory

is implemented.

⇒ General graph directory:-

→ It can share folder (file and it

is cycle.

sharing

user1

user2

cyclic

subdir

subdir

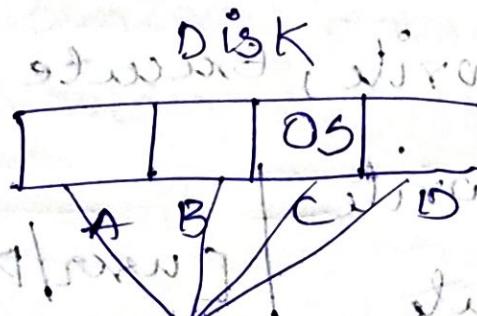
subdir

subdir

subdir

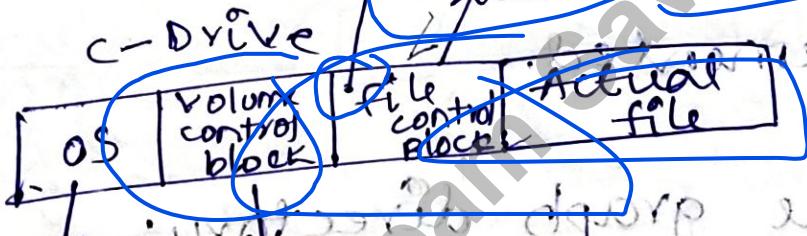
File system implementation

→ File system belongs to each partition.



file system → directory structure

inode block → identification of a file



boot control block → details of the drive

→ layered structure of

user application =

Read(x)

logical file system

[It is a module that

find metadata of a file].

file organisation module

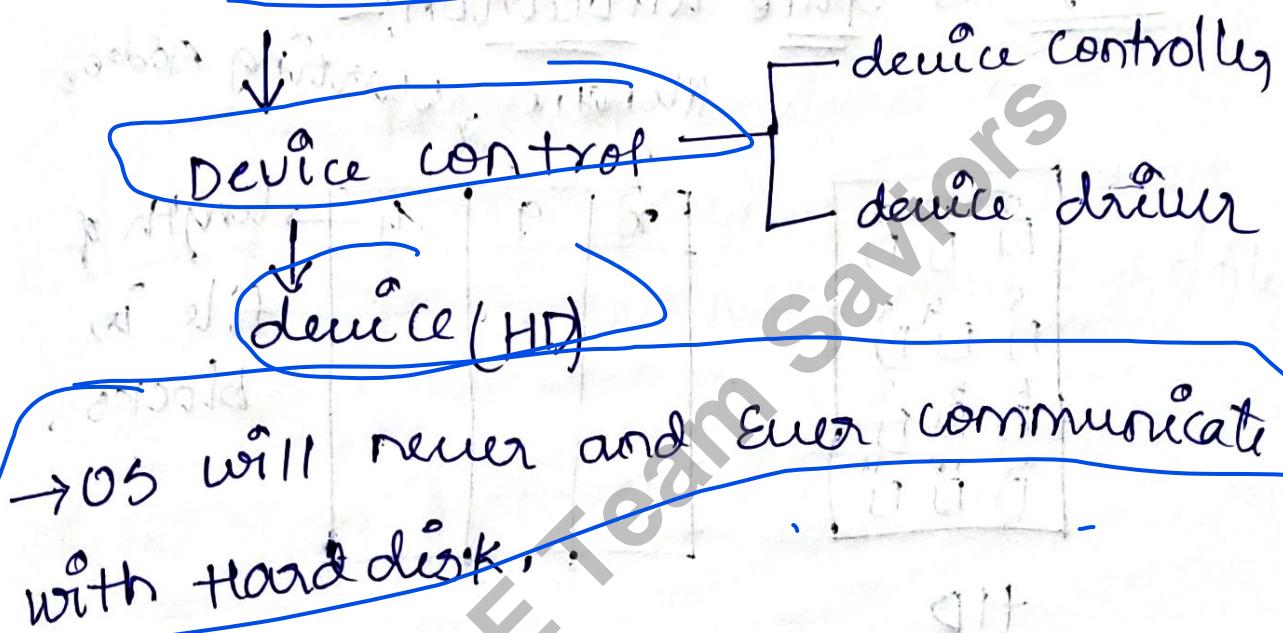
[which converts logical address to physical Address]

Basic file System

[It takes Address from FOM and go to that address]

→ [It creates Generic command]

72 cylinder, 32 track, sector]



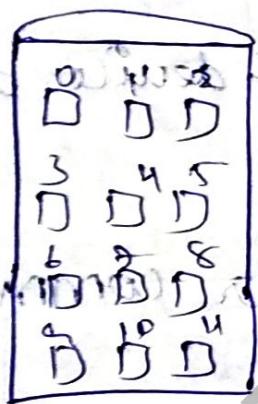
→ OS will never and ever communicate with hard disk

File space allocation methods :-

- continuous space allocation
- linked space allocation
- indexed space allocation

Continuous Space allocation:-

allocation units



filename starting Address
particular address

x	9	4	length of file in blocks,
			file starts at address 4 and ends at address 9.

HD

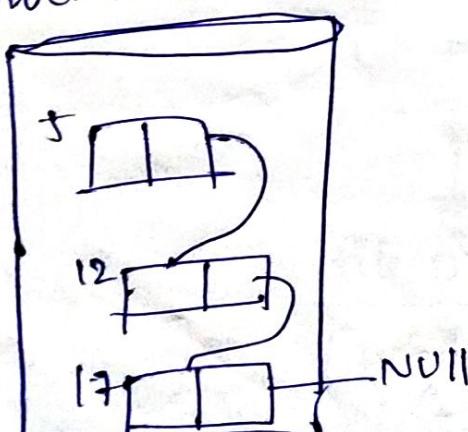
Block

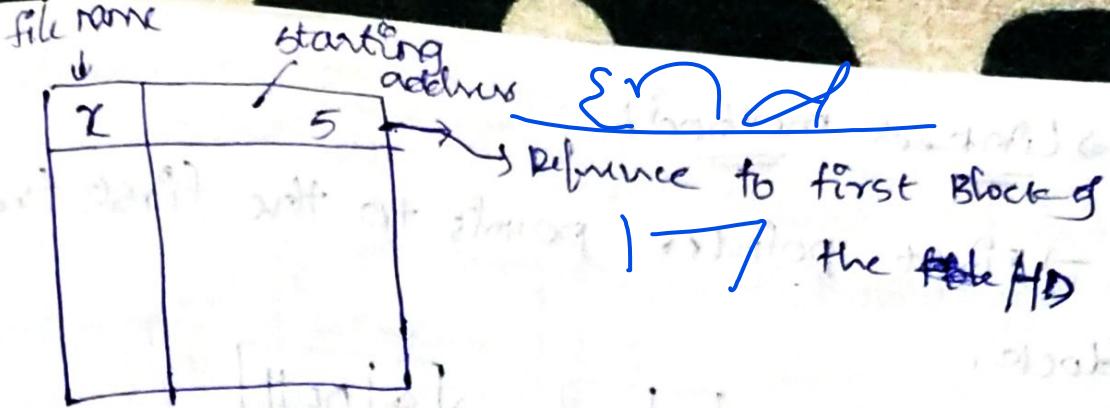
has file name

has x.

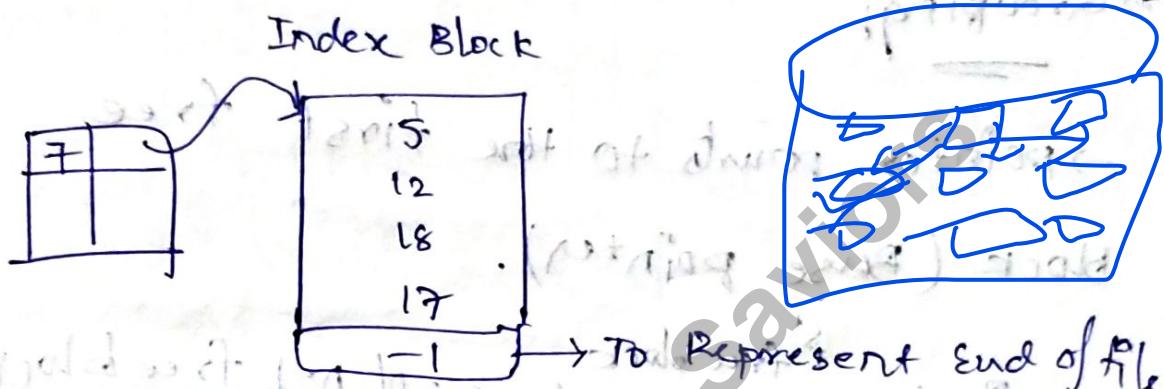
linked Space Allocation:-

→ The Blocks are connected by pointer.





Indexed Space allocation



Free Space Management

- Bit vector method
- Linked method
- Grouping
- Counting

Bit Vector method:

→ In this free space is managed to single

vector

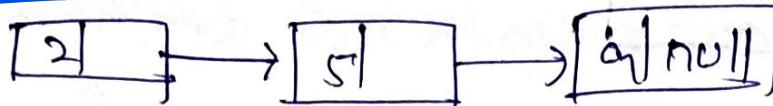
	Not Free	Free
Ex:-	0 0 1 0 0 1 0 1 1	

By this we can say the Block is

free or not.

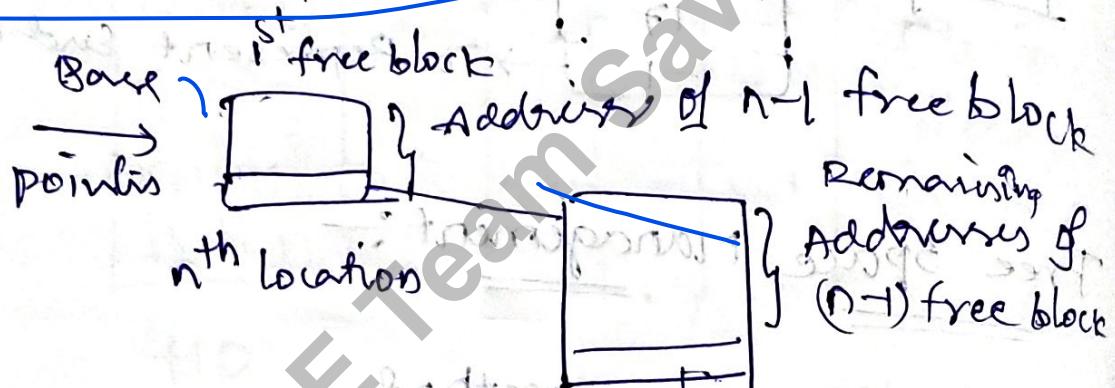
→ Linked method

→ First pointer points to the first free block.



→ Grouping

→ Pointer points to the first free block (Base pointer).



→ Counting

→ It maintains Index table.



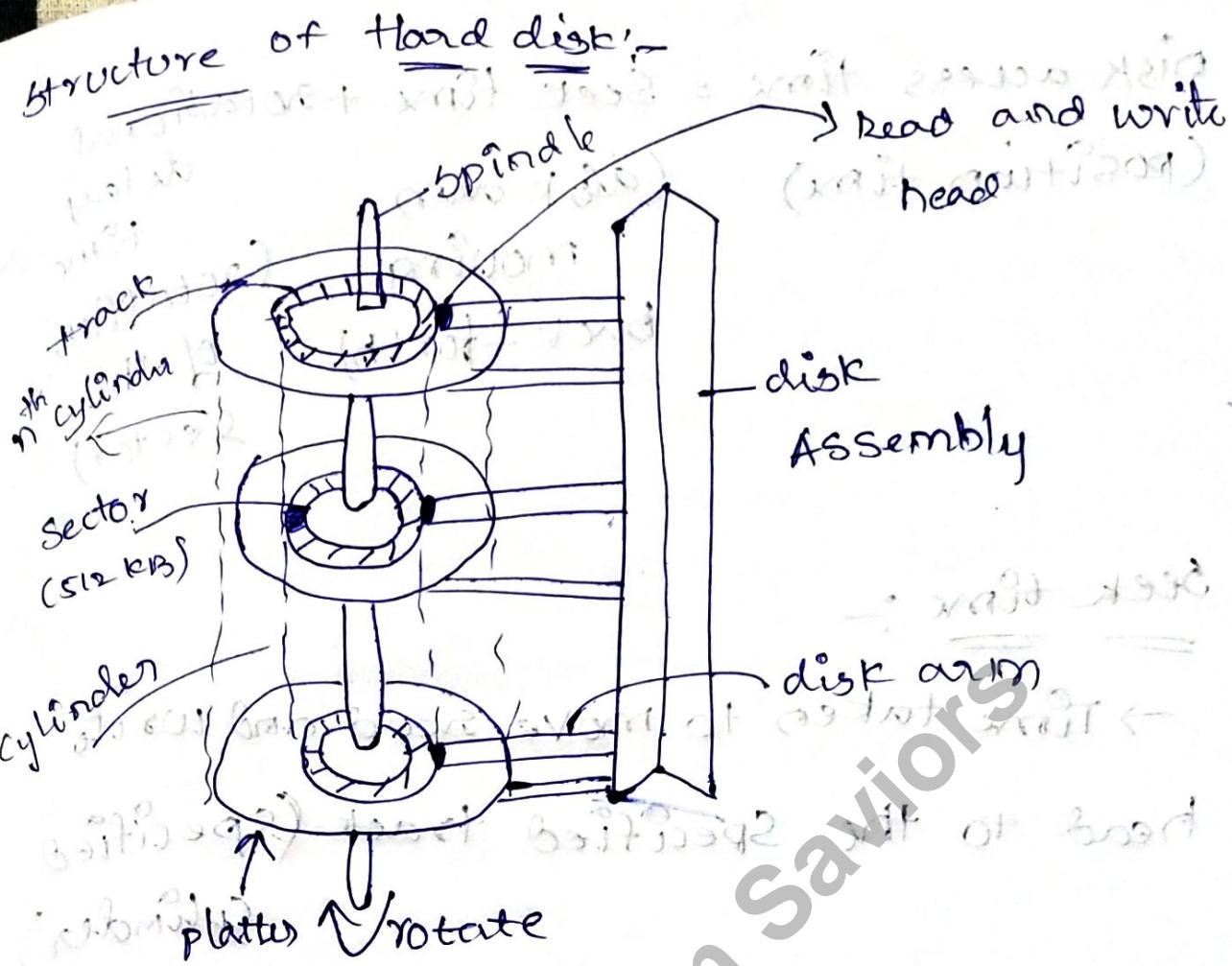
Index Table

4	3
150	60
38	5

→ Count

→ 4 follows 3

more free blocks



→ platter has magnetic coated material which has ability to store and record data.

→ platter → divided into many tracks.

→ File Reading (x) Track contains Sector.

→ data of x started from topmost track.

→ All tracks are of same radius which forms a cylinder.

→ platter is made of n concentric cylinders.

→ When we read data, the data is written on the outermost cylinder.

Disk access time = Seek time + rotational delay
(position time) (disk arm moving back-forth) (rotation of the sector)

Seek time :-

→ Time taken to move read and write head to the specified track (specified cylinder)

Rotational delay time

→ Time taken to rotate specified sector to the read and write head.

segment must be located in []

disk access time = seek time + rotational delay + latency

latency = time taken to move read and write head to the specified track

seek time = time taken to move read and write head to the specified cylinder

rotational delay = time taken to rotate disk to the specified sector

latency = time taken to move read and write head to the specified cylinder

seek time = time taken to move read and write head to the specified cylinder