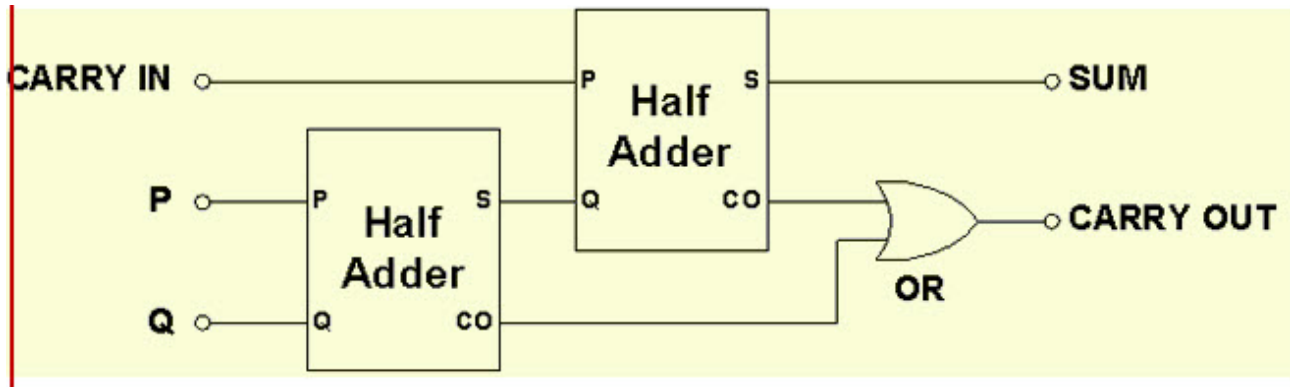# Full Adder



Implemented full adder using two half adder and an "or" gate as shown in the above figure.