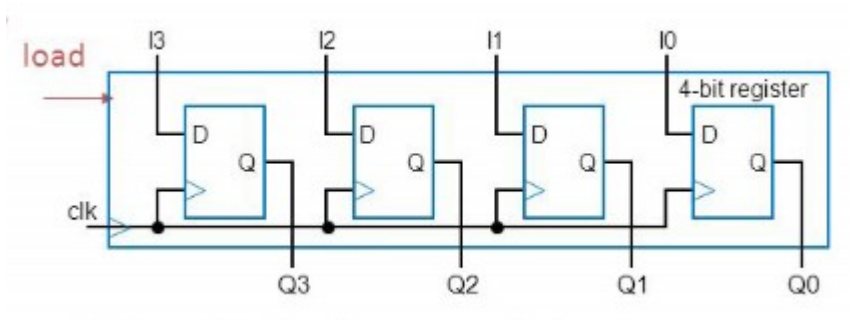


Register



Above is a figure of register with 4 D flip flops.

We implemented the register by combining 32 D flip flops one after another as shown above(with reset not shown above).